



SPEC[®] CINT2006 Result

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Hewlett-Packard Company

SPECint[®]_rate2006 = Not Run

ProLiant DL380 G7
(2.4 GHz, Intel Xeon E5620)

SPECint_rate_base2006 = 209

CPU2006 license: 3

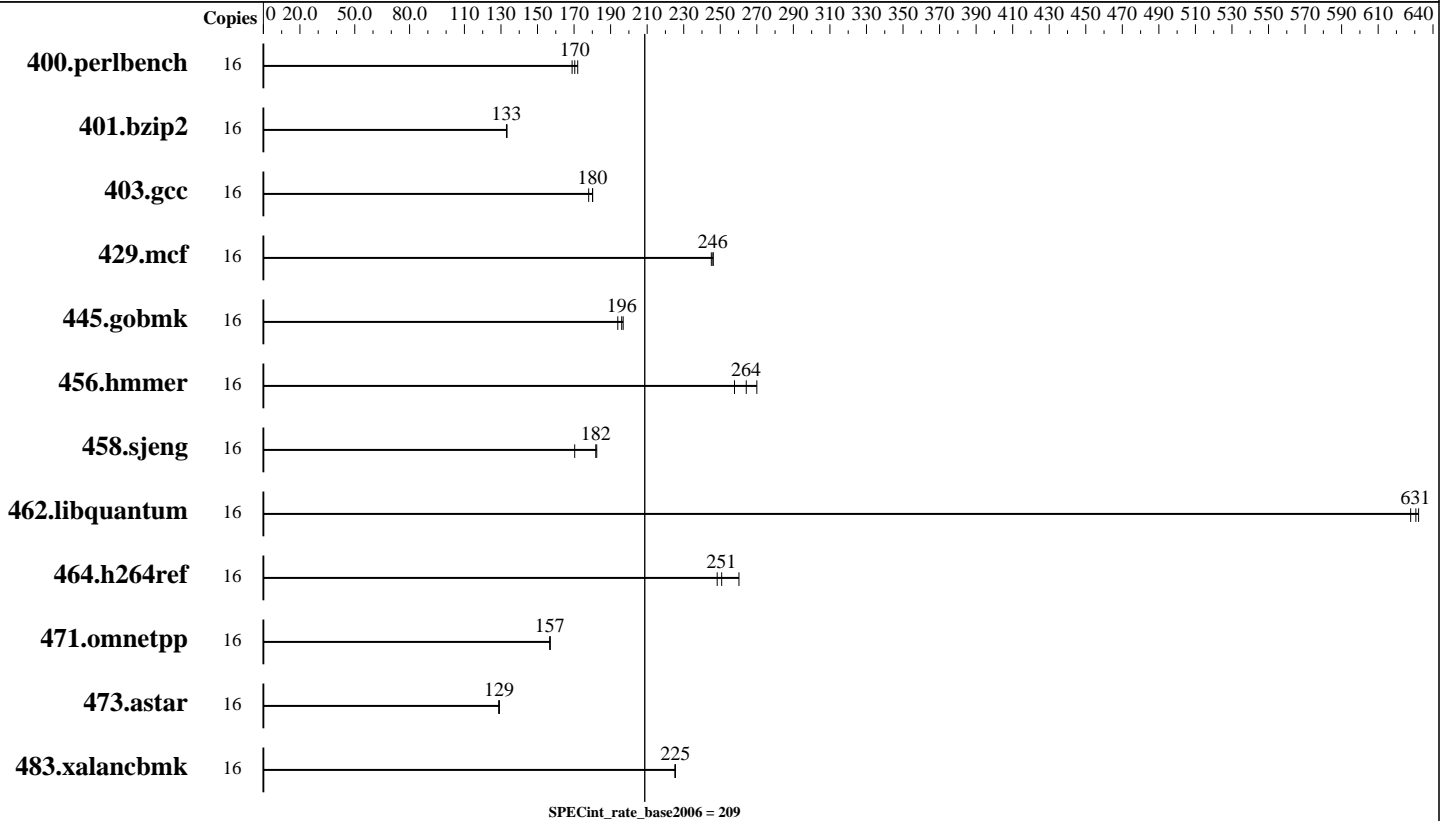
Test date: Sep-2010

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jun-2010

Tested by: Hewlett-Packard Company

Software Availability: Mar-2010



Hardware

CPU Name: Intel Xeon E5620
 CPU Characteristics: Intel Turbo Boost Technology up to 2.67 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
 Disk Subsystem: 2x146 GB 10 K SAS
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 5.5, Advanced Platform, Kernel 2.6.18-194.el5
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Microquill SmartHeap V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	917	170	925	169	909	172							
401.bzip2	16	1161	133	1160	133	1158	133							
403.gcc	16	715	180	715	180	724	178							
429.mcf	16	593	246	595	245	593	246							
445.gobmk	16	865	194	856	196	853	197							
456.hammer	16	579	258	553	270	565	264							
458.sjeng	16	1136	170	1061	182	1065	182							
462.libquantum	16	528	628	524	632	526	631							
464.h264ref	16	1361	260	1412	251	1426	248							
471.omnetpp	16	637	157	638	157	638	157							
473.astar	16	871	129	870	129	872	129							
483.xalancbmk	16	490	225	490	225	490	225							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
HP Power Profile set to Maximum Performance
Thermal Configuration set to Increased Cooling
Data Reuse set to Disabled

General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:
icc -m32

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revF.20101118.html>
<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20100525.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revF.20101118.xml>
<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20100525.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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