SPEC® CINT2006 Result

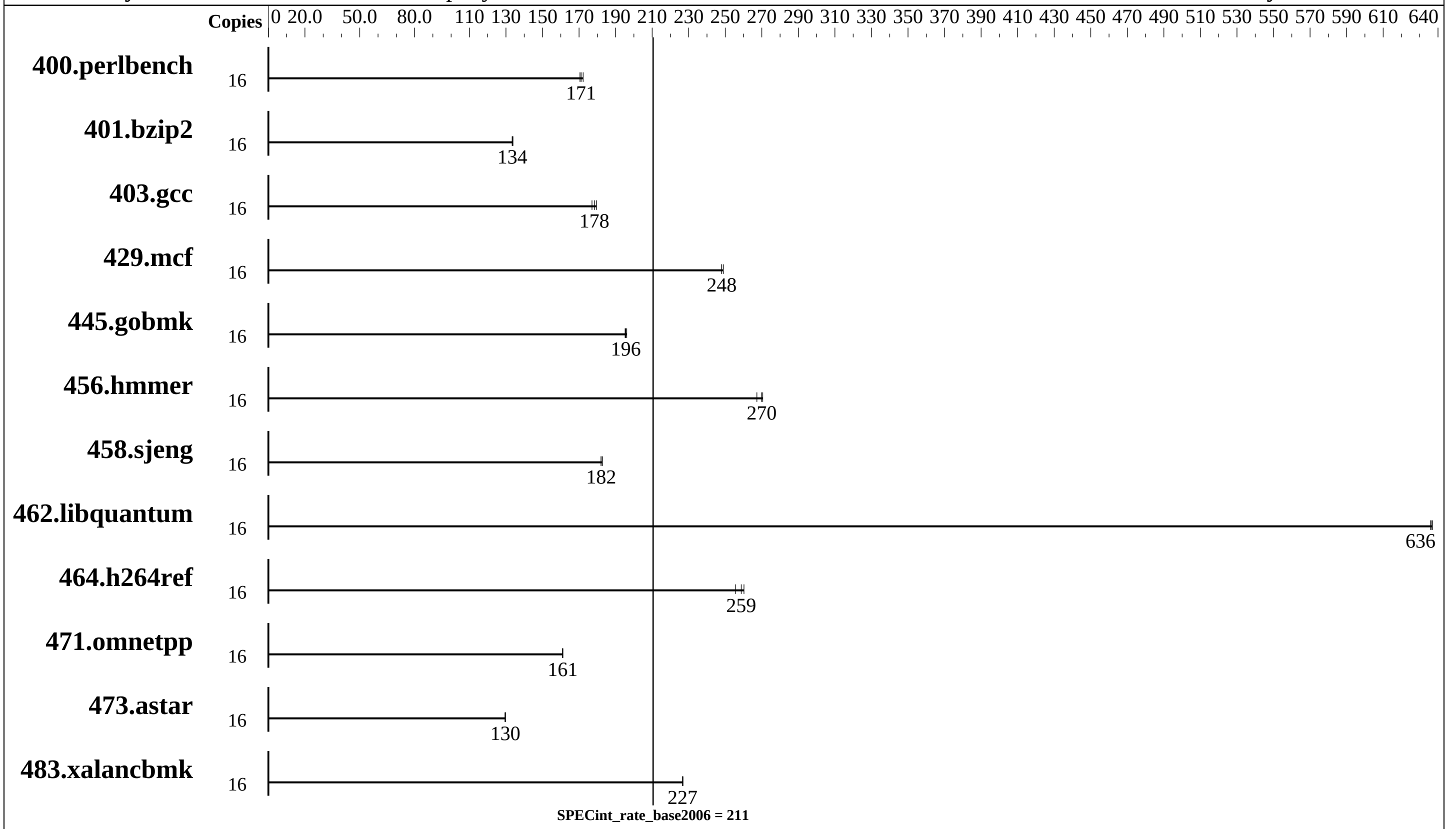
Hewlett-Packard Company

ProLiant DL360 G7
(2.4 GHz, Intel Xeon E5620)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 211

CPU2006 license: 3		**Test date:**	Sep-2010
Test sponsor:	Hewlett-Packard Company	**Hardware Availability:**	Jun-2010
Tested by:	Hewlett-Packard Company	**Software Availability:**	Mar-2010



Hardware

CPU Name:	Intel Xeon E5620
CPU Characteristics:	Intel Turbo Boost Technology up to 2.67 GHz
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem:	2x146 GB 10 K SAS
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 5.5, Advanced Platform, Kernel 2.6.18-194.el5
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Microquill SmartHeap V8.1

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	917	171	**913**	**171**	908	172							
401.bzip2	16	1154	134	1158	133	**1157**	**134**							
403.gcc	16	727	177	717	180	**722**	**178**							
429.mcf	16	**588**	**248**	586	249	588	248							
445.gobmk	16	860	195	**858**	**196**	856	196							
456.hmmer	16	552	271	**553**	**270**	558	267							
458.sjeng	16	1064	182	**1063**	**182**	1059	183							
462.libquantum	16	521	636	520	637	**521**	**636**							
464.h264ref	16	1361	260	**1369**	**259**	1385	256							
471.omnetpp	16	**621**	**161**	620	161	621	161							
473.astar	16	868	129	**866**	**130**	866	130							
483.xalancbmk	16	**487**	**227**	487	227	487	227							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
BIOS configuration:
 HP Power Profile set to Maximum Performance
 Thermal Configuration set to Increased Cooling
 Data Reuse set to Disabled
```

General Notes

```
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
```

Base Compiler Invocation

C benchmarks:
```
icc  -m32
```

Continued on next page

Base Compiler Invocation (Continued)

C++ benchmarks:
`icpc -m32`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revF.20101118.html
http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20100525.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revF.20101118.xml
http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20100525.xml