SPEC® CINT2006 Result

SGI

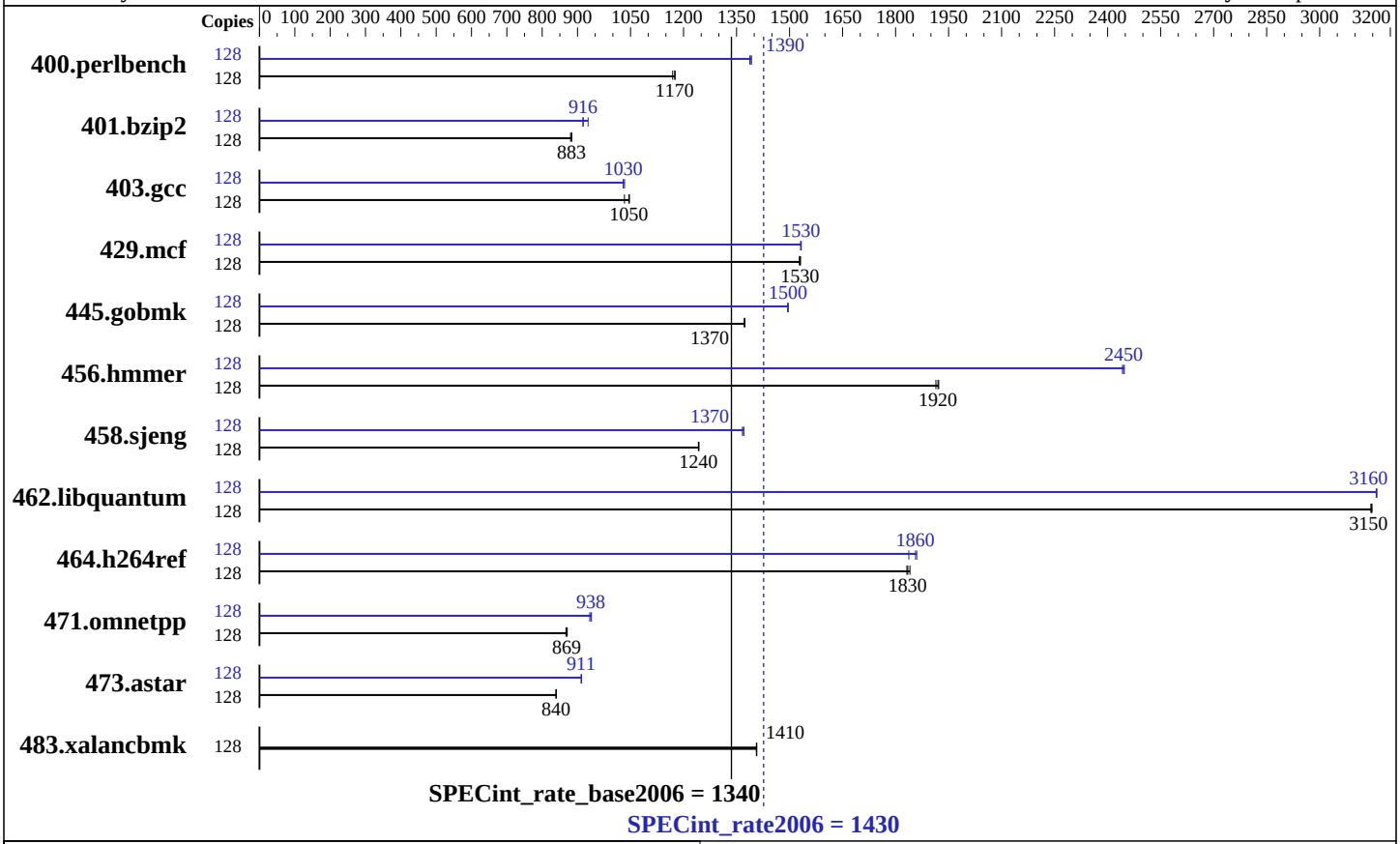
SGI Altix UV 100 (Intel Xeon X7560, 2.26 GHz)

SPECint®_rate2006 = 1430

SPECint_rate_base2006 = 1340

CPU2006 license: 4
Test sponsor: SGI
Tested by: SGI

Test date: Oct-2010
Hardware Availability: Jun-2010
Software Availability: Sep-2010



Hardware

CPU Name:	Intel Xeon X7560
CPU Characteristics:	Intel Turbo Boost Technology up to 2.67 GHz
CPU MHz:	2262
FPU:	Integrated
CPU(s) enabled:	64 cores, 8 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable:	2-96 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	24 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (64 x 4 GB 4Rx8 PC3-10600R-7, ECC)
Disk Subsystem:	None
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.19-0.3.1.1982.0.PTF-default
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel:	No
File System:	tmpfs
System State:	Run Level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	128	1070	1170	1063	1180	**1064**	**1170**	128	898	1390	901	1390	**899**	**1390**
401.bzip2	128	1398	883	**1399**	**883**	1403	880	128	1328	930	1350	915	**1348**	**916**
403.gcc	128	**986**	**1050**	998	1030	984	1050	128	997	1030	1001	1030	**1001**	**1030**
429.mcf	128	764	1530	**763**	**1530**	762	1530	128	761	1530	**762**	**1530**	762	1530
445.gobmk	128	979	1370	**978**	**1370**	977	1370	128	898	1500	898	1490	**898**	**1500**
456.hmmer	128	**622**	**1920**	624	1910	621	1920	128	488	2450	489	2440	**488**	**2450**
458.sjeng	128	**1246**	**1240**	1245	1240	1247	1240	128	1130	1370	1133	1370	**1130**	**1370**
462.libquantum	128	**843**	**3150**	843	3150	842	3150	128	**839**	**3160**	839	3160	839	3160
464.h264ref	128	**1544**	**1830**	1539	1840	1546	1830	128	1542	1840	**1526**	**1860**	1523	1860
471.omnetpp	128	919	871	**920**	**869**	922	867	128	**853**	**938**	856	935	852	939
473.astar	128	1071	839	1070	840	**1070**	**840**	128	**987**	**911**	987	910	986	912
483.xalancbmk	128	628	1410	628	1410	**628**	**1410**	128	628	1410	628	1410	**628**	**1410**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
Tmpfs filesystem set up with:
  mkdir -p /mnt/shm
  mount -t tmpfs -o size=256g,rw,mpol=interleave tmpfs /mnt/shm/
  The mpol=interleave option sets the NUMA memory allocation
  policy for all files to allocate from each node in turn.
```

Platform Notes

```
OS on 146 GB SAS disk
```

General Notes

```
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
```

Base Compiler Invocation

C benchmarks:

```
icc  -m32
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):
icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
-opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

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Peak Optimization Flags (Continued)

473.astar: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100511.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100511.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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