



# SPEC® CINT2006 Result

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## Supermicro

**SPECint®2006 = 30.4**

Motherboard X8SIT-F (Intel Core i3-550, 3.20 GHz)

**SPECint\_base2006 = 27.9**

CPU2006 license: 001176

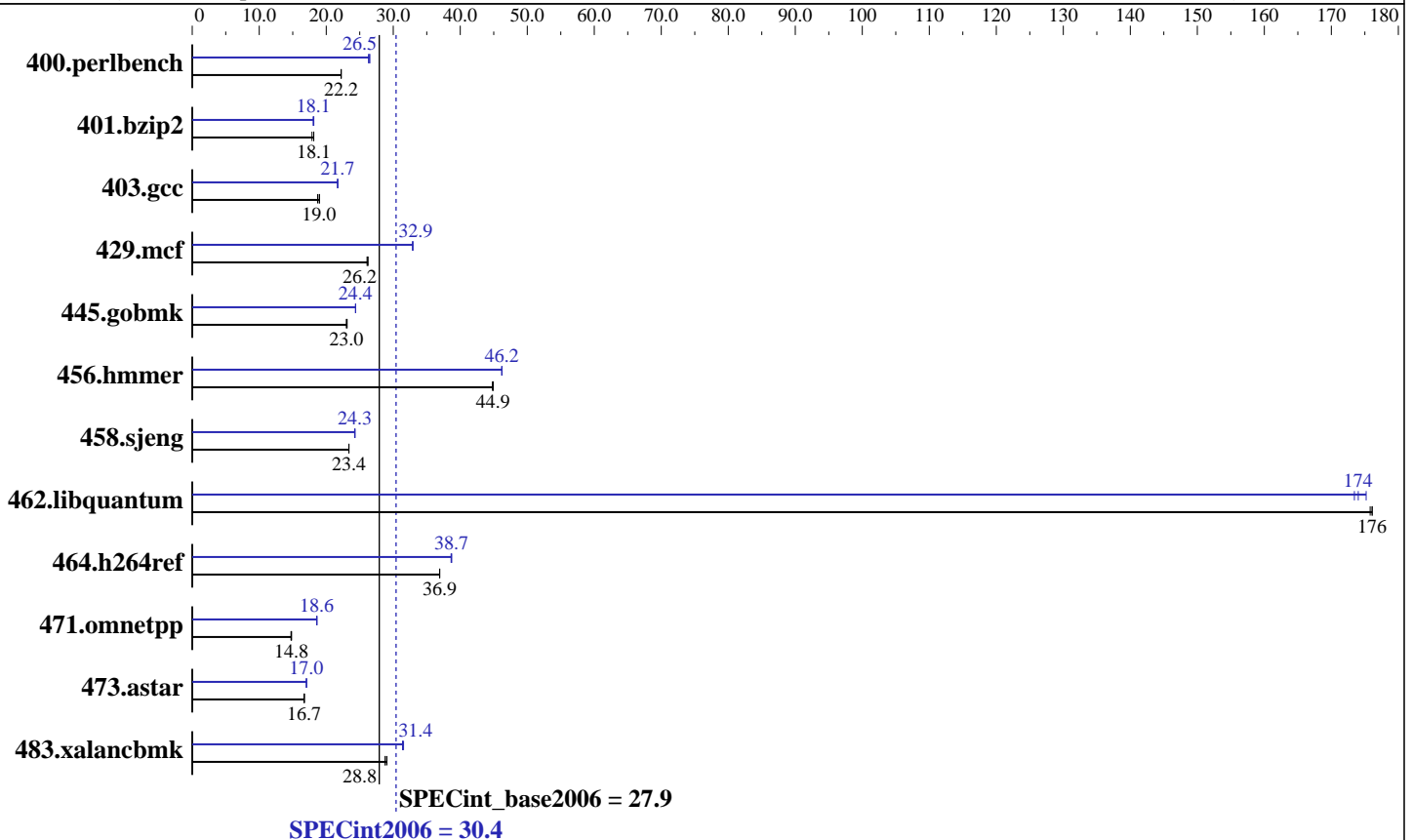
Test date: Sep-2010

Test sponsor: Supermicro

Hardware Availability: Jun-2010

Tested by: Supermicro

Software Availability: Jan-2010



### Hardware

CPU Name: Intel Core i3-550  
 CPU Characteristics:  
 CPU MHz: 3200  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 4 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 16 GB (4 x 4 GB 2Rx8 DDR3-1333 UDIMM, ECC, CL9)  
 Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64)  
 Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1  
 Build 20091130 Package ID: l\_cproc\_p\_11.1.064  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b>440</b>	<b>22.2</b>	439	22.2	440	22.2	371	26.3	369	26.5	<b>369</b>	<b>26.5</b>
401.bzip2	540	17.9	532	18.1	<b>532</b>	<b>18.1</b>	<b>532</b>	<b>18.1</b>	535	18.0	532	18.1
403.gcc	424	19.0	<b>425</b>	<b>19.0</b>	430	18.7	<b>371</b>	<b>21.7</b>	371	21.7	371	21.7
429.mcf	<b>348</b>	<b>26.2</b>	350	26.1	348	26.2	277	32.9	<b>277</b>	<b>32.9</b>	277	32.9
445.gobmk	454	23.1	<b>456</b>	<b>23.0</b>	456	23.0	<b>430</b>	<b>24.4</b>	431	24.4	430	24.4
456.hammer	208	44.8	<b>208</b>	<b>44.9</b>	208	44.9	202	46.2	<b>202</b>	<b>46.2</b>	202	46.2
458.sjeng	<b>517</b>	<b>23.4</b>	518	23.4	517	23.4	499	24.2	<b>499</b>	<b>24.3</b>	498	24.3
462.libquantum	118	176	<b>118</b>	<b>176</b>	118	176	119	173	118	175	<b>119</b>	<b>174</b>
464.h264ref	600	36.9	599	36.9	<b>599</b>	<b>36.9</b>	572	38.7	573	38.6	<b>572</b>	<b>38.7</b>
471.omnetpp	422	14.8	<b>423</b>	<b>14.8</b>	424	14.7	335	18.6	337	18.6	<b>336</b>	<b>18.6</b>
473.astar	419	16.7	420	16.7	<b>420</b>	<b>16.7</b>	<b>412</b>	<b>17.0</b>	412	17.0	412	17.1
483.xalancbmk	237	29.1	<b>240</b>	<b>28.8</b>	240	28.8	220	31.4	219	31.5	<b>220</b>	<b>31.4</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run  
OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to granularity=fine,scatter

## Platform Notes

Fan speed set to Full Speed in BIOS Setup.  
As tested, the system used a Supermicro CSE-827H-R920B chassis.  
The chassis is bundled with a PWS-920P-1R power supply, SNK-P0046P heatsink,  
and 4 FAN-00111L4 cooling fans.

## General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

## Base Compiler Invocation

C benchmarks:  
icc -m64  
  
C++ benchmarks:  
icpc -m64



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## Base Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

429.mcf: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

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## Peak Compiler Invocation (Continued)

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch  
  
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div -static(pass 2) -prof-use(pass 2)  
-auto-ilp32 -opt-prefetch -ansi-alias  
  
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc  
-opt-malloc-options=3 -auto-ilp32  
  
429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
  
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias  
  
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32  
  
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4  
  
462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel  
-opt-prefetch -par-schedule-static=32768 -ansi-alias  
  
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

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## Peak Optimization Flags (Continued)

C++ benchmarks:

```
471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
             -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
             -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
             -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
```

```
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
            -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
            -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64
```

```
483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
               -Wl,-z,muldefs
               -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
```

## Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100915.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100915.xml>

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