



SPEC® CINT2006 Result

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Fujitsu

SPECint®_rate2006 = 76.1

PRIMERGY BX922 S2, Intel Xeon E5506, 2.13 GHz

SPECint_rate_base2006 = 71.1

CPU2006 license: 19

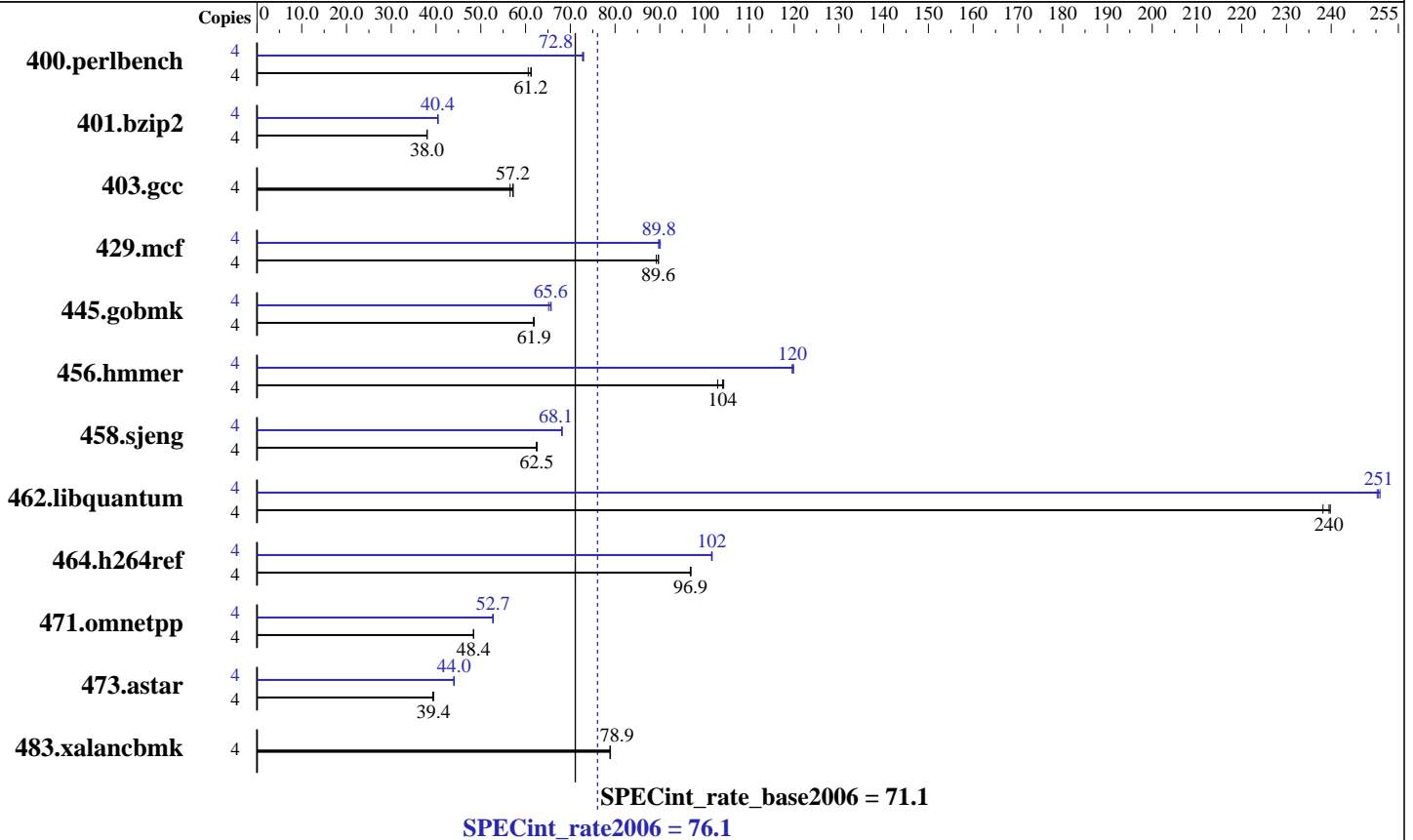
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jul-2010

Hardware Availability: Oct-2010

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon E5506
 CPU Characteristics: 2133
 CPU MHz: 2133
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 4 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (6x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)
 Disk Subsystem: 1 x SSD SATA, 64 GB
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	<u>639</u>	<u>61.2</u>	638	61.3	645	60.6	4	536	73.0	537	72.7	<u>537</u>	<u>72.8</u>
401.bzip2	4	<u>1016</u>	<u>38.0</u>	1016	38.0	1015	38.0	4	954	40.5	955	40.4	<u>955</u>	<u>40.4</u>
403.gcc	4	<u>563</u>	<u>57.2</u>	563	57.2	569	56.6	4	<u>563</u>	<u>57.2</u>	563	57.2	569	56.6
429.mcf	4	409	89.2	<u>407</u>	<u>89.6</u>	407	89.7	4	<u>406</u>	<u>89.8</u>	405	90.1	406	89.8
445.gobmk	4	<u>678</u>	<u>61.9</u>	679	61.8	678	61.9	4	<u>640</u>	<u>65.6</u>	644	65.2	638	65.7
456.hammer	4	<u>359</u>	<u>104</u>	358	104	363	103	4	312	120	311	120	<u>312</u>	<u>120</u>
458.sjeng	4	774	62.6	775	62.4	<u>775</u>	<u>62.5</u>	4	711	68.1	710	68.1	<u>710</u>	<u>68.1</u>
462.libquantum	4	348	238	346	240	<u>346</u>	<u>240</u>	4	331	250	330	251	<u>331</u>	<u>251</u>
464.h264ref	4	914	96.8	913	97.0	<u>914</u>	<u>96.9</u>	4	872	102	<u>871</u>	<u>102</u>	871	102
471.omnetpp	4	516	48.4	517	48.4	<u>517</u>	<u>48.4</u>	4	474	52.8	<u>474</u>	<u>52.7</u>	475	52.7
473.astar	4	715	39.3	<u>712</u>	<u>39.4</u>	712	39.4	4	639	44.0	637	44.1	<u>638</u>	<u>44.0</u>
483.xalancbmk	4	<u>350</u>	<u>78.9</u>	350	78.9	349	79.0	4	<u>350</u>	<u>78.9</u>	350	78.9	349	79.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

The system automatically configures the memory to run at 800 MHz.

General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.icl11.1/libic11.1-32bit -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64

Continued on next page



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Peak Portability Flags (Continued)

456.hmmcr: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: basepeak = yes

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
 -ipo -no-prec-div -ansi-alias

456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
 -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs

Continued on next page



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Peak Optimization Flags (Continued)

473.astar (continued):

`-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64`

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

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