



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu

**SPECint®2006 = 23.9**

PRIMERGY BX922 S2, Intel Xeon E5506, 2.13 GHz

**SPECint\_base2006 = 22.0**

CPU2006 license: 19

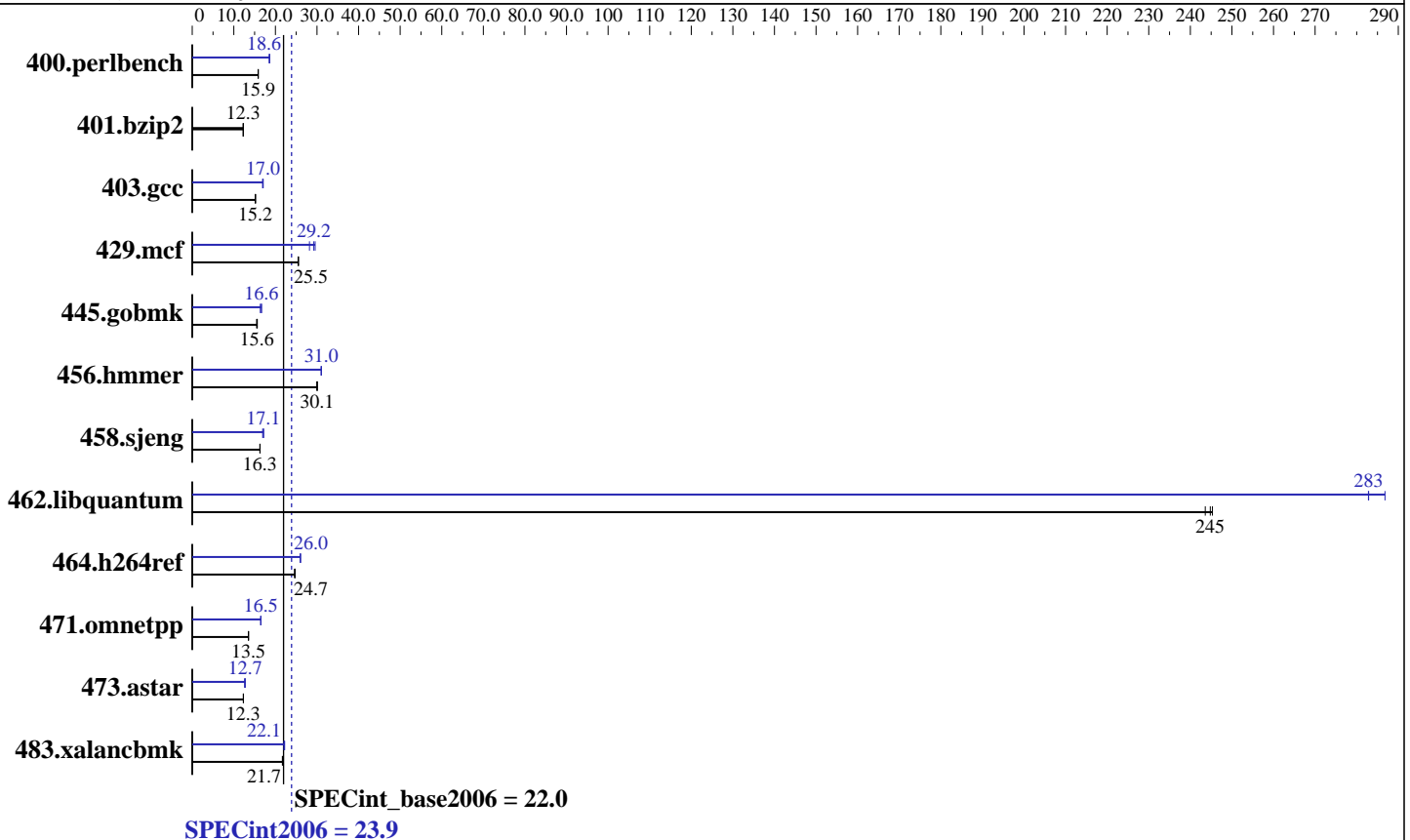
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jul-2010

Hardware Availability: Oct-2010

Software Availability: Jan-2010



### Hardware

CPU Name: Intel Xeon E5506  
 CPU Characteristics:  
 CPU MHz: 2133  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 4 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)  
 Disk Subsystem: 1 x SSD SATA, 64 GB  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l\_cproc\_p\_11.1.064  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu

SPECint2006 = **23.9**

PRIMERGY BX922 S2, Intel Xeon E5506, 2.13 GHz

SPECint\_base2006 = **22.0**

CPU2006 license: 19  
Test sponsor: Fujitsu  
Tested by: Fujitsu

Test date: Jul-2010  
Hardware Availability: Oct-2010  
Software Availability: Jan-2010

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	616	15.9	<b>616</b>	<b>15.9</b>	616	15.9	528	18.5	<b>525</b>	<b>18.6</b>	525	18.6
401.bzip2	<b>786</b>	<b>12.3</b>	785	12.3	787	12.3	<b>786</b>	<b>12.3</b>	785	12.3	787	12.3
403.gcc	529	15.2	<b>530</b>	<b>15.2</b>	530	15.2	<b>473</b>	<b>17.0</b>	476	16.9	472	17.1
429.mcf	358	25.5	357	25.5	<b>357</b>	<b>25.5</b>	323	28.2	309	29.6	<b>312</b>	<b>29.2</b>
445.gobmk	<b>673</b>	<b>15.6</b>	670	15.6	677	15.5	<b>630</b>	<b>16.6</b>	641	16.4	630	16.6
456.hammer	<b>310</b>	<b>30.1</b>	312	30.0	310	30.1	301	31.0	<b>301</b>	<b>31.0</b>	301	31.0
458.sjeng	<b>742</b>	<b>16.3</b>	741	16.3	744	16.3	705	17.2	714	16.9	<b>706</b>	<b>17.1</b>
462.libquantum	85.1	244	84.5	245	<b>84.6</b>	<b>245</b>	73.3	283	72.2	287	<b>73.2</b>	<b>283</b>
464.h264ref	<b>896</b>	<b>24.7</b>	894	24.8	900	24.6	851	26.0	<b>851</b>	<b>26.0</b>	851	26.0
471.omnetpp	<b>462</b>	<b>13.5</b>	461	13.5	463	13.5	<b>378</b>	<b>16.5</b>	380	16.5	378	16.5
473.astar	572	12.3	<b>570</b>	<b>12.3</b>	570	12.3	557	12.6	551	12.7	<b>552</b>	<b>12.7</b>
483.xalancbmk	318	21.7	317	21.8	<b>318</b>	<b>21.7</b>	<b>312</b>	<b>22.1</b>	313	22.0	310	22.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

The system automatically configures the memory to run at 800 MHz.

## General Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to granularity=fine,scatter  
For information about Fujitsu please visit: <http://www.fujitsu.com>  
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

## Base Compiler Invocation

C benchmarks:  
icc -m64  
  
C++ benchmarks:  
icpc -m64



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Fujitsu**

**SPECint2006 = 23.9**

PRIMERGY BX922 S2, Intel Xeon E5506, 2.13 GHz

**SPECint\_base2006 = 22.0**

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jul-2010

Hardware Availability: Oct-2010

Software Availability: Jan-2010

## Base Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

## Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m64
```

```
400.perlbench: icc -m32
```

```
429.mcf: icc -m32
```

```
445.gobmk: icc -m32
```

```
464.h264ref: icc -m32
```

C++ benchmarks (except as noted below):

```
icpc -m32
```

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Fujitsu**

**SPECint2006 = 23.9**

PRIMERGY BX922 S2, Intel Xeon E5506, 2.13 GHz

**SPECint\_base2006 = 22.0**

CPU2006 license: 19

Test date: Jul-2010

Test sponsor: Fujitsu

Hardware Availability: Oct-2010

Tested by: Fujitsu

Software Availability: Jan-2010

## Peak Compiler Invocation (Continued)

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
 401.bzip2: -DSPEC\_CPU\_LP64  
 403.gcc: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: basepeak = yes

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
 -opt-malloc-options=3 -auto-ilp32

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll4

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel  
 -opt-prefetch -par-schedule-static=32768 -ansi-alias

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll2 -ansi-alias

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint2006 = 23.9

PRIMERGY BX922 S2, Intel Xeon E5506, 2.13 GHz

SPECint\_base2006 = 22.0

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jul-2010

Hardware Availability: Oct-2010

Software Availability: Jan-2010

## Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Wed Jul 23 10:06:01 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 14 September 2010.