



SPEC® CFP2006 Result

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Fujitsu

SPECfp®_rate2006 = 108

PRIMERGY BX920 S2, Intel Xeon X5667, 3.06 GHz

SPECfp_rate_base2006 = 104

CPU2006 license: 19

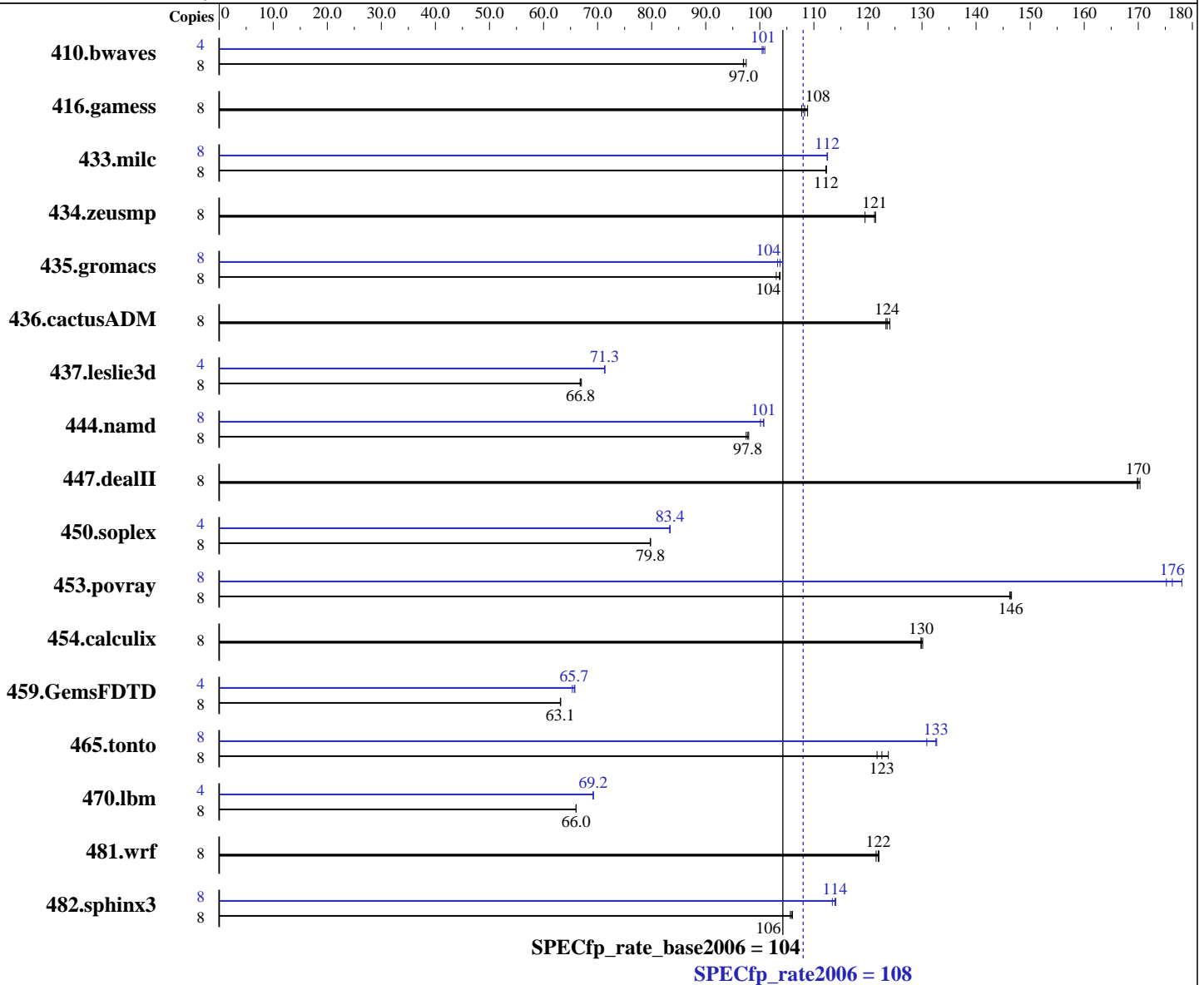
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Aug-2010

Hardware Availability: Aug-2010

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon X5667
 CPU Characteristics: Intel Turbo Boost Technology up to 3.46 GHz
 CPU MHz: 3067
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (3x8 GB PC3-10600R, 2 rank, CL9-9-9, ECC)
Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	1115	97.5	<u>1121</u>	<u>97.0</u>	1121	97.0	4	541	100	539	101	<u>540</u>	<u>101</u>
416.gamess	8	<u>1447</u>	<u>108</u>	1439	109	1454	108	8	<u>1447</u>	<u>108</u>	1439	109	1454	108
433.milc	8	654	112	<u>654</u>	<u>112</u>	654	112	8	<u>653</u>	<u>112</u>	653	113	653	112
434.zeusmp	8	609	119	600	121	<u>601</u>	<u>121</u>	8	609	119	600	121	<u>601</u>	<u>121</u>
435.gromacs	8	550	104	<u>551</u>	<u>104</u>	554	103	8	553	103	<u>551</u>	<u>104</u>	548	104
436.cactusADM	8	771	124	775	123	<u>774</u>	<u>124</u>	8	771	124	775	123	<u>774</u>	<u>124</u>
437.leslie3d	8	1122	67.0	1126	66.8	<u>1125</u>	<u>66.8</u>	4	527	71.3	527	71.3	<u>527</u>	<u>71.3</u>
444.namd	8	655	98.0	<u>656</u>	<u>97.8</u>	658	97.5	8	<u>637</u>	<u>101</u>	641	100	637	101
447.dealII	8	539	170	<u>539</u>	<u>170</u>	537	170	8	539	170	<u>539</u>	<u>170</u>	537	170
450.soplex	8	836	79.8	<u>836</u>	<u>79.8</u>	837	79.7	4	<u>400</u>	<u>83.4</u>	400	83.3	400	83.4
453.povray	8	291	146	290	147	<u>291</u>	<u>146</u>	8	<u>241</u>	<u>176</u>	243	175	239	178
454.calculix	8	508	130	507	130	<u>508</u>	<u>130</u>	8	508	130	507	130	<u>508</u>	<u>130</u>
459.GemsFDTD	8	<u>1345</u>	<u>63.1</u>	1345	63.1	1343	63.2	4	<u>646</u>	<u>65.7</u>	650	65.3	645	65.8
465.tonto	8	647	122	636	124	<u>642</u>	<u>123</u>	8	593	133	<u>594</u>	<u>133</u>	601	131
470.lbm	8	1665	66.0	1665	66.0	<u>1665</u>	<u>66.0</u>	4	794	69.2	<u>794</u>	<u>69.2</u>	794	69.2
481.wrf	8	<u>733</u>	<u>122</u>	732	122	735	122	8	<u>733</u>	<u>122</u>	732	122	735	122
482.sphinx3	8	<u>1473</u>	<u>106</u>	1470	106	1476	106	8	1367	114	<u>1369</u>	<u>114</u>	1375	113

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
Data Reuse Optimization = Disable
Performance/Power Setting = Traditional



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General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

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Base Optimization Flags (Continued)

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):
icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -opt-prefetch

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -ansi-alias -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealIII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

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Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.xml>

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For other inquiries, please contact webmaster@spec.org.

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