SPEC® CINT2006 Result

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Itautec

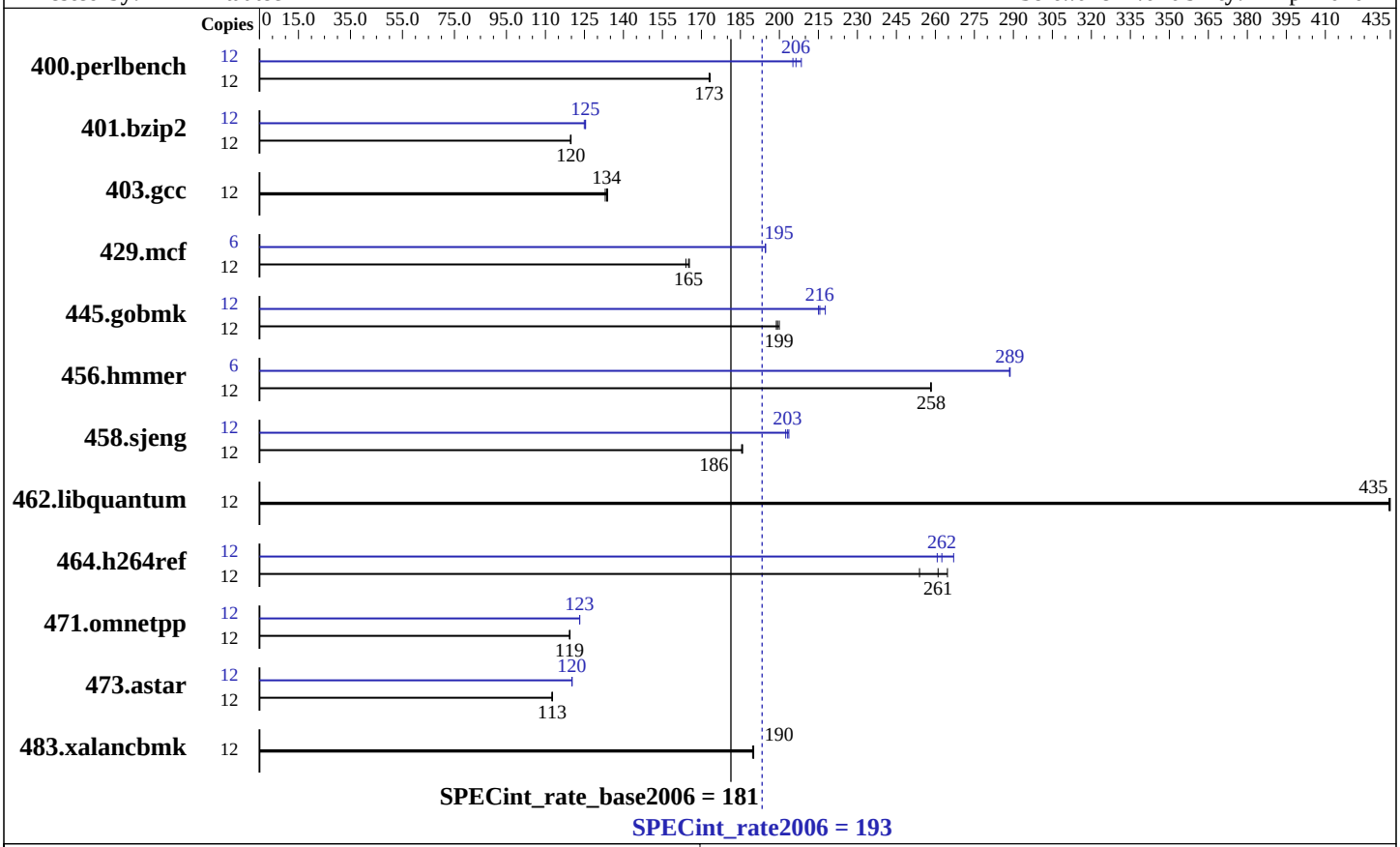
Servidor Itautec MX223 (Intel Xeon X5680)

SPECint®_rate2006 = 193

SPECint_rate_base2006 = 181

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Aug-2010
Hardware Availability: Apr-2010
Software Availability: Apr-2010



Hardware

CPU Name:	Intel Xeon X5680
CPU Characteristics:	Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz:	3333
FPU:	Integrated
CPU(s) enabled:	6 cores, 1 chip, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (6 x 4GB, DDR3-1333, Dual Rank, CL 9, ECC)
Disk Subsystem:	1 x 160 GB SATA-2, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-smp
Compiler:	Intel C++ Professional Compiler 11.1 for Linux Build 20100414 Package ID: l_cproc_p_11.1.072
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	12	678	173	676	173	**678**	**173**	12	562	208	**568**	**206**	571	205
401.bzip2	12	**967**	**120**	966	120	968	120	12	927	125	923	125	**924**	**125**
403.gcc	12	**723**	**134**	721	134	726	133	12	**723**	**134**	721	134	726	133
429.mcf	12	662	165	667	164	**663**	**165**	6	281	195	281	195	**281**	**195**
445.gobmk	12	630	200	**632**	**199**	633	199	12	578	218	585	215	**584**	**216**
456.hmmer	12	**433**	**258**	434	258	433	258	6	194	288	194	289	**194**	**289**
458.sjeng	12	783	185	781	186	**782**	**186**	12	713	204	717	202	**715**	**203**
462.libquantum	12	**572**	**435**	572	435	572	435	12	**572**	**435**	572	435	572	435
464.h264ref	12	**1017**	**261**	1003	265	1046	254	12	995	267	1019	261	**1012**	**262**
471.omnetpp	12	628	119	**629**	**119**	629	119	12	609	123	**609**	**123**	609	123
473.astar	12	**748**	**113**	747	113	749	112	12	701	120	**701**	**120**	701	120
483.xalancbmk	12	**436**	**190**	437	190	436	190	12	**436**	**190**	437	190	436	190

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
```

General Notes

```
This result was measured on the Servidor Itautec MX223.
The Servidor Itautec MX203 and the Servidor Itautec MX223 are electronically equivalent.
```

Base Compiler Invocation

C benchmarks:

```
icc  -m32
```

C++ benchmarks:

```
icpc -m32
```

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/richard/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks (except as noted below):
icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64

Continued on next page

Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: basepeak = yes

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/richard/sh/SmartHeap_8.1/lib -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/richard/sh/SmartHeap_8/lib -lsmartheap64

Continued on next page

Peak Optimization Flags (Continued)

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.1-linux64-revE.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.1-linux64-revE.xml`

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