



SPEC[®] CFP2006 Result

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Tyan

(Test Sponsor: Advanced Micro Devices)

SPECfp[®]_rate2006 = 132

Tyan YR190B8228, AMD Opteron 4162 EE

SPECfp_rate_base2006 = 123

CPU2006 license: 49

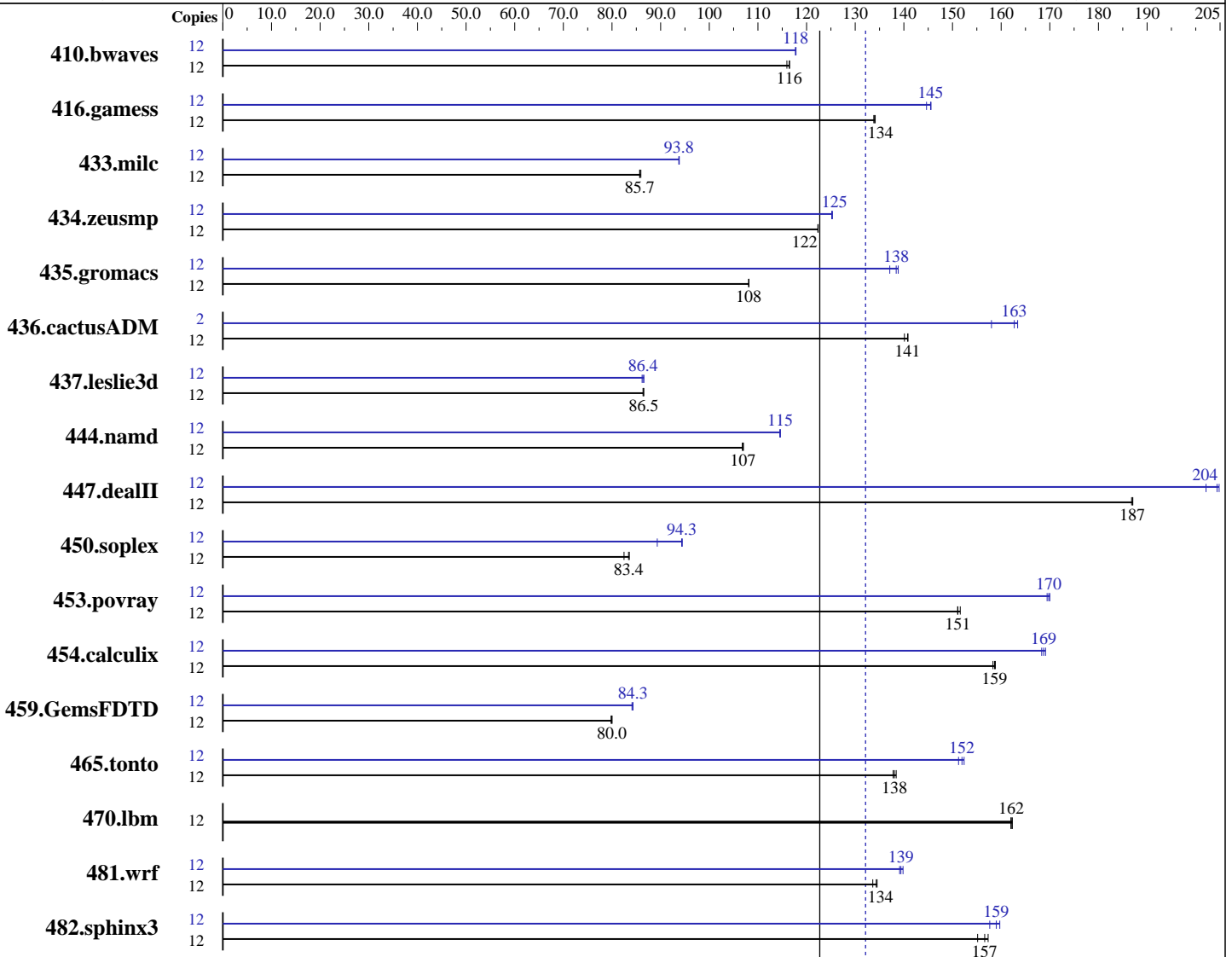
Test sponsor: Advanced Micro Devices

Tested by: Advanced Micro Devices

Test date: Aug-2010

Hardware Availability: Aug-2010

Software Availability: Jul-2010



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Hardware

CPU Name: AMD Opteron 4162 EE
 CPU Characteristics:
 CPU MHz: 1700
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 64 KB I + 64 KB D on chip per core
 Secondary Cache: 512 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
 Compiler: x86 Open64 4.2.4 Compiler Suite (from AMD)
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (Full multiuser with network)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

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L3 Cache: 6 MB I+D on chip per chip
 Other Cache: None
 Memory: 32 GB (4 x 8 GB, DDR3-1333, CL9, Reg, Dual Rank)
 Disk Subsystem: 1 x 128 GB SATA SSD,
 Western Digital SiliconEdge Blue SSC-D0128SC-2100
 Other Hardware: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	12	1406	116	1401	116	1400	117	12	1385	118	1385	118	1384	118
416.gamess	12	1752	134	1756	134	1754	134	12	1615	145	1624	145	1614	146
433.milc	12	1282	85.9	1286	85.7	1285	85.7	12	1175	93.7	1175	93.8	1174	93.8
434.zeusmp	12	893	122	893	122	892	122	12	871	125	872	125	872	125
435.gromacs	12	793	108	792	108	792	108	12	625	137	617	139	619	138
436.cactusADM	12	1018	141	1019	141	1023	140	2	151	158	146	163	147	163
437.leslie3d	12	1304	86.5	1304	86.5	1306	86.4	12	1303	86.6	1309	86.2	1306	86.4
444.namd	12	901	107	899	107	900	107	12	840	115	841	114	840	115
447.dealII	12	734	187	735	187	734	187	12	679	202	672	204	670	205
450.soplex	12	1214	82.5	1200	83.4	1198	83.6	12	1121	89.3	1061	94.3	1060	94.5
453.povray	12	422	151	423	151	421	152	12	377	169	376	170	376	170
454.calculix	12	623	159	624	159	625	158	12	585	169	587	169	588	168
459.GemsFDTD	12	1595	79.8	1592	80.0	1591	80.0	12	1514	84.1	1510	84.3	1511	84.3
465.tonto	12	853	138	857	138	855	138	12	781	151	777	152	775	152
470.lbm	12	1018	162	1016	162	1017	162	12	1018	162	1016	162	1017	162
481.wrf	12	997	134	1003	134	998	134	12	959	140	963	139	962	139
482.sphinx3	12	1507	155	1493	157	1487	157	12	1484	158	1464	160	1471	159

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
 'numactl' was used to bind copies to the cores.
 See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
 'ulimit -l 2097152' was used to set environment locked pages in memory limit

Set vm/nr_hugepages=5400 in /etc/sysctl.conf
 mount -t hugetlbfs nodev /mnt/hugepages



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General Notes

Environment variables set by runspec before the start of the run:

HUGETLB_LIMIT = "450"

LD_LIBRARY_PATH = "/root/work/cpu2006/amd1002li-rate-libs-revC/64:/root/work/cpu2006/amd1002li-rate-libs-revC/32"

OMP_NUM_THREADS = "6"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at

<http://developer.amd.com/cpu/open64>

Binaries were compiled on SLES10 SP2 with binutils 2.18

Base Compiler Invocation

C benchmarks:

opencc

C++ benchmarks:

openCC

Fortran benchmarks:

openf95

Benchmarks using both Fortran and C:

opencc openf95

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64
 436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG
 -fno-second-underscore
 482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

`-march=barcelona -mso -Ofast -OPT:malloc_alg=1 -HP:bdt=2m`

C++ benchmarks:

`-march=barcelona -mso -Ofast -static -INLINE:aggressive=on
-OPT:malloc_alg=1 -HP:bdt=2m`

Fortran benchmarks:

`-march=barcelona -mso -Ofast -HP`

Benchmarks using both Fortran and C:

`-march=barcelona -mso -Ofast -OPT:malloc_alg=1 -HP:bdt=2m -HP`

Peak Compiler Invocation

C benchmarks:

`opencc`

C++ benchmarks:

`openCC`

Fortran benchmarks:

`openf95`

Benchmarks using both Fortran and C:

`opencc openf95`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64`
436.cactusADM: `-DSPEC_CPU_LP64 -fno-second-underscore`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG
-fno-second-underscore`
482.sphinx3: `-DSPEC_CPU_LP64`



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Peak Optimization Flags

C benchmarks:

433.milc: -march=barcelona -mso -Ofast -CG:movnti=1
-CG:local_sched_alg=1 -CG:locs_shallow_depth=1
-HP:bdt=2m:heap=2m -LNO:prefetch=3

470.lbm: basepeak = yes

482.sphinx3: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -OPT:malloc_alg=2
-CG:sse_cse_regs=0 -CG:locs_shallow_depth=1 -CG:cmp_peep=on
-CG:local_sched_alg=1 -INLINE:aggressive=on

C++ benchmarks:

444.namd: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:ignore_feedback=off
-CG:local_sched_alg=2 -CG:load_exe=0 -CG:compute_to=on
-OPT:unroll_size=256 -fno-exceptions -HP:bdt=2m:heap=2m

447.deallI: -march=barcelona -mso -Ofast -static -INLINE:aggressive=on
-LNO:opt=0 -fno-emit-exceptions -m32
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -HP:bdt=2m:heap=2m -GRA:unspill=on
-CG:cmp_peep=on -TENV:frame_pointer=off

450.soplex: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -INLINE:aggressive=on
-OPT:IEEE_arith=3 -OPT:IEEE_NaN_Inf=off
-OPT:fold_unsigned_relops=on -OPT:malloc_alg=1
-CG:load_exe=0 -fno-exceptions -m32 -HP:bdt=2m

453.povray: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -INLINE:aggressive=on

Fortran benchmarks:

410.bwaves: -march=barcelona -mso -O3 -OPT:Ofast -OPT:treeheight=on
-LNO:blocking=off -LNO:prefetch_ahead=5
-LNO:ignore_feedback=off -WOPT:aggstr=0 -HP:bdt=2m:heap=2m
-CG:cmp_peep=on

416.gamess: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -LNO:fu=6 -LNO:blocking=0
-LNO:prefetch=0 -OPT:Ofast -OPT:ro=3 -OPT:unroll_size=256
-HP:bdt=2m:heap=2m

434.zeusmp: -march=barcelona -mso -Ofast -LNO:blocking=off
-LNO:interchange=off -OPT:treeheight=on -OPT:unroll_size=256
-CG:cmp_peep=on -GRA:prioritize_by_density=on -HP

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Peak Optimization Flags (Continued)

437.leslie3d: -march=barcelona -mso -Ofast -HP:bdt=2m:heap=2m

459.GemsFDTD: -march=barcelona -mso -Ofast -LNO:fission=2
-LNO:prefetch_ahead=1 -CG:load_exe=0 -CG:local_sched_alg=1
-HP

465.tonto: -march=barcelona -mso -Ofast
-OPT:alias=no_f90_pointer_alias -LNO:blocking=off
-CG:load_exe=1 -IPA:plimit=525 -HP

Benchmarks using both Fortran and C:

435.gromacs: -march=barcelona -mso -Ofast -OPT:rsqrt=2
-HP:bdt=2m:heap=2m

436.cactusADM: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -apo -LNO:prefetch_ahead=1
-HP:bdt=2m:heap=2m -LANG:heap_allocation_threshold=100

454.calculix: -march=barcelona -mso -Ofast -CG:load_exe=0
-CG:ptr_load_use=0 -CG:local_sched_alg=2 -CG:compute_to=on
-LNO:prefetch_ahead=30 -WOPT:unroll=2
-GRA:optimize_boundary=on -HP:bdt=2m:heap=2m

481.wrf: -march=barcelona -mso -Ofast -LNO:blocking=off
-LNO:prefetch_ahead=10 -LANG:copyinout=off
-IPA:callee_limit=5000 -GRA:prioritize_by_density=on -m3dnow
-HP

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.html>

<http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.xml>

<http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.xml>

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For other inquiries, please contact webmaster@spec.org.

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