



SPEC[®] CINT2006 Result

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Itautec

SPECint[®]_rate2006 = 375

Servidor Itautec MX223+ (Intel Xeon X5680)

SPECint_rate_base2006 = 350

CPU2006 license: 9001

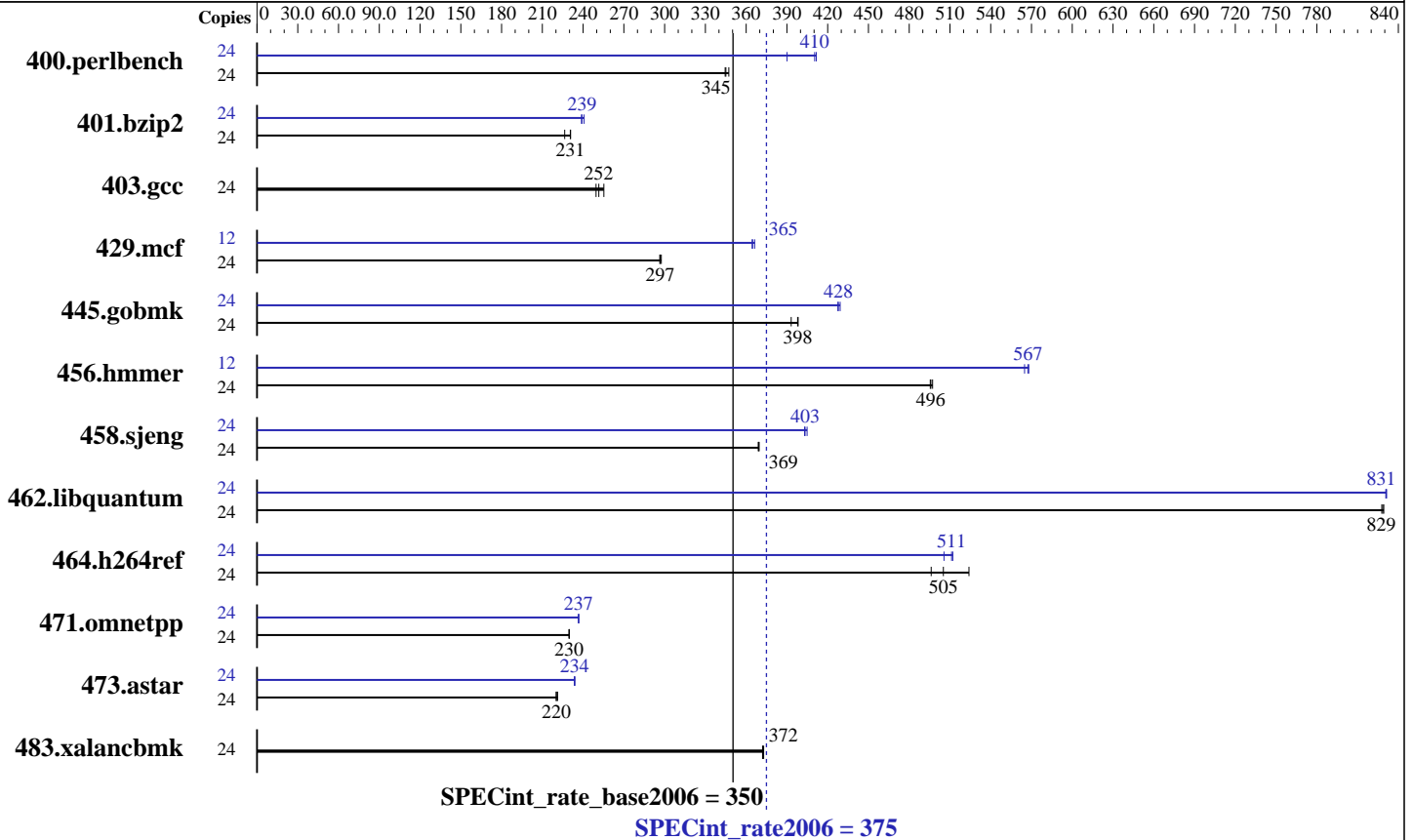
Test date: Aug-2010

Test sponsor: Itautec

Hardware Availability: Apr-2010

Tested by: Itautec

Software Availability: Apr-2010



Hardware

CPU Name: Intel Xeon X5680
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
 CPU MHz: 3333
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12 x 4GB, DDR3-1333, Dual Rank, CL 9, ECC)
 Disk Subsystem: 1 x 160 GB SATA-2, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-smp
 Compiler: Intel C++ Professional Compiler 11.1 for Linux Build 20100414 Package ID: l_cproc_p_11.1.072
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|----------------|--------|------------|------------|------------|------------|-------------|------------|--------|------------|------------|------------|------------|-------------|------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 24 | 681 | 345 | 680 | 345 | 675 | 347 | 24 | 601 | 390 | 570 | 412 | 571 | 410 |
| 401.bzip2 | 24 | 1023 | 226 | 1003 | 231 | 1004 | 231 | 24 | 970 | 239 | 968 | 239 | 962 | 241 |
| 403.gcc | 24 | 757 | 255 | 768 | 252 | 774 | 250 | 24 | 757 | 255 | 768 | 252 | 774 | 250 |
| 429.mcf | 24 | 736 | 298 | 738 | 296 | 738 | 297 | 12 | 300 | 364 | 300 | 365 | 299 | 366 |
| 445.gobmk | 24 | 633 | 398 | 640 | 393 | 632 | 398 | 24 | 587 | 429 | 588 | 428 | 589 | 428 |
| 456.hammer | 24 | 450 | 497 | 452 | 495 | 451 | 496 | 12 | 197 | 567 | 198 | 565 | 197 | 568 |
| 458.sjeng | 24 | 787 | 369 | 786 | 369 | 786 | 369 | 24 | 717 | 405 | 721 | 403 | 720 | 403 |
| 462.libquantum | 24 | 600 | 829 | 600 | 829 | 601 | 828 | 24 | 598 | 831 | 598 | 831 | 599 | 831 |
| 464.h264ref | 24 | 1014 | 524 | 1070 | 496 | 1051 | 505 | 24 | 1050 | 506 | 1037 | 512 | 1039 | 511 |
| 471.omnetpp | 24 | 653 | 230 | 653 | 230 | 653 | 230 | 24 | 634 | 237 | 634 | 237 | 634 | 237 |
| 473.astar | 24 | 762 | 221 | 764 | 220 | 765 | 220 | 24 | 720 | 234 | 721 | 234 | 721 | 234 |
| 483.xalancbmk | 24 | 445 | 372 | 445 | 372 | 444 | 373 | 24 | 445 | 372 | 445 | 372 | 444 | 373 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.

General Notes

This result was measured on the Servidor Itaotec MX223+.
The Servidor Itaotec MX203+, the Servidor Itaotec MX224 and the Servidor Itaotec MX223+ are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/richard/sh/SmartHeap_8.1/lib -lsmarheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):
icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

456.hmmcr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32
403.gcc: basepeak = yes
429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias
456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32
462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
-opt-prefetch
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/home/richard/sh/SmartHeap_8.1/lib -lsmarheap
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs

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Peak Optimization Flags (Continued)

473.astar (continued):
-L/home/richard/sh/SmartHeap_8/lib -lsmartheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.1-linux64-revE.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.1-linux64-revE.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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