



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint®2006 = 39.6

PRIMERGY BX924 S2, Intel Xeon X5670, 2.93 GHz

SPECint_base2006 = 36.7

CPU2006 license: 19

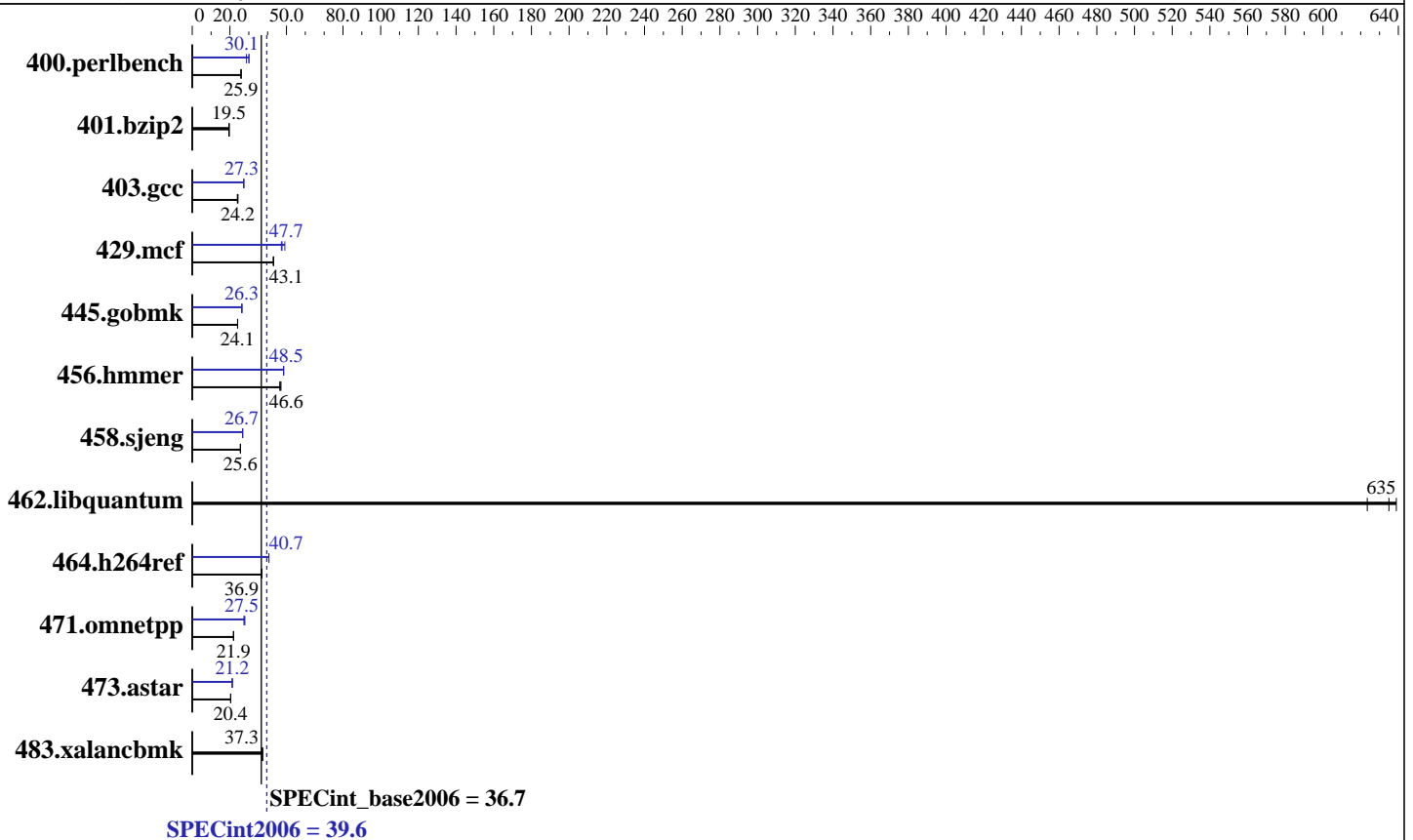
Test date: Jul-2010

Test sponsor: Fujitsu

Hardware Availability: Jun-2010

Tested by: Fujitsu

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon X5670
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC)
 Disk Subsystem: 1 x SATA, 160 GB, 5400 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1
 Build 20091130 Package ID: l_cproc_p_11.1.064
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint2006 = 39.6

PRIMERGY BX924 S2, Intel Xeon X5670, 2.93 GHz

SPECint_base2006 = 36.7

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Jul-2010
Hardware Availability: Jun-2010
Software Availability: Jan-2010

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<u>377</u>	<u>25.9</u>	377	25.9	377	25.9	<u>325</u>	<u>30.1</u>	324	30.1	340	28.7
401.bzip2	494	19.5	<u>494</u>	<u>19.5</u>	493	19.6	494	19.5	<u>494</u>	<u>19.5</u>	493	19.6
403.gcc	332	24.2	336	23.9	<u>333</u>	<u>24.2</u>	<u>295</u>	<u>27.3</u>	294	27.4	295	27.3
429.mcf	212	43.0	211	43.2	<u>212</u>	<u>43.1</u>	193	47.3	<u>191</u>	<u>47.7</u>	186	49.2
445.gobmk	<u>436</u>	<u>24.1</u>	435	24.1	436	24.1	<u>398</u>	<u>26.3</u>	398	26.3	398	26.3
456.hammer	198	47.1	<u>200</u>	<u>46.6</u>	201	46.5	<u>192</u>	<u>48.5</u>	192	48.5	193	48.4
458.sjeng	<u>473</u>	<u>25.6</u>	473	25.6	473	25.6	<u>453</u>	<u>26.7</u>	453	26.7	452	26.8
462.libquantum	32.4	639	33.2	623	<u>32.6</u>	<u>635</u>	32.4	639	33.2	623	<u>32.6</u>	<u>635</u>
464.h264ref	<u>599</u>	<u>36.9</u>	598	37.0	600	36.9	544	40.7	<u>544</u>	<u>40.7</u>	544	40.7
471.omnetpp	285	21.9	285	21.9	<u>285</u>	<u>21.9</u>	227	27.5	224	27.9	<u>227</u>	<u>27.5</u>
473.astar	343	20.4	<u>344</u>	<u>20.4</u>	345	20.4	330	21.3	332	21.2	<u>331</u>	<u>21.2</u>
483.xalancbmk	185	37.4	186	37.1	<u>185</u>	<u>37.3</u>	185	37.4	186	37.1	<u>185</u>	<u>37.3</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
Data Reuse Optimization = Disable
Intel HT Technology = Disable
Performance/Power Setting = Traditional

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint2006 = 39.6

PRIMERGY BX924 S2, Intel Xeon X5670, 2.93 GHz

SPECint_base2006 = 36.7

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Jul-2010
Hardware Availability: Jun-2010
Software Availability: Jan-2010

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

400.perlbench: icc -m32

429.mcf: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):
icpc -m64

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint2006 = 39.6

PRIMERGY BX924 S2, Intel Xeon X5670, 2.93 GHz

SPECint_base2006 = 36.7

CPU2006 license: 19

Test date: Jul-2010

Test sponsor: Fujitsu

Hardware Availability: Jun-2010

Tested by: Fujitsu

Software Availability: Jan-2010

Peak Compiler Invocation (Continued)

471.omnetpp: icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
 401.bzip2: -DSPEC_CPU_LP64
 403.gcc: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: basepeak = yes

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
 -opt-malloc-options=3 -auto-ilp32

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint2006 = 39.6

PRIMERGY BX924 S2, Intel Xeon X5670, 2.93 GHz

SPECint_base2006 = 36.7

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jul-2010

Hardware Availability: Jun-2010

Software Availability: Jan-2010

Peak Optimization Flags (Continued)

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 12:25:11 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 31 August 2010.