



# SPEC® CFP2006 Result

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Fujitsu

PRIMERGY BX924 S2, Intel Xeon L5630, 2.13 GHz

**SPECfp®\_rate2006 = 141**

**SPECfp\_rate\_base2006 = 136**

CPU2006 license: 19

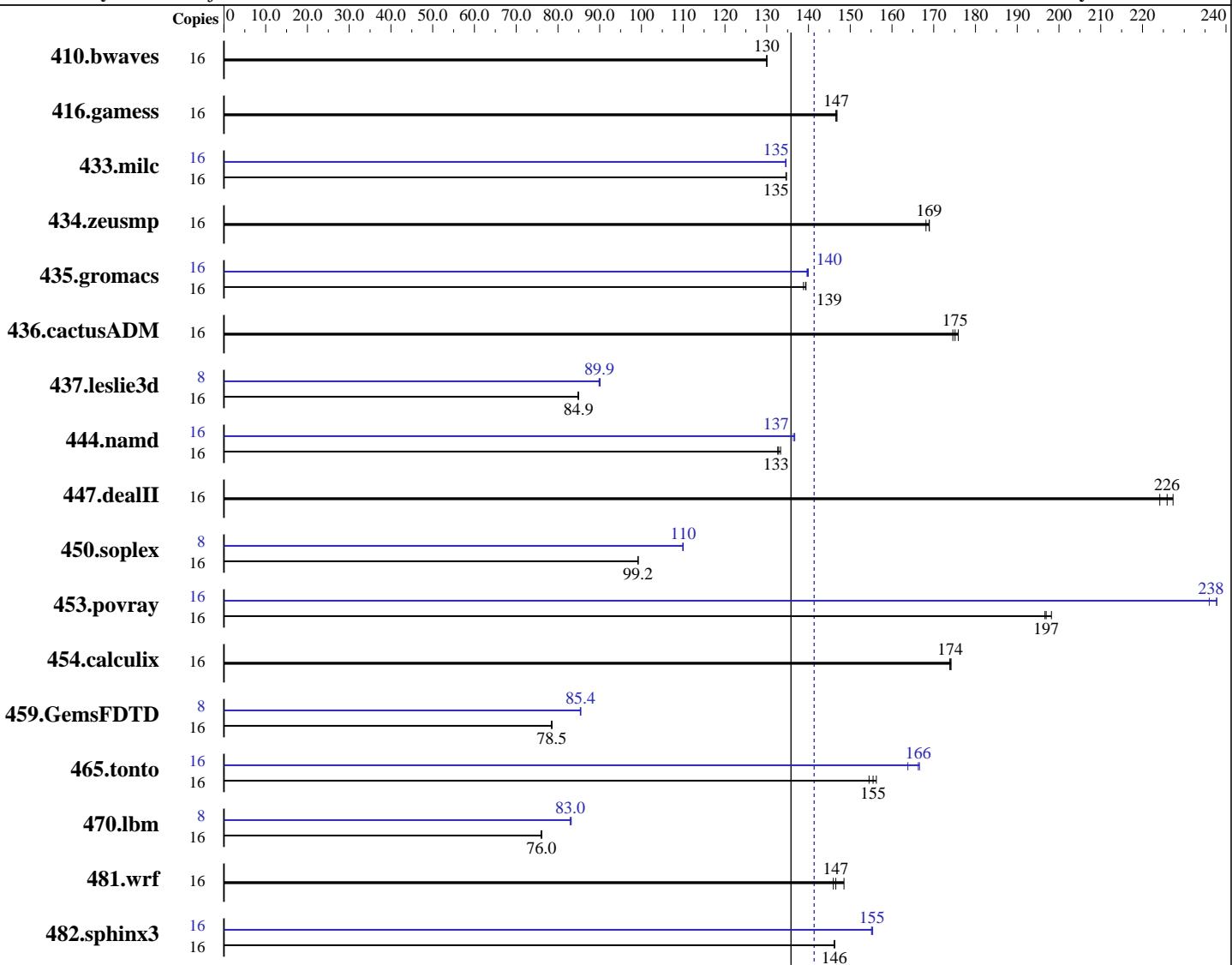
Test date: Jul-2010

Test sponsor: Fujitsu

Hardware Availability: Jun-2010

Tested by: Fujitsu

Software Availability: Jan-2010



## Hardware

CPU Name: Intel Xeon L5630  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.4 GHz  
 CPU MHz: 2133  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

## Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l\_cproc\_p\_11.1.064, l\_cprof\_p\_11.1.064  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)

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L3 Cache:	12 MB I+D on chip per chip	Base Pointers:	64-bit
Other Cache:	None	Peak Pointers:	32/64-bit
Memory:	48 GB (12x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)	Other Software:	Binutils 2.18.50.0.7.20080502
Disk Subsystem:	1 x SATA, 160 GB, 5400 RPM		
Other Hardware:	None		

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	1672	130	1673	130	<b>1673</b>	<b>130</b>	16	1672	130	1673	130	<b>1673</b>	<b>130</b>		
416.gamess	16	2133	147	<b>2136</b>	<b>147</b>	2138	147	16	2133	147	<b>2136</b>	<b>147</b>	2138	147		
433.milc	16	<b>1091</b>	<b>135</b>	1090	135	1091	135	16	1091	135	<b>1092</b>	<b>135</b>	1092	135		
434.zeusmp	16	862	169	<b>862</b>	<b>169</b>	866	168	16	862	169	<b>862</b>	<b>169</b>	866	168		
435.gromacs	16	<b>820</b>	<b>139</b>	819	139	823	139	16	816	140	818	140	<b>817</b>	<b>140</b>		
436.cactusADM	16	1095	175	1087	176	<b>1092</b>	<b>175</b>	16	1095	175	1087	176	<b>1092</b>	<b>175</b>		
437.leslie3d	16	1773	84.8	1771	84.9	<b>1771</b>	<b>84.9</b>	8	835	90.1	<b>836</b>	<b>89.9</b>	837	89.8		
444.namd	16	962	133	<b>966</b>	<b>133</b>	967	133	16	939	137	941	136	<b>939</b>	<b>137</b>		
447.dealII	16	<b>810</b>	<b>226</b>	805	227	817	224	16	<b>810</b>	<b>226</b>	805	227	817	224		
450.soplex	16	1344	99.3	<b>1346</b>	<b>99.2</b>	1346	99.1	8	<b>607</b>	<b>110</b>	607	110	607	110		
453.povray	16	<b>432</b>	<b>197</b>	429	198	433	197	16	<b>358</b>	<b>238</b>	358	238	361	236		
454.calculix	16	759	174	758	174	<b>759</b>	<b>174</b>	16	759	174	758	174	<b>759</b>	<b>174</b>		
459.GemsFDTD	16	2161	78.6	<b>2162</b>	<b>78.5</b>	2164	78.4	8	<b>994</b>	<b>85.4</b>	993	85.5	994	85.4		
465.tonto	16	1008	156	<b>1013</b>	<b>155</b>	1019	155	16	<b>947</b>	<b>166</b>	945	167	961	164		
470.lbm	16	2887	76.1	<b>2891</b>	<b>76.0</b>	2894	76.0	8	1321	83.2	1325	83.0	<b>1325</b>	<b>83.0</b>		
481.wrf	16	<b>1220</b>	<b>147</b>	1203	149	1224	146	16	<b>1220</b>	<b>147</b>	1203	149	1224	146		
482.sphinx3	16	<b>2133</b>	<b>146</b>	2133	146	2131	146	16	2011	155	<b>2009</b>	<b>155</b>	2007	155		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

The system automatically configures the memory to run at 1067 MHz.  
BIOS configuration:  
Data Reuse Optimization = Disable  
Performance/Power Setting = Traditional



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## General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>  
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

## Base Compiler Invocation

C benchmarks:

    icc -m64

C++ benchmarks:

    icpc -m64

Fortran benchmarks:

    ifort -m64

Benchmarks using both Fortran and C:

    icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
    433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
    444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
    470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

    -xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

    -xSSE4.2 -ipo -O3 -no-prec-div -static

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## Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

```
433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -fno-alias -opt-prefetch
```

```
470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -opt-malloc-options=3 -ansi-alias -auto-ilp32
```

```
482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
```

C++ benchmarks:

```
444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -fno-alias -auto-ilp32
```

```
447.dealII: basepeak = yes
```

```
450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -opt-malloc-options=3
```

```
453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -unroll4 -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: basepeak = yes
```

```
416.gamess: basepeak = yes
```

```
434.zeusmp: basepeak = yes
```

```
437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static
```

```
459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
               -unroll2 -Ob0
```

```
465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -unroll4 -auto -inline-calloc -opt-malloc-options=3
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
              -opt-prefetch -auto-ilp32
```

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## Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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