



# SPEC® CFP2006 Result

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**IBM Corporation**

**SPECfp®\_rate2006 = 200**

IBM BladeCenter HS22V (Intel Xeon E5645)

**SPECfp\_rate\_base2006 = 193**

CPU2006 license: 11

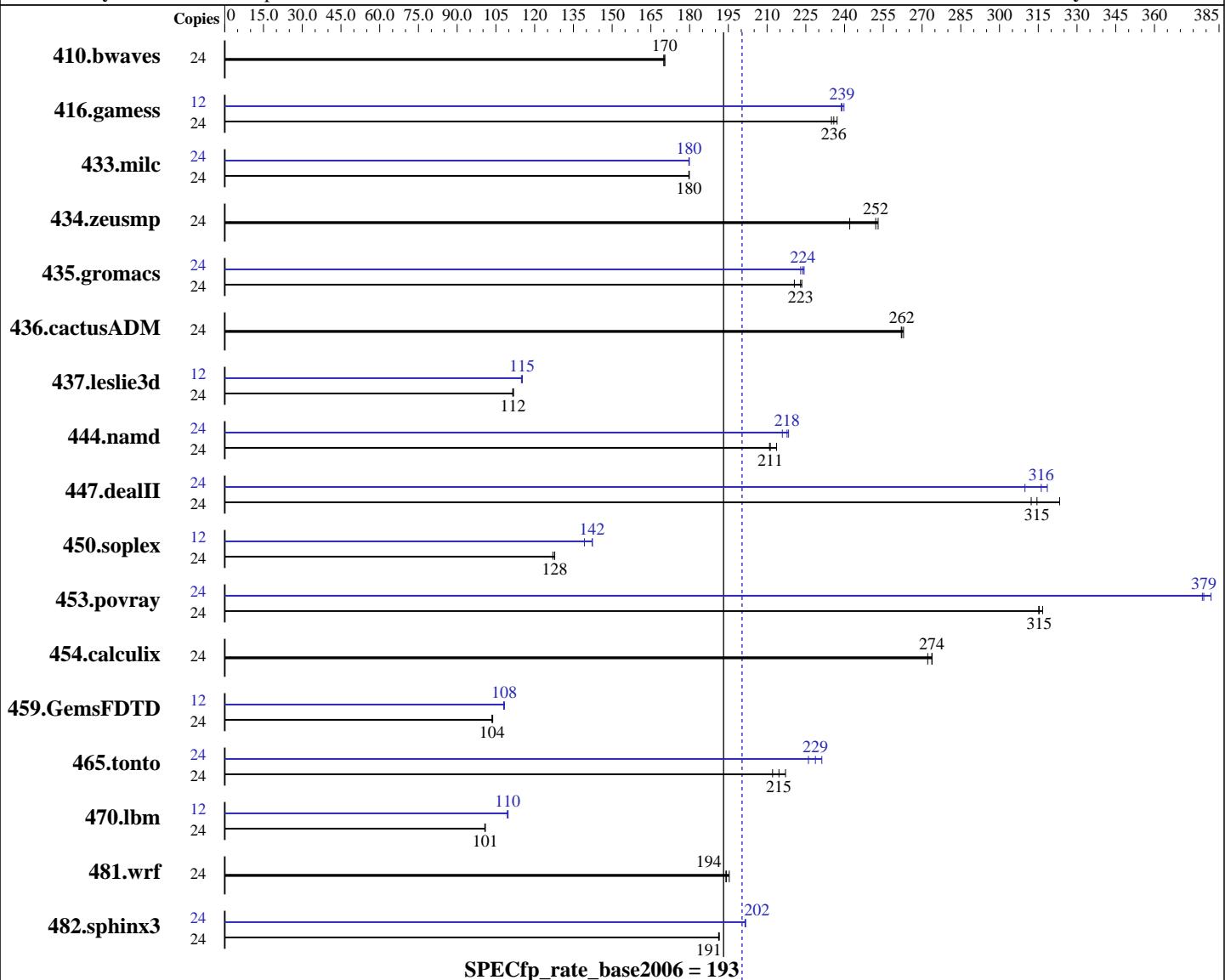
Test date: Jun-2010

Test sponsor: IBM Corporation

Hardware Availability: Sep-2010

Tested by: IBM Corporation

Software Availability: Jan-2010



## Hardware

CPU Name: Intel Xeon E5645  
CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
CPU MHz: 2400  
FPU: Integrated  
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
CPU(s) orderable: 1,2 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

## Software

Operating System: SuSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-default  
Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l\_cproc\_p\_11.1.064, l\_cprof\_p\_11.1.064  
Auto Parallel: No  
File System: ext3  
System State: Run level 3 (multi-user)

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L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB PC3-10600R CL9, 2 Rank)  
 Disk Subsystem: 2 x 50 GB SATA, SSD, RAID 0  
 Other Hardware: None

Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	1919	170	<b>1915</b>	<b>170</b>	1914	170	24	1919	170	<b>1915</b>	<b>170</b>	1914	170
416.gamess	24	2000	235	1982	237	<b>1993</b>	<b>236</b>	12	984	239	<b>983</b>	<b>239</b>	980	240
433.milc	24	1225	180	<b>1226</b>	<b>180</b>	1226	180	24	<b>1225</b>	<b>180</b>	1225	180	1226	180
434.zeusmp	24	863	253	902	242	<b>866</b>	<b>252</b>	24	863	253	902	242	<b>866</b>	<b>252</b>
435.gromacs	24	767	223	<b>769</b>	<b>223</b>	777	221	24	<b>766</b>	<b>224</b>	768	223	764	224
436.cactusADM	24	1095	262	<b>1094</b>	<b>262</b>	1091	263	24	1095	262	<b>1094</b>	<b>262</b>	1091	263
437.leslie3d	24	2018	112	<b>2020</b>	<b>112</b>	2023	112	12	<b>979</b>	<b>115</b>	982	115	979	115
444.namd	24	901	214	913	211	<b>911</b>	<b>211</b>	24	892	216	882	218	<b>884</b>	<b>218</b>
447.dealII	24	879	312	849	323	<b>873</b>	<b>315</b>	24	<b>868</b>	<b>316</b>	886	310	862	319
450.soplex	24	1575	127	1566	128	<b>1567</b>	<b>128</b>	12	719	139	<b>703</b>	<b>142</b>	703	142
453.povray	24	403	317	405	315	<b>405</b>	<b>315</b>	24	334	382	<b>337</b>	<b>379</b>	337	379
454.calculix	24	<b>723</b>	<b>274</b>	723	274	727	272	24	<b>723</b>	<b>274</b>	723	274	727	272
459.GemsFDTD	24	2454	104	2460	104	<b>2459</b>	<b>104</b>	12	<b>1177</b>	<b>108</b>	1176	108	1179	108
465.tonto	24	1088	217	<b>1101</b>	<b>215</b>	1113	212	24	1021	231	1045	226	<b>1033</b>	<b>229</b>
470.lbm	24	3269	101	<b>3271</b>	<b>101</b>	3275	101	12	1503	110	1507	109	<b>1505</b>	<b>110</b>
481.wrf	24	1372	195	<b>1380</b>	<b>194</b>	1381	194	24	1372	195	<b>1380</b>	<b>194</b>	1381	194
482.sphinx3	24	2446	191	2442	192	<b>2445</b>	<b>191</b>	24	<b>2321</b>	<b>202</b>	2322	201	2319	202

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
 numactl was used to bind copies to the cores

## Platform Notes

Turbo Mode enabled  
 Turbo Boost set to Traditional  
 Power C-states enabled  
 Demand Scrub disabled



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## General Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run  
Binaries were compiled on SLES 10 with Binutils 2.18.50.50.0.7.20080502

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

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## Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

```
433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -fno-alias -opt-prefetch
```

```
470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -opt-malloc-options=3 -ansi-alias -auto-ilp32
```

```
482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
```

C++ benchmarks:

```
444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -fno-alias -auto-ilp32
```

```
447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -unroll2 -ansi-alias -scalar-rep-
```

```
450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -opt-malloc-options=3
```

```
453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -unroll4 -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: basepeak = yes
```

```
416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -unroll2 -Obo -ansi-alias -scalar-rep-
```

```
434.zeusmp: basepeak = yes
```

```
437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static
```

```
459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -unroll2 -Obo
```

```
465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -unroll4 -auto -inline-calloc -opt-malloc-options=3
```

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## Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
435.gromacs: -xSSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
               -opt-prefetch -auto-ilp32
```

```
436.cactusADM: basepeak = yes
```

```
454.calculix: basepeak = yes
```

```
481.wrf: basepeak = yes
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100603.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100603.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

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