



# SPEC<sup>®</sup> CINT2006 Result

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## Hewlett-Packard Company

SPECint<sup>®</sup>\_rate2006 = 382

ProLiant DL580 G7  
(2.27 GHz, Intel Xeon X7560)

SPECint\_rate\_base2006 = 355

CPU2006 license: 3

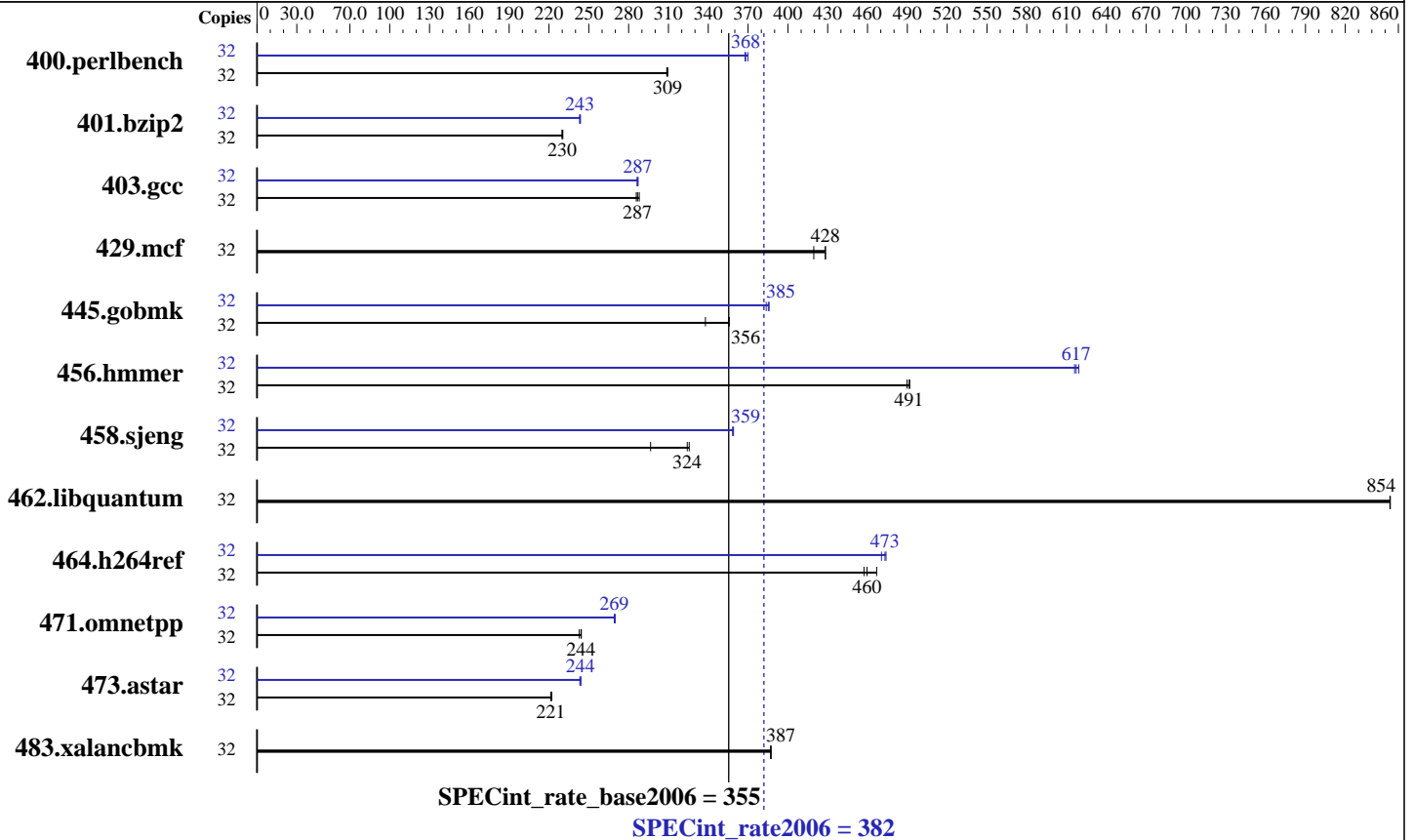
Test date: Jun-2010

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jun-2010

Tested by: Hewlett-Packard Company

Software Availability: Mar-2010



### Hardware

CPU Name: Intel Xeon X7560  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.67 GHz  
 CPU MHz: 2267  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 24 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 512 GB (32 x 16 GB 4Rx4 PC3-8500R CL7)  
 Disk Subsystem: 2 x 146 GB 15 k SAS  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 5.5  
 Advanced Platform, Kernel 2.6.18-194.el5  
 Compiler: Intel C++ Compiler 11.1 for Linux  
 Build 20090827 Package ID: l\_cproc\_p\_11.1.056  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1  
 Binutils 2.17.50.0.6-12.el5



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### Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	<u>1011</u>	<u>309</u>	1013	309	1011	309	32	845	370	850	368	<u>849</u>	<u>368</u>
401.bzip2	32	1340	230	1344	230	<u>1342</u>	<u>230</u>	32	1267	244	<u>1270</u>	<u>243</u>	1270	243
403.gcc	32	894	288	<u>898</u>	<u>287</u>	901	286	32	900	286	897	287	<u>898</u>	<u>287</u>
429.mcf	32	696	420	<u>682</u>	<u>428</u>	681	428	32	696	420	<u>682</u>	<u>428</u>	681	428
445.gobmk	32	993	338	943	356	<u>944</u>	<u>356</u>	32	870	386	<u>871</u>	<u>385</u>	875	384
456.hammer	32	610	490	<u>608</u>	<u>491</u>	607	492	32	482	619	485	616	<u>484</u>	<u>617</u>
458.sjeng	32	1306	297	<u>1194</u>	<u>324</u>	1189	326	32	1080	358	1078	359	<u>1080</u>	<u>359</u>
462.libquantum	32	777	854	<u>777</u>	<u>854</u>	777	854	32	777	854	<u>777</u>	<u>854</u>	777	854
464.h264ref	32	<u>1541</u>	<u>460</u>	1517	467	1548	457	32	<u>1497</u>	<u>473</u>	1494	474	1505	471
471.omnetpp	32	818	244	824	243	<u>819</u>	<u>244</u>	32	741	270	743	269	<u>743</u>	<u>269</u>
473.astar	32	1011	222	<u>1015</u>	<u>221</u>	1015	221	32	924	243	<u>921</u>	<u>244</u>	920	244
483.xalancbmk	32	570	387	<u>570</u>	<u>387</u>	570	387	32	570	387	<u>570</u>	<u>387</u>	570	387

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

### Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

### Platform Notes

BIOS configuration:  
Power Profile set to Maximum Performance  
Thermal Configuration set to Increased Cooling

### Base Compiler Invocation

C benchmarks:  
icc  
  
C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/cpu2006/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc

401.bzip2: /opt/intel/Compiler/11.1/056/bin/intel64/icc  
456.hmmer: /opt/intel/Compiler/11.1/056/bin/intel64/icc  
458.sjeng: /opt/intel/Compiler/11.1/056/bin/intel64/icc

C++ benchmarks:  
icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/cpu2006/SmartHeap\_8.1/lib -lsmarheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs  
-L/cpu2006/SmartHeap\_8.1/lib -lsmarheap

483.xalanbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

Continued on next page



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## Peak Other Flags (Continued)

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revF.html>  
<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20100525.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revF.xml>  
<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20100525.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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