



SPEC® CINT2006 Result

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Acer Incorporated

SPECint®_rate2006 = 353

Acer AB2x280 F1 (Intel Xeon X5670, 2.93 GHz)

SPECint_rate_base2006 = 329

CPU2006 license: 97

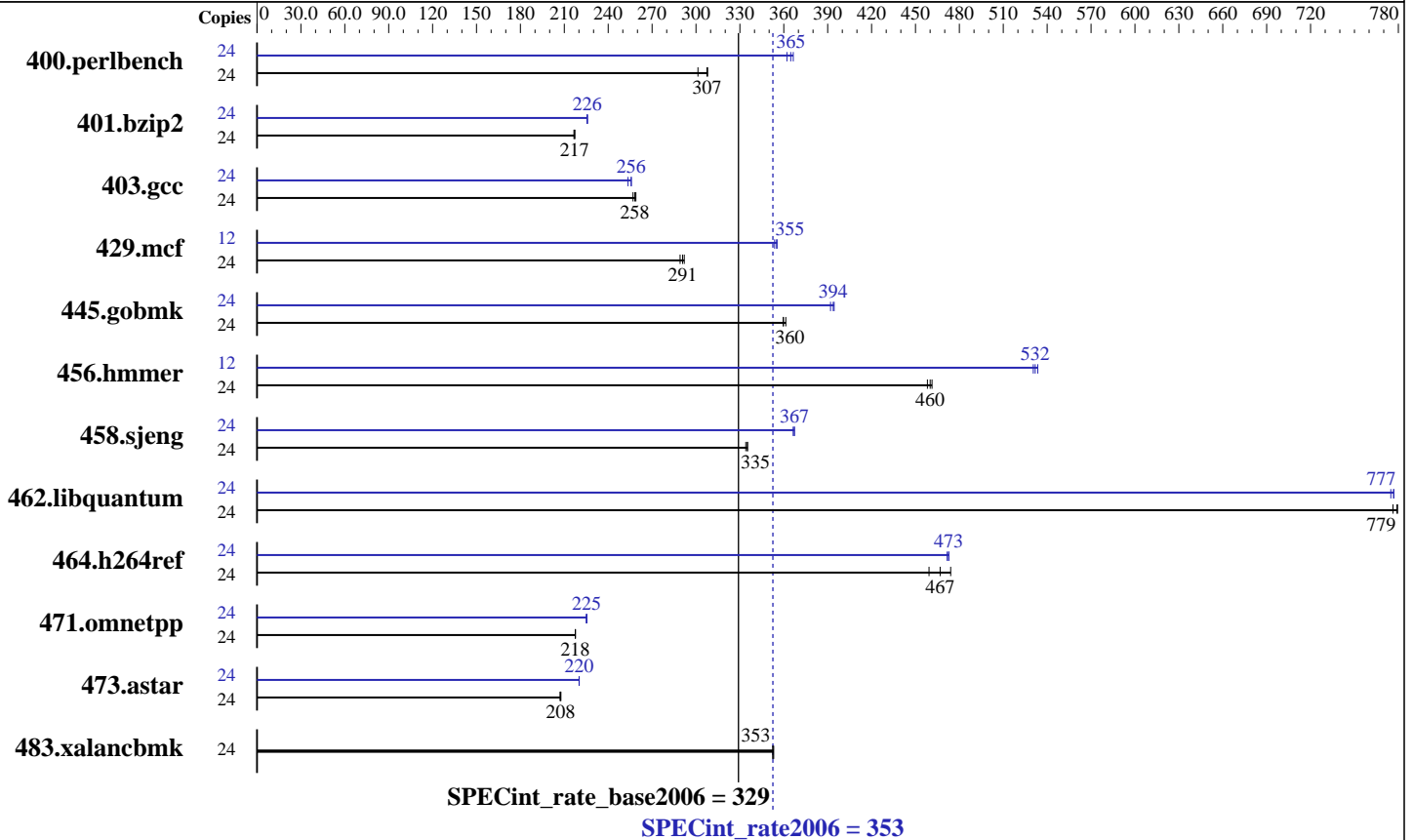
Test date: Feb-2009

Test sponsor: Acer Incorporated

Hardware Availability: Sep-2010

Tested by: Acer Incorporated

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon X5670
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (6 x 8 GB DDR3-1333 RDIMM, ECC, CL9)
 Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64)
 Kernel 2.6.27.19-5-default
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1
 Build 20091130 Package ID: l_cproc_p_11.1.064
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



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Results Table

| Benchmark | Base | | | | | | Peak | | | | | | | |
|----------------|--------|-------------------|-------------------|--------------------|-------------------|--------------------|-------------------|--------|--------------------|-------------------|--------------------|-------------------|-------------------|-------------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 24 | 778 | 301 | <u>763</u> | <u>307</u> | 761 | 308 | 24 | <u>643</u> | <u>365</u> | 647 | 362 | 640 | 366 |
| 401.bzip2 | 24 | 1069 | 217 | <u>1067</u> | <u>217</u> | 1067 | 217 | 24 | <u>1026</u> | <u>226</u> | 1027 | 226 | 1025 | 226 |
| 403.gcc | 24 | <u>748</u> | <u>258</u> | 752 | 257 | 746 | 259 | 24 | 762 | 254 | 755 | 256 | <u>755</u> | <u>256</u> |
| 429.mcf | 24 | 757 | 289 | <u>753</u> | <u>291</u> | 750 | 292 | 12 | <u>308</u> | <u>355</u> | 309 | 354 | 308 | 355 |
| 445.gobmk | 24 | <u>700</u> | <u>360</u> | 697 | 361 | 700 | 360 | 24 | <u>639</u> | <u>394</u> | 642 | 392 | 638 | 394 |
| 456.hammer | 24 | <u>487</u> | <u>460</u> | 485 | 461 | 489 | 458 | 12 | <u>211</u> | <u>532</u> | 211 | 530 | 210 | 534 |
| 458.sjeng | 24 | 869 | 334 | <u>866</u> | <u>335</u> | 866 | 335 | 24 | <u>791</u> | <u>367</u> | 793 | 366 | 791 | 367 |
| 462.libquantum | 24 | 641 | 776 | 638 | 779 | <u>638</u> | <u>779</u> | 24 | 642 | 775 | 640 | 777 | <u>640</u> | <u>777</u> |
| 464.h264ref | 24 | 1120 | 474 | 1156 | 459 | <u>1138</u> | <u>467</u> | 24 | 1123 | 473 | <u>1124</u> | <u>473</u> | 1126 | 472 |
| 471.omnetpp | 24 | 690 | 218 | <u>690</u> | <u>218</u> | 689 | 218 | 24 | 667 | 225 | <u>666</u> | <u>225</u> | 665 | 225 |
| 473.astar | 24 | 814 | 207 | 811 | 208 | <u>812</u> | <u>208</u> | 24 | 765 | 220 | <u>765</u> | <u>220</u> | 765 | 220 |
| 483.xalancbmk | 24 | <u>470</u> | <u>353</u> | 469 | 353 | 470 | 353 | 24 | <u>470</u> | <u>353</u> | 469 | 353 | 470 | 353 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

Platform Notes

Fan speed set to full Speed (ie. Enterprise Blade mode) with Smart Blade Console through CMM (Chassis Management Module)

General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

The Acer AB2x280 F1, and Gateway GB2x280 F1 are electronically equivalent.
This result was measured on Gateway GB2x280 F1.

Base Compiler Invocation

C benchmarks:
icc -m32

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmarheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):
icpc -m32

473.astar: icpc -m64



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Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
            -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
            -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
            -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
            -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
                -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
              -prof-use(pass 2) -unroll2 -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
              -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

```

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Peak Optimization Flags (Continued)

```
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
           -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64
```

```
483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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