SPEC® CINT2006 Result
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Fujitsu

PRIMERGY RX200 S6, Intel Xeon X5670, 2.93 GHz

SPECint®_rate2006 =   185
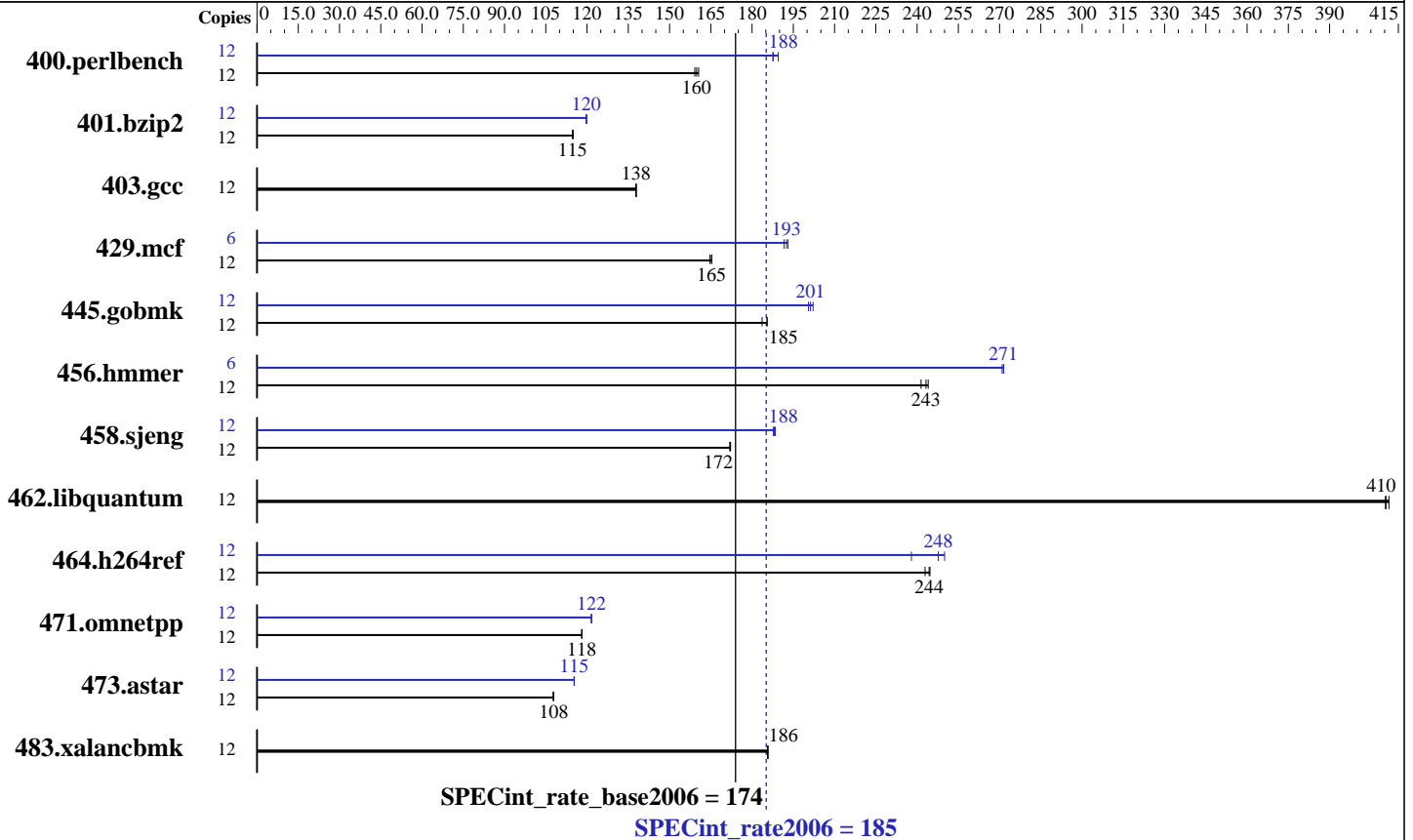
SPECint_rate_base2006 = 174

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Jun-2010
Hardware Availability: Oct-2010
Software Availability: Jan-2010

Copies 0 15.0 30.0 45.0 60.0 75.0 90.0 105 120 135 150 165 180 195 210 225 240 255 270 285 300 315 330 345 360 375 390 415

400.perlbench 12 / 12 — 188 / 160

401.bzip2 12 / 12 — 120 / 115

403.gcc 12 — 138

429.mcf 6 / 12 — 193 / 165

445.gobmk 12 / 12 — 201 / 185

456.hmmer 6 / 12 — 271 / 243

458.sjeng 12 / 12 — 188 / 172

462.libquantum 12 — 410

464.h264ref 12 / 12 — 248 / 244

471.omnetpp 12 / 12 — 122 / 118

473.astar 12 / 12 — 115 / 108

483.xalancbmk 12 — 186

SPECint_rate_base2006 = 174

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Hardware

CPU Name:	Intel Xeon X5670
CPU Characteristics:	Intel Turbo Boost Technology up to 3.33 GHz
CPU MHz:	2933
FPU:	Integrated
CPU(s) enabled:	6 cores, 1 chip, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (6x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC)
Disk Subsystem:	1 x SATA, 160 GB, 5400 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel:	No
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1

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Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	12	730	161	**733**	**160**	736	159	12	625	188	619	190	**625**	**188**
401.bzip2	12	1008	115	**1009**	**115**	1009	115	12	966	120	968	120	**967**	**120**
403.gcc	12	701	138	**701**	**138**	700	138	12	701	138	**701**	**138**	700	138
429.mcf	12	**662**	**165**	665	165	662	165	6	286	192	283	193	**284**	**193**
445.gobmk	12	685	184	**679**	**185**	679	185	12	**626**	**201**	623	202	628	201
456.hmmer	12	459	244	**460**	**243**	464	241	6	**206**	**271**	207	271	206	272
458.sjeng	12	**844**	**172**	845	172	843	172	12	770	188	**772**	**188**	774	188
462.libquantum	12	**606**	**410**	606	410	604	412	12	**606**	**410**	606	410	604	412
464.h264ref	12	**1087**	**244**	1093	243	1085	245	12	**1072**	**248**	1062	250	1116	238
471.omnetpp	12	**635**	**118**	635	118	635	118	12	617	122	**617**	**122**	617	122
473.astar	12	781	108	783	108	**781**	**108**	12	**730**	**115**	731	115	730	115
483.xalancbmk	12	**446**	**186**	446	186	446	186	12	**446**	**186**	446	186	446	186

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
Data Reuse Optimization = Disable

General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
        icc -m32

C++ benchmarks:
        icpc -m32

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Base Portability Flags

```
  400.perlbench: -DSPEC_CPU_LINUX_IA32
  462.libquantum: -DSPEC_CPU_LINUX
  483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
    -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
```

C++ benchmarks:

```
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
```

Base Other Flags

C benchmarks:

```
        403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
    icc  -m32
```

```
    401.bzip2: icc -m64
```

```
    456.hmmer: icc -m64
```

```
    458.sjeng: icc -m64
```

C++ benchmarks (except as noted below):
```
    icpc -m32
```

```
    473.astar: icpc -m64
```

Peak Portability Flags

```
  400.perlbench: -DSPEC_CPU_LINUX_IA32
    401.bzip2: -DSPEC_CPU_LP64
    456.hmmer: -DSPEC_CPU_LP64
    458.sjeng: -DSPEC_CPU_LP64
```

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Peak Portability Flags (Continued)

```
462.libquantum: -DSPEC_CPU_LINUX
     473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -ansi-alias

   401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
              -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

     403.gcc: basepeak = yes

     429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

   445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
              -ipo -no-prec-div -ansi-alias

   456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
              -ansi-alias -auto-ilp32

   458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
              -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

  464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
             -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
             -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
             -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

   473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
              -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64
```

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.01.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.01.xml

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