



# SPEC® CFP2006 Result

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Fujitsu

PRIMERGY BX922 S2, Intel Xeon X5670, 2.93 GHz

**SPECfp®\_rate2006 = 125**

**SPECfp\_rate\_base2006 = 121**

CPU2006 license: 19

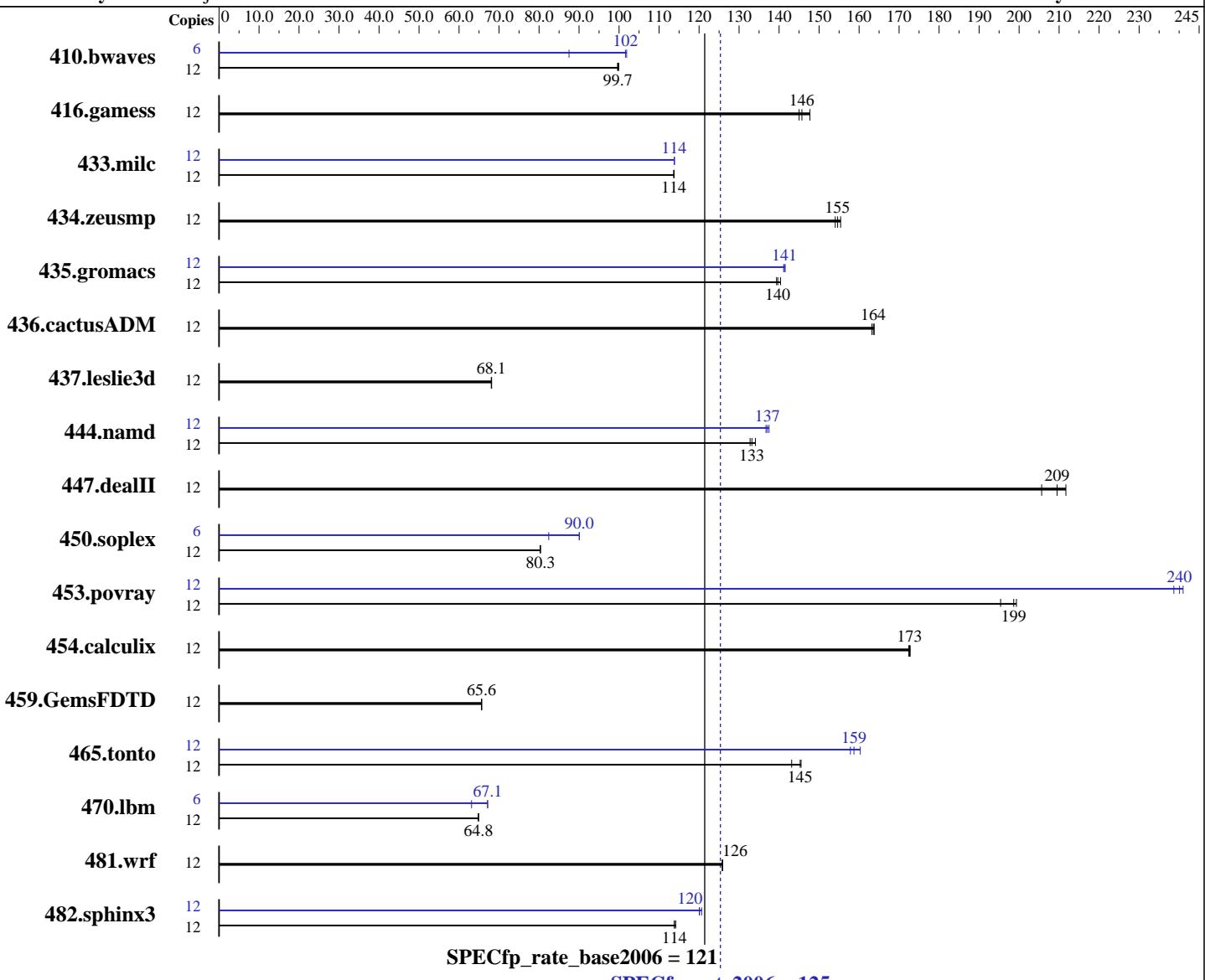
Test date: May-2010

Test sponsor: Fujitsu

Hardware Availability: Oct-2010

Tested by: Fujitsu

Software Availability: Jan-2010



## Hardware

CPU Name: Intel Xeon X5670  
CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz  
CPU MHz: 2933  
FPU: Integrated  
CPU(s) enabled: 6 cores, 1 chip, 6 cores/chip, 2 threads/core  
CPU(s) orderable: 1,2 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

## Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-default  
Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l\_cproc\_p\_11.1.064, l\_cprof\_p\_11.1.064  
Auto Parallel: No  
File System: ext3  
System State: Multi-User Run Level 3

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L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (6x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC)  
 Disk Subsystem: 1 x SSD SATA, 64 GB  
 Other Hardware: None

Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	12	1632	99.9	<b>1635</b>	<b>99.7</b>	1637	99.6	6	932	87.5	800	102	<b>803</b>	<b>102</b>
416.gamess	12	1591	148	<b>1613</b>	<b>146</b>	1620	145	12	1591	148	<b>1613</b>	<b>146</b>	1620	145
433.milc	12	<b>969</b>	<b>114</b>	968	114	969	114	12	968	114	968	114	<b>968</b>	<b>114</b>
434.zeusmp	12	703	155	709	154	<b>706</b>	<b>155</b>	12	703	155	709	154	<b>706</b>	<b>155</b>
435.gromacs	12	<b>613</b>	<b>140</b>	615	139	610	140	12	<b>606</b>	<b>141</b>	607	141	605	142
436.cactusADM	12	876	164	879	163	<b>877</b>	<b>164</b>	12	876	164	879	163	<b>877</b>	<b>164</b>
437.leslie3d	12	<b>1657</b>	<b>68.1</b>	1655	68.1	1657	68.1	12	<b>1657</b>	<b>68.1</b>	1655	68.1	1657	68.1
444.namd	12	718	134	<b>722</b>	<b>133</b>	725	133	12	704	137	700	137	<b>702</b>	<b>137</b>
447.dealII	12	648	212	668	206	<b>655</b>	<b>209</b>	12	648	212	668	206	<b>655</b>	<b>209</b>
450.soplex	12	1246	80.3	<b>1246</b>	<b>80.3</b>	1246	80.3	6	607	82.4	<b>556</b>	<b>90.0</b>	556	90.1
453.povray	12	<b>321</b>	<b>199</b>	320	199	327	195	12	<b>266</b>	<b>240</b>	265	241	267	239
454.calculix	12	573	173	<b>574</b>	<b>173</b>	574	172	12	573	173	<b>574</b>	<b>173</b>	574	172
459.GemsFDTD	12	1939	65.7	1941	65.6	<b>1939</b>	<b>65.6</b>	12	1939	65.7	1941	65.6	<b>1939</b>	<b>65.6</b>
465.tonto	12	825	143	<b>813</b>	<b>145</b>	812	145	12	737	160	<b>744</b>	<b>159</b>	748	158
470.lbm	12	2542	64.9	2543	64.8	<b>2543</b>	<b>64.8</b>	6	<b>1228</b>	<b>67.1</b>	1228	67.2	1306	63.1
481.wrf	12	1065	126	<b>1065</b>	<b>126</b>	1066	126	12	1065	126	<b>1065</b>	<b>126</b>	1066	126
482.sphinx3	12	2049	114	<b>2055</b>	<b>114</b>	2055	114	12	<b>1939</b>	<b>121</b>	1948	120	<b>1947</b>	<b>120</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

BIOS configuration:  
 Data Reuse Optimization = Disabled  
 Performance/Power Setting = Traditional

## General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>  
 Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502



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**SPECfp\_rate\_base2006 = 121**

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static



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## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-fno-alias -opt-prefetch

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3 -ansi-alias -auto-ilp32

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## Peak Optimization Flags (Continued)

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll12

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll14 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.html>



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You can also download the XML flags source by saving the following link:

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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