



SPEC® CINT2006 Result

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NEC Corporation

SPECint®_rate2006 = 530

Express5800/A1080a-S/D (Intel Xeon E7540)

SPECint_rate_base2006 = 489

CPU2006 license: 9006

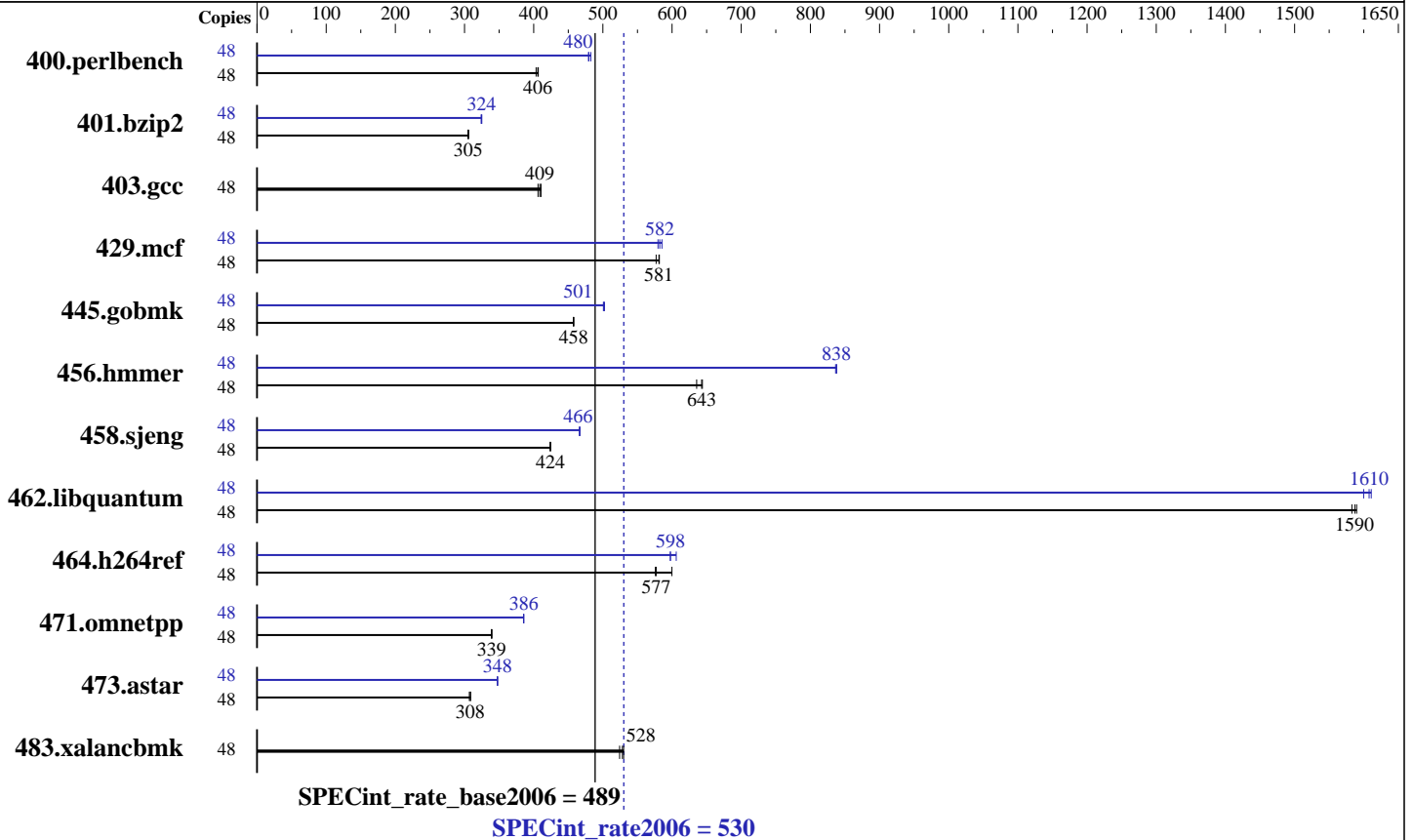
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Jun-2010

Hardware Availability: Jul-2010

Software Availability: Mar-2010



Hardware

CPU Name: Intel Xeon E7540
 CPU Characteristics: Intel Turbo Boost Technology disabled
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 24 cores, 4 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2,3,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 18 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (64 x 4 GB PC3-8500R, 2 rank, CL7, ECC)
 Disk Subsystem: 1x300.0 GB SAS, 10000 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 5.5, Kernel 2.6.18-194.el5 on an x86_64
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: 1_cproc_p_11.1.064
 Auto Parallel: No
 File System: ext2
 System State: Run level 5 (multi-user mode, with display manager as well as console logins)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	48	1153	407	1155	406	1162	403	48	978	479	972	483	978	480
401.bzip2	48	1517	305	1519	305	1514	306	48	1429	324	1425	325	1428	324
403.gcc	48	941	411	951	406	945	409	48	941	411	951	406	945	409
429.mcf	48	753	582	759	577	753	581	48	752	582	755	580	748	585
445.gobmk	48	1099	458	1100	458	1101	457	48	1002	502	1006	501	1004	501
456.hammer	48	696	644	705	636	697	643	48	535	836	534	838	535	838
458.sjeng	48	1371	424	1369	424	1369	424	48	1246	466	1246	466	1244	467
462.libquantum	48	628	1580	626	1590	627	1590	48	622	1600	619	1610	617	1610
464.h264ref	48	1845	576	1840	577	1771	600	48	1753	606	1778	598	1777	598
471.omnetpp	48	885	339	885	339	883	340	48	778	386	778	386	779	385
473.astar	48	1093	308	1091	309	1098	307	48	969	348	969	348	970	347
483.xalancbmk	48	627	528	625	530	632	524	48	627	528	625	530	632	524

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

Power Technology set to disabled in BIOS
Patrol Scrubbing set to disabled in Maintenance Console

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

456.hmmcr: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: basepeak = yes

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
 -ipo -no-prec-div -ansi-alias

456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -Wl,-z,muldefs
 -L/opt/SmartHeap_8.1/lib -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs

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Peak Optimization Flags (Continued)

473.astar (continued):

`-L/opt/SmartHeap_8.1/lib64 -lsmartheap64`

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC.Express5800.A1080a-S.Intel-ic11.1-linux64-revE.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC.Express5800.A1080a-S.Intel-ic11.1-linux64-revE.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

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