



SPEC® CFP2006 Result

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Fujitsu

PRIMERGY BX924 S2, Intel Xeon L5640, 2.27 GHz

SPECfp®_rate2006 = 110

SPECfp_rate_base2006 = 107

CPU2006 license: 19

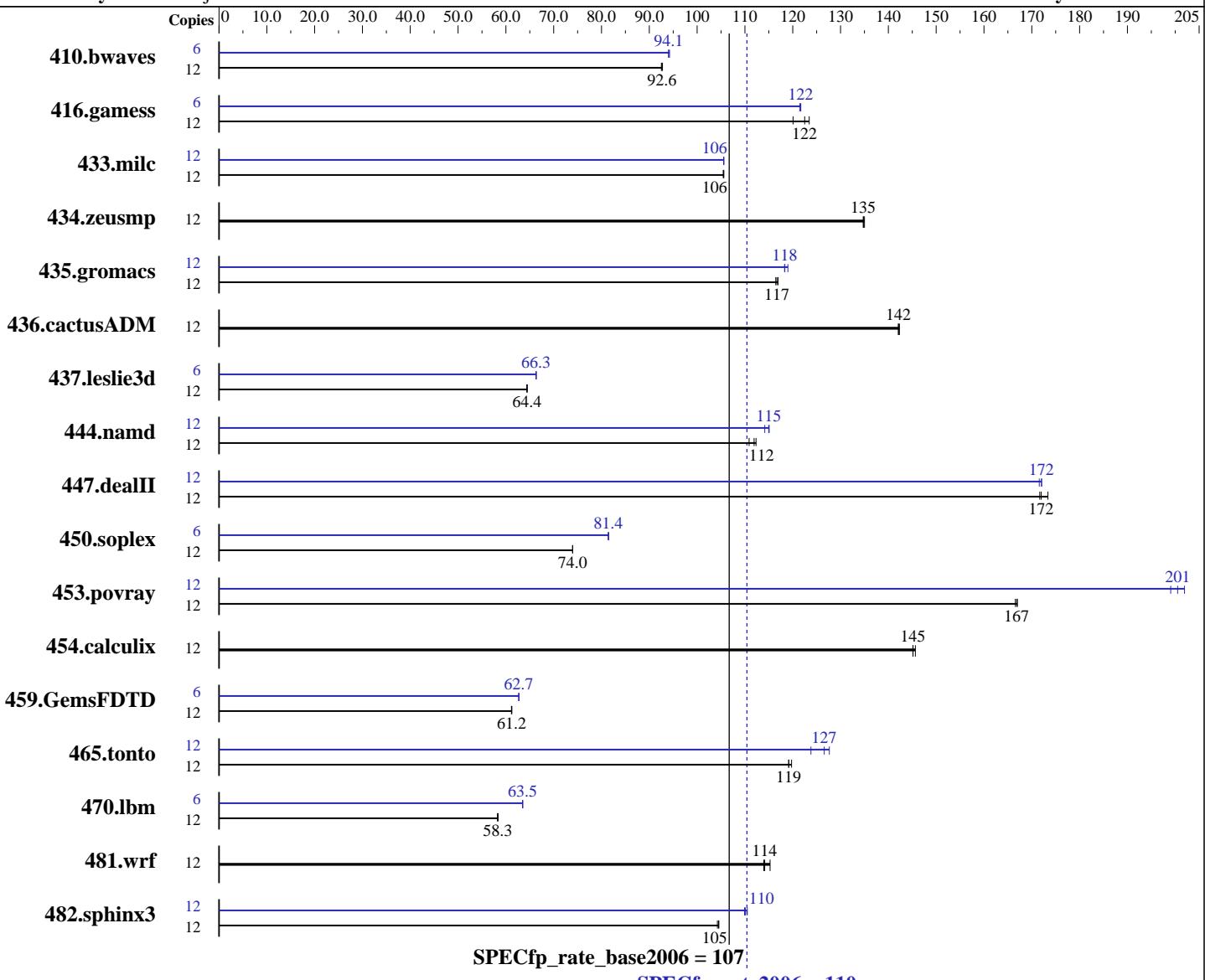
Test date: May-2010

Test sponsor: Fujitsu

Hardware Availability: Jun-2010

Tested by: Fujitsu

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon L5640
CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz: 2267
FPU: Integrated
CPU(s) enabled: 6 cores, 1 chip, 6 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
Auto Parallel: No
File System: ext3
System State: Multi-User Run Level 3

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L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (6x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC)
 Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	12	1758	92.8	1760	92.6	1763	92.5	6	867	94.0	865	94.2	867	94.1
416.gamess	12	1957	120	1918	122	1903	123	6	967	121	966	122	966	122
433.milc	12	1045	105	1043	106	1044	106	12	1044	106	1044	106	1043	106
434.zeusmp	12	810	135	809	135	810	135	12	810	135	809	135	810	135
435.gromacs	12	733	117	736	116	734	117	12	724	118	724	118	720	119
436.cactusADM	12	1008	142	1009	142	1009	142	12	1008	142	1009	142	1009	142
437.leslie3d	12	1750	64.4	1752	64.4	1750	64.5	6	850	66.3	850	66.3	851	66.3
444.namd	12	868	111	857	112	860	112	12	843	114	836	115	837	115
447.dealII	12	800	172	792	173	798	172	12	800	172	798	172	798	172
450.soplex	12	1353	74.0	1353	74.0	1353	74.0	6	614	81.5	615	81.4	615	81.4
453.povray	12	382	167	383	167	383	167	12	318	201	321	199	316	202
454.calculix	12	682	145	682	145	680	146	12	682	145	682	145	680	146
459.GemsFDTD	12	2082	61.2	2081	61.2	2079	61.3	6	1015	62.7	1015	62.7	1015	62.7
465.tonto	12	986	120	991	119	991	119	12	925	128	933	127	954	124
470.lbm	12	2825	58.4	2829	58.3	2830	58.3	6	1299	63.5	1298	63.5	1298	63.5
481.wrf	12	1176	114	1174	114	1163	115	12	1176	114	1174	114	1163	115
482.sphinx3	12	2238	105	2237	105	2244	104	12	2117	110	2128	110	2125	110

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
 numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
 Data Reuse Optimization = Disable
 Performance/Power Setting = Traditional



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General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:

 icc -m64

C++ benchmarks:

 icpc -m64

Fortran benchmarks:

 ifort -m64

Benchmarks using both Fortran and C:

 icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

 -xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

 -xSSE4.2 -ipo -O3 -no-prec-div -static

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Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX



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SPECfp_rate_base2006 = 107

Peak Optimization Flags

C benchmarks:

```
433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -fno-alias -opt-prefetch
```

```
470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -opt-malloc-options=3 -ansi-alias -auto-ilp32
```

```
482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
```

C++ benchmarks:

```
444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -fno-alias -auto-ilp32
```

```
447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -unroll2 -ansi-alias -scalar-rep-
```

```
450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -opt-malloc-options=3
```

```
453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -unroll4 -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
```

```
416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -unroll2 -Ob0 -ansi-alias -scalar-rep-
```

```
434.zeusmp: basepeak = yes
```

```
437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static
```

```
459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -unroll2 -Ob0
```

```
465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -unroll4 -auto -inline-calloc -opt-malloc-options=3
```

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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
435.gromacs: -xSSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
               -opt-prefetch -auto-ilp32
```

```
436.cactusADM: basepeak = yes
```

```
454.calculix: basepeak = yes
```

```
481.wrf: basepeak = yes
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

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