# SPEC® CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu

## CELSIUS W380, Intel Xeon X3480

SPECint®_rate2006 =    130

SPECint_rate_base2006 =  122
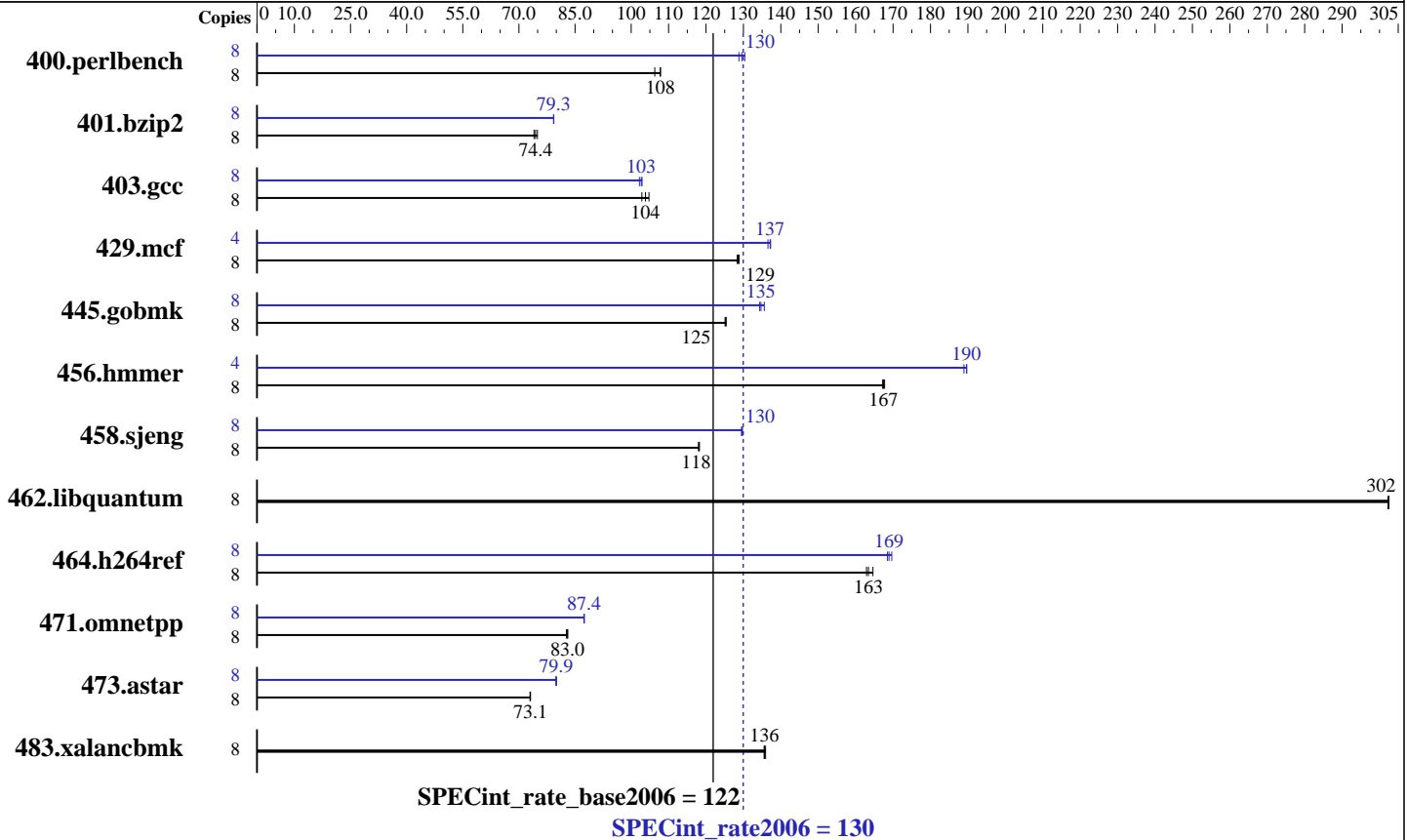
**CPU2006 license:** 19

**Test sponsor:** Fujitsu

**Tested by:** Fujitsu

**Test date:** Jun-2010

**Hardware Availability:** Jun-2010

**Software Availability:** Oct-2009



SPECint_rate_base2006 = 122

SPECint_rate2006 = 130

## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon X3480 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.73 GHz |
| CPU MHz: | 3067 |
| FPU: | Integrated |
| CPU(s) enabled: | 4 cores, 1 chip, 4 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 8 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 8 GB (2x4 GB PC3-10600E, 2 rank, CL9, ECC) |
| Disk Subsystem: | 1 x SATA II, 400 GB, 7200 rpm |
| Other Hardware: | None |

## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 (x86_64), kernel 2.6.27.19-5-default |
| Compiler: | Intel C++ Compiler for IA32 and Intel 64, Version 11.1 Build 20091012 Package ID: l_cproc_p_11.1.059 |
| Auto Parallel: | No |
| File System: | ext3 |
| System State: | Multi-User Run Level 3 |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V8.1 |

# SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu

## CELSIUS W380, Intel Xeon X3480

| SPECint_rate2006 = | 130 |
|---|---|
| SPECint_rate_base2006 = | 122 |

**CPU2006 license:** 19
**Test sponsor:** Fujitsu
**Tested by:** Fujitsu

**Test date:** Jun-2010
**Hardware Availability:** Jun-2010
**Software Availability:** Oct-2009

## Results Table

| Benchmark | Base | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 8 | 725 | 108 | 735 | 106 | **725** | **108** | 8 | 607 | 129 | 600 | 130 | **603** | **130** |
| 401.bzip2 | 8 | 1030 | 75.0 | **1037** | **74.4** | 1043 | 74.0 | 8 | **973** | **79.3** | 973 | 79.3 | 974 | 79.3 |
| 403.gcc | 8 | 615 | 105 | **620** | **104** | 626 | 103 | 8 | **627** | **103** | 626 | 103 | 630 | 102 |
| 429.mcf | 8 | 569 | 128 | 566 | 129 | **567** | **129** | 4 | **266** | **137** | 266 | 137 | 267 | 137 |
| 445.gobmk | 8 | **670** | **125** | 671 | 125 | 669 | 125 | 8 | 625 | 134 | 619 | 136 | **623** | **135** |
| 456.hmmer | 8 | **446** | **167** | 446 | 167 | 445 | 168 | 4 | 197 | 190 | 198 | 189 | **197** | **190** |
| 458.sjeng | 8 | 819 | 118 | 820 | 118 | **820** | **118** | 8 | 746 | 130 | 748 | 129 | **747** | **130** |
| 462.libquantum | 8 | **548** | **302** | 548 | 303 | 548 | 302 | 8 | **548** | **302** | 548 | 303 | 548 | 302 |
| 464.h264ref | 8 | 1087 | 163 | **1083** | **163** | 1075 | 165 | 8 | **1048** | **169** | 1044 | 170 | 1051 | 169 |
| 471.omnetpp | 8 | 605 | 82.7 | **602** | **83.0** | 602 | 83.0 | 8 | 572 | 87.4 | 572 | 87.5 | **572** | **87.4** |
| 473.astar | 8 | **769** | **73.1** | 770 | 73.0 | 768 | 73.1 | 8 | **703** | **79.9** | 703 | 79.9 | 702 | 80.0 |
| 483.xalancbmk | 8 | 406 | 136 | 407 | 136 | **407** | **136** | 8 | 406 | 136 | 407 | 136 | **407** | **136** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

## Operating System Notes

    'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## General Notes

    Binaries were compiled on SLES10 with Binutils 2.18.50.0.7.20080502

## Base Compiler Invocation

C benchmarks:
        icc -m32

C++ benchmarks:
        icpc -m32

## Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32

# SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu

## CELSIUS W380, Intel Xeon X3480

SPECint_rate2006 = 130

SPECint_rate_base2006 = 122

**CPU2006 license:** 19
**Test sponsor:** Fujitsu
**Tested by:** Fujitsu

**Test date:** Jun-2010
**Hardware Availability:** Jun-2010
**Software Availability:** Oct-2009

## Base Portability Flags (Continued)

```
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

## Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc -m32
```

```
401.bzip2: icc -m64
```

```
456.hmmer: icc -m64
```

```
458.sjeng: icc -m64
```

C++ benchmarks (except as noted below):
```
icpc -m32
```

```
473.astar: icpc -m64
```

## Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
```

## Fujitsu

CELSIUS W380, Intel Xeon X3480

SPECint_rate2006 = 130

SPECint_rate_base2006 = 122

| | | | |
|---|---|---|---|
| **CPU2006 license:** 19 | | **Test date:** | Jun-2010 |
| **Test sponsor:** Fujitsu | | **Hardware Availability:** | Jun-2010 |
| **Tested by:** Fujitsu | | **Software Availability:** | Oct-2009 |

# Peak Portability Flags (Continued)

473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

# Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -ansi-alias`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32`

403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div -static`

429.mcf: `-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2`
`-ipo -no-prec-div -ansi-alias`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2`
`-ansi-alias -auto-ilp32`

458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -unroll4 -auto-ilp32`

462.libquantum: `basepeak = yes`

464.h264ref: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap`

473.astar: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64`

# SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu

CELSIUS W380, Intel Xeon X3480

SPECint_rate2006 =         130

SPECint_rate_base2006 = 122

**CPU2006 license:** 19
**Test sponsor:** Fujitsu
**Tested by:** Fujitsu

**Test date:** Jun-2010
**Hardware Availability:** Jun-2010
**Software Availability:** Oct-2009

## Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revD.20100511.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revD.20100511.xml

SPEC and SPECint are registered trademarks of the Standard Performance
Evaluation Corporation.  All other brand and product names appearing in
this result are trademarks or registered trademarks of their respective
holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 08:11:47 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 22 June 2010.