



SPEC® CFP2006 Result

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Dell Inc.

SPECfp®_rate2006 = 309

PowerEdge R815 (AMD Opteron 6172, 2.10 GHz)

SPECfp_rate_base2006 = 282

CPU2006 license: 55

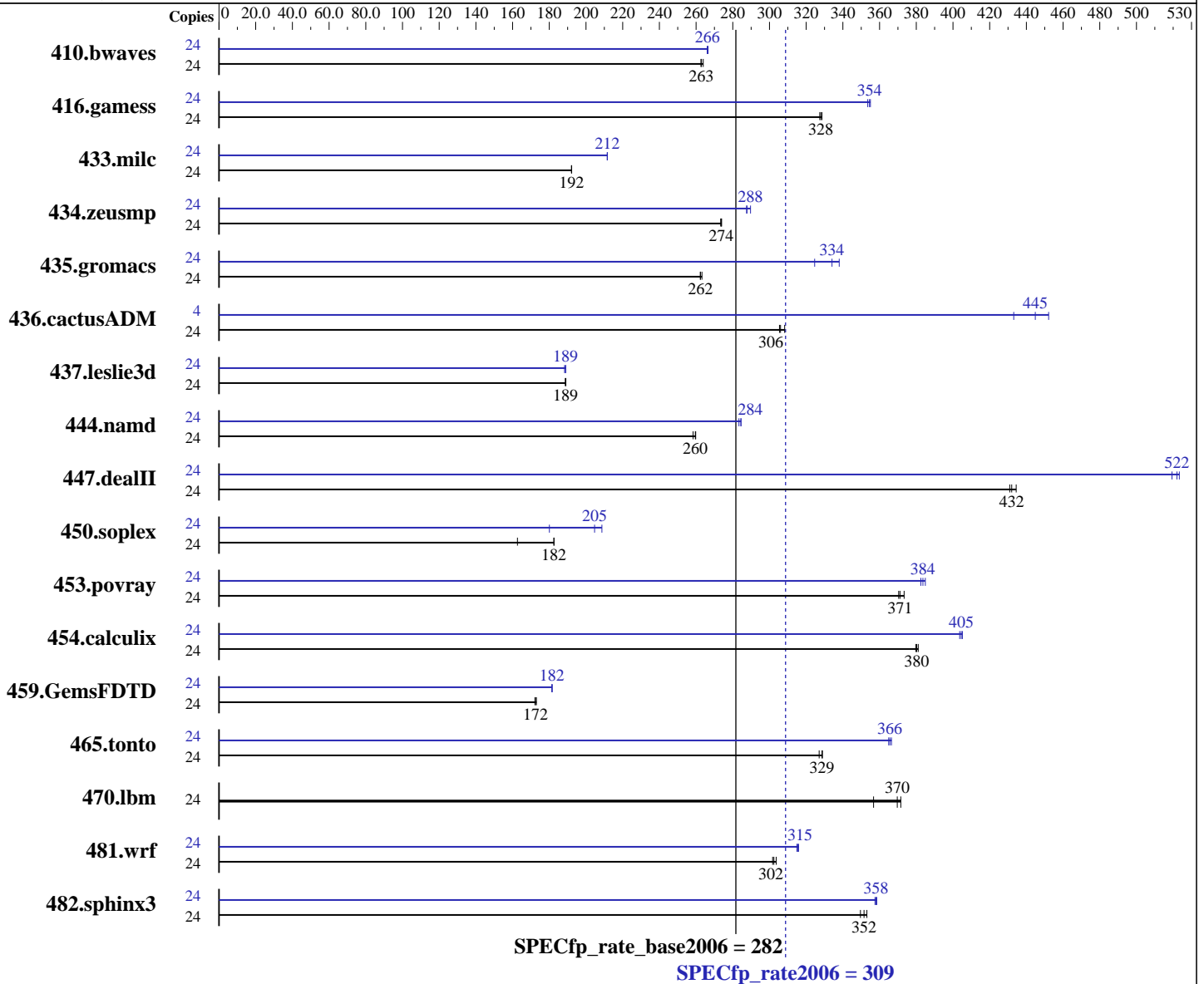
Test date: Apr-2010

Test sponsor: Dell Inc.

Hardware Availability: Mar-2010

Tested by: Dell Inc.

Software Availability: Feb-2010



Hardware

CPU Name: AMD Opteron 6172
 CPU Characteristics:
 CPU MHz: 2100
 FPU: Integrated
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip
 CPU(s) orderable: 2,4 chips
 Primary Cache: 64 KB I + 64 KB D on chip per core
 Secondary Cache: 512 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-smp
 Compiler: x86 Open64 4.2.3 Compiler Suite (from AMD)
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (Full multiuser with network)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

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L3 Cache: 12 MB I+D on chip per chip, 6 MB shared / 6 cores
 Other Cache: None
 Memory: 64 GB (16 x 4 GB DDR3-1333 DR RDIMM, CL9, ECC)
 Disk Subsystem: 1 x 148 GB 10000 RPM SAS
 Other Hardware: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	1242	263	1236	264	<u>1241</u>	<u>263</u>	24	1226	266	<u>1225</u>	<u>266</u>	1224	267
416.gamess	24	<u>1433</u>	<u>328</u>	1435	327	1430	329	24	<u>1326</u>	<u>354</u>	1324	355	1329	354
433.milc	24	1146	192	<u>1147</u>	<u>192</u>	1147	192	24	1041	212	<u>1041</u>	<u>212</u>	1041	212
434.zeusmp	24	799	273	797	274	<u>798</u>	<u>274</u>	24	760	288	<u>759</u>	<u>288</u>	754	290
435.gromacs	24	651	263	<u>653</u>	<u>262</u>	653	262	24	507	338	<u>513</u>	<u>334</u>	528	325
436.cactusADM	24	930	308	939	305	<u>937</u>	<u>306</u>	4	106	452	<u>107</u>	<u>445</u>	110	433
437.leslie3d	24	1195	189	1193	189	<u>1195</u>	<u>189</u>	24	1198	188	<u>1195</u>	<u>189</u>	1194	189
444.namd	24	745	258	741	260	<u>741</u>	<u>260</u>	24	679	283	677	284	<u>677</u>	<u>284</u>
447.dealII	24	<u>636</u>	<u>432</u>	637	431	632	434	24	529	519	<u>526</u>	<u>522</u>	525	523
450.soplex	24	1231	163	1096	183	<u>1097</u>	<u>182</u>	24	1112	180	<u>978</u>	<u>205</u>	959	209
453.povray	24	345	370	<u>344</u>	<u>371</u>	342	373	24	<u>333</u>	<u>384</u>	334	382	332	385
454.calculix	24	520	381	<u>521</u>	<u>380</u>	521	380	24	<u>489</u>	<u>405</u>	489	405	491	404
459.GemsFDTD	24	1479	172	1472	173	<u>1476</u>	<u>172</u>	24	1403	182	<u>1403</u>	<u>182</u>	1404	181
465.tonto	24	722	327	<u>719</u>	<u>329</u>	718	329	24	647	365	644	366	<u>646</u>	<u>366</u>
470.lbm	24	<u>892</u>	<u>370</u>	888	372	924	357	24	<u>892</u>	<u>370</u>	888	372	924	357
481.wrf	24	888	302	883	304	<u>886</u>	<u>302</u>	24	851	315	849	316	<u>850</u>	<u>315</u>
482.sphinx3	24	<u>1331</u>	<u>352</u>	1338	350	1325	353	24	1308	357	1305	358	<u>1306</u>	<u>358</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
 'numactl' was used to bind copies to the cores.
 See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
 'ulimit -l 2097152' was used to set environment locked pages in memory limit
 Set vm/nr_hugepages=10800 in /etc/sysctl.conf
 mount -t hugetlbfs nodev /mnt/hugepages



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General Notes

Environment variables set by runspec before the start of the run:

HUGETLB_LIMIT = "450"

LD_LIBRARY_PATH = "/root/cpu2006-1.1/amd1002mc-rate-revA-libs/64:/root/cpu2006-1.1/amd1002mc-rate-revA-libs/32"

OMP_NUM_THREADS = "6"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at

<http://developer.amd.com/cpu/open64>

Binaries were compiled on SLES10 SP2 with binutils 2.18

Base Compiler Invocation

C benchmarks:

opencc

C++ benchmarks:

openCC

Fortran benchmarks:

openf95

Benchmarks using both Fortran and C:

opencc openf95

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64
 436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG
 -fno-second-underscore
 482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

-march=barcelona -mso -Ofast -OPT:malloc_alg=1 -HP:bdt=2m

C++ benchmarks:

-march=barcelona -mso -Ofast -static -INLINE:aggressive=on
-OPT:malloc_alg=1 -HP:bdt=2m

Fortran benchmarks:

-march=barcelona -mso -Ofast -HP

Benchmarks using both Fortran and C:

-march=barcelona -mso -Ofast -OPT:malloc_alg=1 -HP:bdt=2m -HP

Peak Compiler Invocation

C benchmarks:

opencc

C++ benchmarks:

openCC

Fortran benchmarks:

openf95

Benchmarks using both Fortran and C:

opencc openf95

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
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Peak Optimization Flags

C benchmarks:

433.milc: -march=barcelona -mso -Ofast -CG:movnti=1
-CG:local_sched_alg=1 -CG:locs_shallow_depth=1
-HP:bdt=2m:heap=2m -LNO:prefetch=3

470.lbm: basepeak = yes

482.sphinx3: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -OPT:malloc_alg=2
-CG:sse_cse_regs=0 -CG:locs_shallow_depth=1 -CG:cmp_peep=on
-CG:local_sched_alg=1 -INLINE:aggressive=on

C++ benchmarks:

444.namd: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:ignore_feedback=off
-CG:local_sched_alg=2 -CG:load_exe=0 -CG:compute_to=on
-OPT:unroll_size=256 -fno-exceptions -HP:bdt=2m:heap=2m

447.deallI: -march=barcelona -mso -Ofast -static -INLINE:aggressive=on
-LNO:opt=0 -fno-emit-exceptions -m32
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -HP:bdt=2m:heap=2m -GRA:unspill=on
-CG:cmp_peep=on -TENV:frame_pointer=off

450.soplex: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -INLINE:aggressive=on
-OPT:IEEE_arith=3 -OPT:IEEE_NaN_Inf=off
-OPT:fold_unsigned_relops=on -OPT:malloc_alg=1
-CG:load_exe=0 -fno-exceptions -m32 -HP:bdt=2m

453.povray: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -INLINE:aggressive=on

Fortran benchmarks:

410.bwaves: -march=barcelona -mso -O3 -OPT:Ofast -OPT:treeheight=on
-LNO:blocking=off -LNO:prefetch_ahead=5
-LNO:ignore_feedback=off -WOPT:aggstr=0 -HP:bdt=2m:heap=2m
-CG:cmp_peep=on

416.gamess: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -LNO:fu=6 -LNO:blocking=0
-LNO:prefetch=0 -OPT:Ofast -OPT:ro=3 -OPT:unroll_size=256
-HP:bdt=2m:heap=2m

434.zeusmp: -march=barcelona -mso -Ofast -LNO:blocking=off
-LNO:interchange=off -OPT:treeheight=on -OPT:unroll_size=256
-CG:cmp_peep=on -GRA:prioritize_by_density=on -HP

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Peak Optimization Flags (Continued)

437.leslie3d: -march=barcelona -mso -Ofast -HP:bdt=2m:heap=2m

459.GemsFDTD: -march=barcelona -mso -Ofast -LNO:fission=2
-LNO:prefetch_ahead=1 -CG:load_exe=0 -CG:local_sched_alg=1
-HP

465.tonto: -march=barcelona -mso -Ofast
-OPT:alias=no_f90_pointer_alias -LNO:blocking=off
-CG:load_exe=1 -IPA:plimit=525 -HP

Benchmarks using both Fortran and C:

435.gromacs: -march=barcelona -mso -Ofast -OPT:rsqrt=2
-HP:bdt=2m:heap=2m

436.cactusADM: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -apo -LNO:prefetch_ahead=1
-HP:bdt=2m:heap=2m -LANG:heap_allocation_threshold=100

454.calculix: -march=barcelona -mso -Ofast -CG:load_exe=0
-CG:ptr_load_use=0 -CG:local_sched_alg=2 -CG:compute_to=on
-LNO:prefetch_ahead=30 -WOPT:unroll=2
-GRA:optimize_boundary=on -HP:bdt=2m:heap=2m

481.wrf: -march=barcelona -mso -Ofast -LNO:blocking=off
-LNO:prefetch_ahead=10 -LANG:copyinout=off
-IPA:callee_limit=5000 -GRA:prioritize_by_density=on -m3dnow
-HP

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/x86-open64-4.2.3-flags-revA.html>

<http://www.spec.org/cpu2006/flags/amd-platform-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/x86-open64-4.2.3-flags-revA.xml>

<http://www.spec.org/cpu2006/flags/amd-platform-revA.xml>

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For other inquiries, please contact webmaster@spec.org.

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