



SPEC® CFP2006 Result

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Supermicro

Motherboard X8DAE (Intel Xeon X5677, 3.46 GHz)

SPECfp®_rate2006 = 221

CPU2006 license: 001176

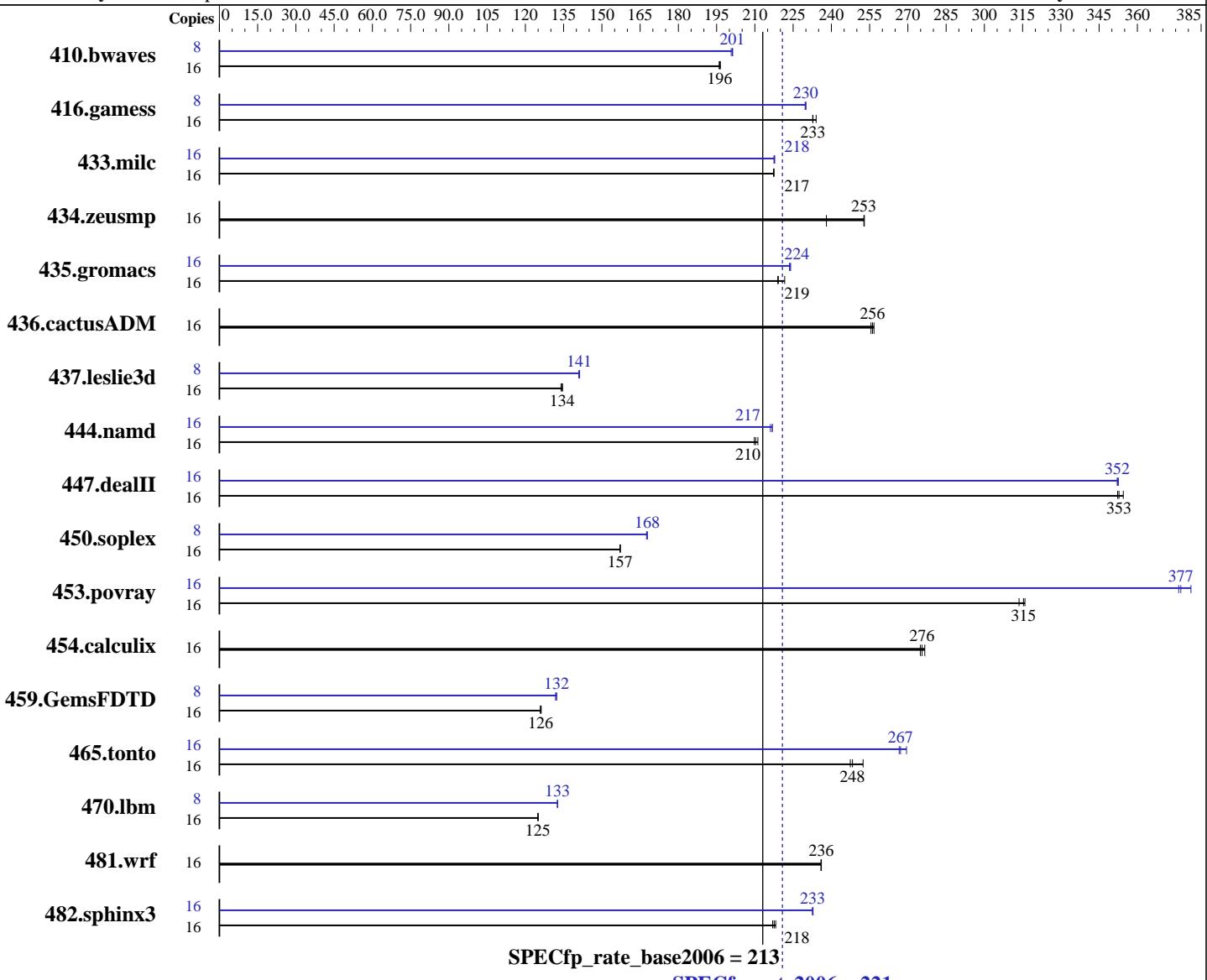
Test date: May-2010

Test sponsor: Supermicro

Hardware Availability: Mar-2010

Tested by: Supermicro

Software Availability: Jan-2010



Hardware		Software	
CPU Name:	Intel Xeon X5677	Operating System:	SUSE Linux Enterprise Server 11 (x86_64)
CPU Characteristics:	Intel Turbo Boost Technology up to 3.73 GHz	Compiler:	Kernel 2.6.27.19-5-default
CPU MHz:	3466	Auto Parallel:	Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1
FPU:	Integrated	File System:	Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core	System State:	No
CPU(s) orderable:	1,2 chips		ReiserFS
Primary Cache:	32 KB I + 32 KB D on chip per core		Run level 3 (multi-user)
Secondary Cache:	256 KB I+D on chip per core	Continued on next page	

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L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12 x 4 GB DDR3-1333 RDIMM, ECC, CL9)
 Disk Subsystem: 1 x 1000 GB SATA II, 7200 RPM
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	1107	196	1108	196	1109	196	8	542	201	540	201	540	201
416.gamess	16	1346	233	1339	234	1346	233	8	681	230	681	230	682	230
433.milc	16	676	217	675	217	676	217	16	674	218	675	218	675	217
434.zeusmp	16	612	238	576	253	576	253	16	612	238	576	253	576	253
435.gromacs	16	522	219	521	219	515	222	16	511	224	510	224	511	224
436.cactusADM	16	748	256	747	256	745	257	16	748	256	747	256	745	257
437.leslie3d	16	1123	134	1118	135	1119	134	8	532	141	533	141	534	141
444.namd	16	612	210	608	211	610	210	16	594	216	592	217	592	217
447.dealII	16	519	353	520	352	516	354	16	520	352	520	352	519	353
450.soplex	16	849	157	849	157	849	157	8	398	168	398	168	398	168
453.povray	16	270	315	269	316	271	314	16	226	377	223	381	226	376
454.calculix	16	477	277	479	276	480	275	16	477	277	479	276	480	275
459.GemsFDTD	16	1349	126	1348	126	1345	126	8	644	132	642	132	642	132
465.tonto	16	624	252	636	247	634	248	16	589	267	584	269	591	267
470.lbm	16	1758	125	1760	125	1760	125	8	829	133	829	133	829	133
481.wrf	16	758	236	757	236	757	236	16	758	236	757	236	757	236
482.sphinx3	16	1429	218	1433	218	1437	217	16	1339	233	1341	233	1340	233

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
 numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

Platform Notes

Fan speed set to Full Speed in BIOS Setup.
 As tested, the system used a Supermicro
 PWS-865-PQ power supply and 2 SNK-P0037 heatsinks, as well as
 2 FAN-0074L4 and 2 FAN-0077L4 cooling fans.



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General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:

 icc -m64

C++ benchmarks:

 icpc -m64

Fortran benchmarks:

 ifort -m64

Benchmarks using both Fortran and C:

 icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

 -xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

 -xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:

 -xSSE4.2 -ipo -O3 -no-prec-div -static

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

433.milc: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -opt-prefetch

470.lbm: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -ansi-alias -auto-ilp32

482.sphinx3: -xsse4 .2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep-

450.soplex: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xsse4 .2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xsse4 .2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0

465.tonto: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

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Peak Optimization Flags (Continued)

435.gromacs: -xSSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml>

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For other inquiries, please contact webmaster@spec.org.

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