SPEC® CINT2006 Result
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Fujitsu

PRIMERGY BX920 S2, Intel Xeon E5620, 2.40 GHz

$SPECint®\_rate2006 =$ 113

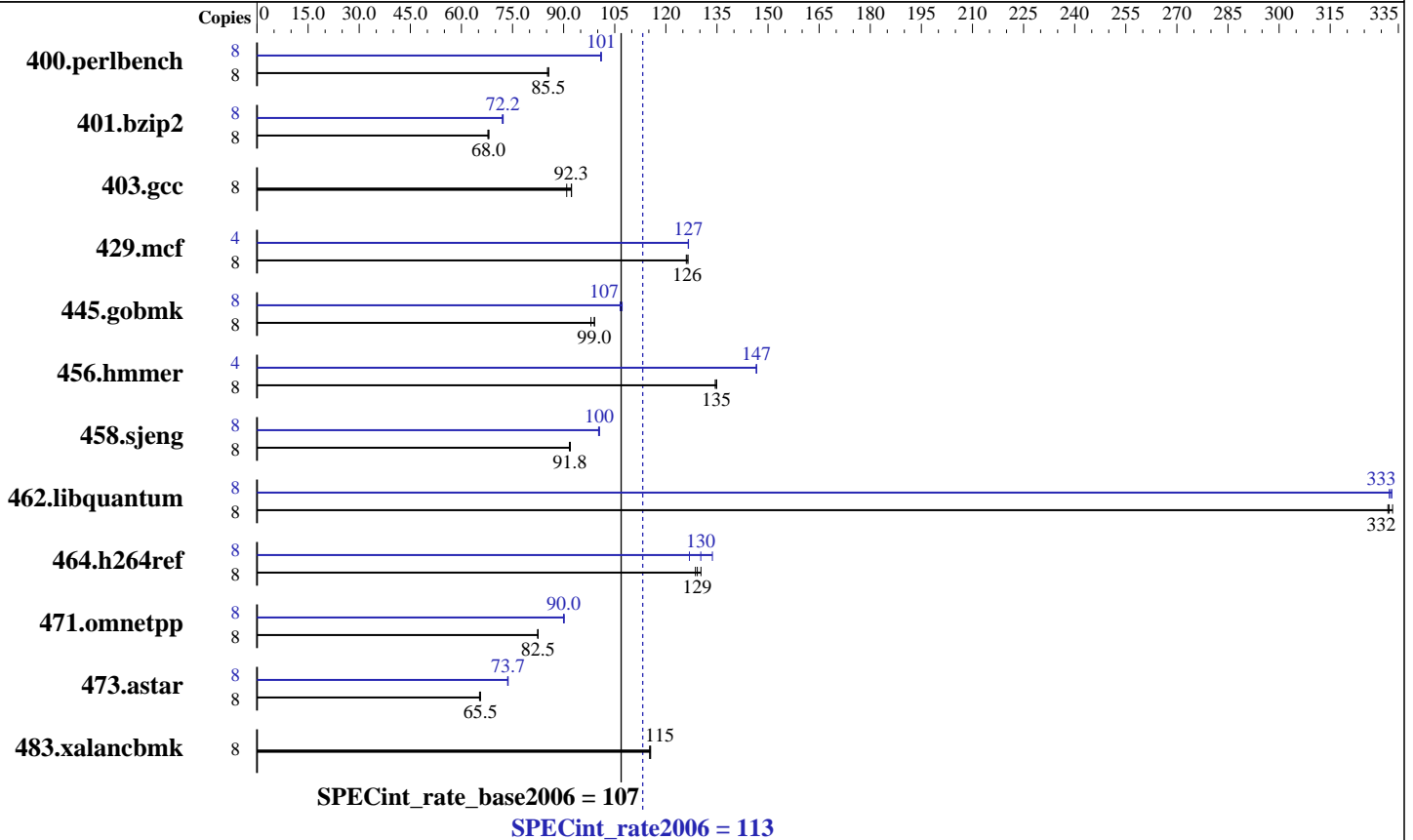$SPECint\_rate\_base2006 =$ 107

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Apr-2010
Hardware Availability: May-2010
Software Availability: Jan-2010

SPECint_rate_base2006 = 107

SPECint_rate2006 = 113

Hardware

CPU Name:	Intel Xeon E5620
CPU Characteristics:	Intel Turbo Boost Technology up to 2.67 GHz
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (3x8 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)
Disk Subsystem:	1 x SSD, 64 GB
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel:	No
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	913	85.6	**914**	**85.5**	917	85.3	8	**774**	**101**	775	101	773	101
401.bzip2	8	1139	67.8	1134	68.1	**1136**	**68.0**	8	**1070**	**72.2**	1068	72.3	1072	72.0
403.gcc	8	698	92.3	708	90.9	**698**	**92.3**	8	698	92.3	708	90.9	**698**	**92.3**
429.mcf	8	579	126	577	127	**578**	**126**	4	288	127	**288**	**127**	288	127
445.gobmk	8	**848**	**99.0**	856	98.0	847	99.1	8	783	107	**787**	**107**	787	107
456.hmmer	8	**554**	**135**	555	134	553	135	4	254	147	**255**	**147**	255	147
458.sjeng	8	1056	91.7	1052	92.0	**1054**	**91.8**	8	**964**	**100**	966	100	963	101
462.libquantum	8	**499**	**332**	499	332	497	333	8	**498**	**333**	499	332	498	333
464.h264ref	8	**1370**	**129**	1359	130	1376	129	8	1325	134	1395	127	**1359**	**130**
471.omnetpp	8	607	82.4	**606**	**82.5**	606	82.5	8	554	90.2	**556**	**90.0**	556	90.0
473.astar	8	860	65.3	**857**	**65.5**	856	65.6	8	762	73.7	**762**	**73.7**	764	73.6
483.xalancbmk	8	479	115	**478**	**115**	478	116	8	479	115	**478**	**115**	478	116

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
    The config file option 'submit' was used.
     numactl was used to bind copies to the cores
```

Operating System Notes

```
     'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
    The system automatically configures the memory to run at 1067 MHz.
    BIOS configuration:
    Data Reuse Optimization = Disable
    Turbo Boost Technology: Performance/Power Setting = Traditional
```

General Notes

```
    For information about Fujitsu please visit: http://www.fujitsu.com
```

Base Compiler Invocation

C benchmarks:
```
     icc  -m32
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
    icpc -m32

Base Portability Flags

        400.perlbench: -DSPEC_CPU_LINUX_IA32
        462.libquantum: -DSPEC_CPU_LINUX
        483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

        401.bzip2: icc -m64

      456.hmmer: icc -m64

        458.sjeng: icc -m64

    462.libquantum: icc -m64

C++ benchmarks (except as noted below):
    icpc -m32

        473.astar: icpc -m64

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Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
    401.bzip2: -DSPEC_CPU_LP64
   456.hmmer: -DSPEC_CPU_LP64
    458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
    473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -ansi-alias

    401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

      403.gcc: basepeak = yes

      429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

    445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
               -ipo -no-prec-div -ansi-alias

   456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
              -ansi-alias -auto-ilp32

    458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
                -opt-prefetch

   464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
   471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
                -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
```

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Peak Optimization Flags (Continued)

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.xml

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