SPEC® CINT2006 Result
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Oracle Corporation

$SPECint^{®}\_rate2006 =$ 201

Sun Netra X4270 (Intel Xeon L5518 2.13GHz)
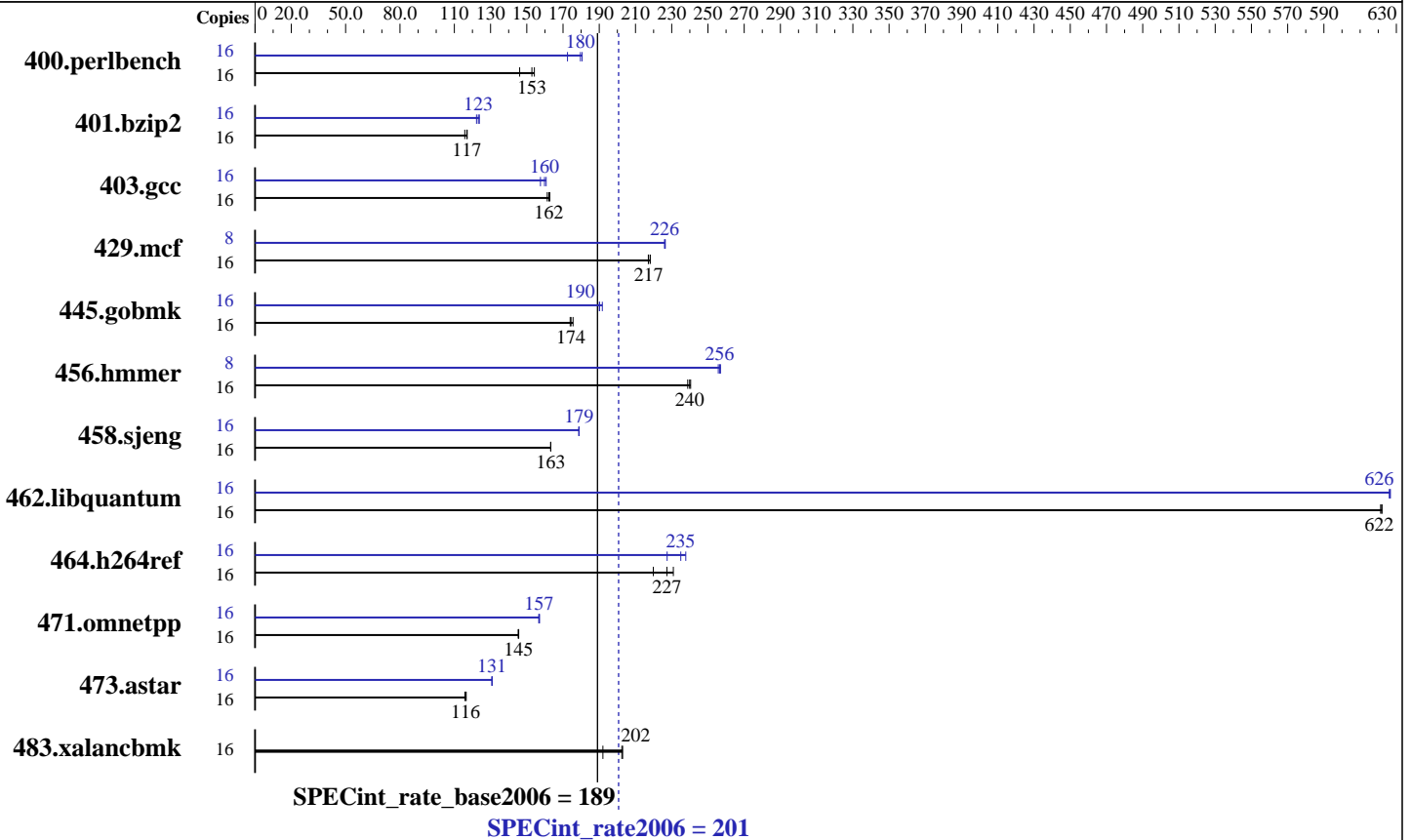
$SPECint\_rate\_base2006 =$ 189

CPU2006 license: 6
Test sponsor: Oracle Corporation
Tested by: Oracle Corporation

Test date: Apr-2010
Hardware Availability: May-2010
Software Availability: Dec-2009

Copies 0 20.0 50.0 80.0 110 130 150 170 190 210 230 250 270 290 310 330 350 370 390 410 430 450 470 490 510 530 550 570 590 630

400.perlbench 16 180 16 153

401.bzip2 16 123 16 117

403.gcc 16 160 16 162

429.mcf 8 226 16 217

445.gobmk 16 190 16 174

456.hmmer 8 256 16 240

458.sjeng 16 179 16 163

462.libquantum 16 626 16 622

464.h264ref 16 235 16 227

471.omnetpp 16 157 16 145

473.astar 16 131 16 116

483.xalancbmk 16 202

SPECint_rate_base2006 = 189

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Hardware

CPU Name: Intel Xeon L5518
CPU Characteristics: Intel Turbo Boost Technology up to 2.37 GHz
CPU MHz: 2133
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1 or 2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (6x4 GB DDR3-1333, 2 Rank, CL9 running at 1066 MHz)
Disk Subsystem: 1 x 300 GB, SAS, 10000 RPM
Other Hardware: None

Software

Operating System: Oracle Enterprise Linux Server release 5.4
Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1
Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel: No
File System: ReiserFS
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V8.1
Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	1070	146	**1023**	**153**	1014	154	16	906	172	**870**	**180**	866	181
401.bzip2	16	1334	116	**1321**	**117**	1319	117	16	1263	122	**1253**	**123**	1247	124
403.gcc	16	792	163	**794**	**162**	799	161	16	**806**	**160**	818	158	801	161
429.mcf	16	669	218	672	217	**672**	**217**	8	323	226	322	226	**323**	**226**
445.gobmk	16	965	174	956	176	**962**	**174**	16	876	192	883	190	**883**	**190**
456.hmmer	16	621	240	625	239	**622**	**240**	8	292	256	**291**	**256**	291	257
458.sjeng	16	1186	163	1186	163	**1186**	**163**	16	1083	179	**1083**	**179**	1082	179
462.libquantum	16	**533**	**622**	534	621	533	622	16	529	627	**529**	**626**	530	626
464.h264ref	16	1534	231	**1557**	**227**	1611	220	16	1489	238	**1507**	**235**	1557	227
471.omnetpp	16	688	145	687	146	**688**	**145**	16	**638**	**157**	637	157	639	157
473.astar	16	**967**	**116**	964	117	970	116	16	859	131	858	131	**859**	**131**
483.xalancbmk	16	575	192	544	203	**545**	**202**	16	575	192	544	203	**545**	**202**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Base Compiler Invocation

C benchmarks:
```
icc -m32
```

C++ benchmarks:
```
icpc -m32
```

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
```

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Base Optimization Flags (Continued)

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
```

Base Other Flags

C benchmarks:

> 403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m32
```

> 401.bzip2: `icc -m64`

> 456.hmmer: `icc -m64`

> 458.sjeng: `icc -m64`

> 462.libquantum: `icc -m64`

C++ benchmarks (except as noted below):
```
icpc -m32
```

> 473.astar: `icpc -m64`

Peak Portability Flags

> 400.perlbench: `-DSPEC_CPU_LINUX_IA32`
> 401.bzip2: `-DSPEC_CPU_LP64`
> 456.hmmer: `-DSPEC_CPU_LP64`
> 458.sjeng: `-DSPEC_CPU_LP64`
> 462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
> 473.astar: `-DSPEC_CPU_LP64`
> 483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

Continued on next page

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Peak Optimization Flags (Continued)

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -ansi-alias`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32`

403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div -static`

429.mcf: `-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2`
`-ipo -no-prec-div -ansi-alias`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2`
`-ansi-alias -auto-ilp32`

458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -unroll4 -auto-ilp32`

462.libquantum: `-xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32`
`-opt-prefetch`

464.h264ref: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap`

473.astar: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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