



# SPEC® CFP2006 Result

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Dell Inc.

**SPECfp®\_rate2006 = 139**

PowerEdge R810 (Intel Xeon E7520, 1.87 GHz)

**SPECfp\_rate\_base2006 = 136**

CPU2006 license: 55

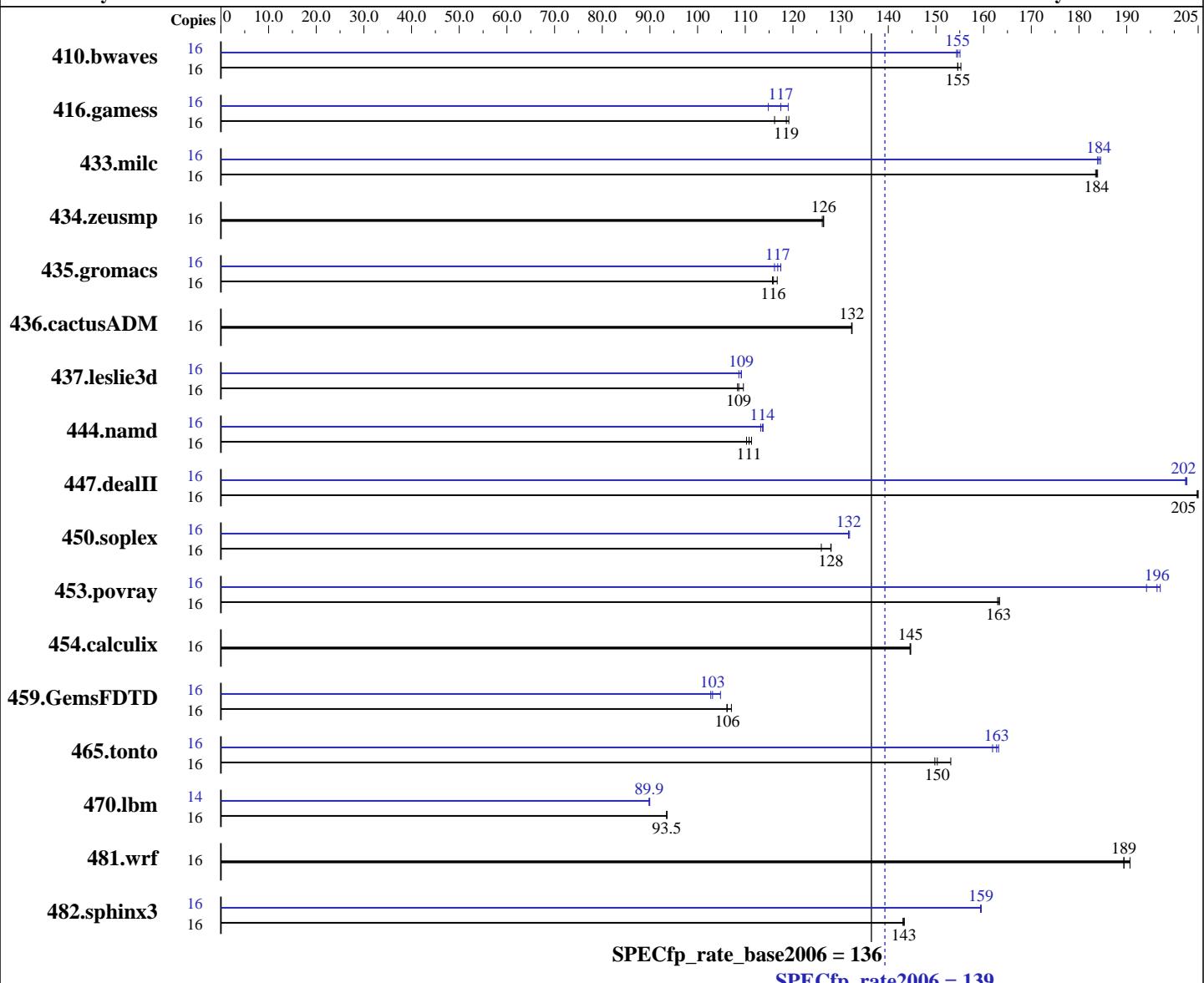
Test date: Mar-2010

Test sponsor: Dell Inc.

Hardware Availability: Mar-2010

Tested by: Dell Inc.

Software Availability: Dec-2009



## Hardware

CPU Name: Intel Xeon E7520  
CPU Characteristics:  
CPU MHz: 1866  
FPU: Integrated  
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
CPU(s) orderable: 2 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

## Software

Operating System: Red Hat Enterprise Linux 5 (x86\_64) Update 4 errata kernel (RHEL 5.4.z) kernel-2.6.18-164.9.1.el5.x86\_64  
Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l\_cproc\_p\_11.1.064, l\_cprof\_p\_11.1.064  
Auto Parallel: No  
File System: ext3

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L3 Cache: 18 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (32 x 4 GB DDR3-1066 QR RDIMM, CL7, ECC, downclocked to 800 MHz)  
 Disk Subsystem: 6 x 73 GB 15000 RPM SAS  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Binutils 2.18.50.0.7.20080502

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	1401	155	<u>1407</u>	<u>155</u>	1407	155	16	1409	154	1402	155	<u>1407</u>	<u>155</u>
416.gamess	16	<u>2641</u>	<u>119</u>	2629	119	2697	116	16	2728	115	<u>2667</u>	<u>117</u>	2632	119
433.milc	16	800	183	799	184	<u>800</u>	<u>184</u>	16	799	184	<u>798</u>	<u>184</u>	796	185
434.zeusmp	16	<u>1152</u>	<u>126</u>	1152	126	1154	126	16	<u>1152</u>	<u>126</u>	1152	126	1154	126
435.gromacs	16	<u>986</u>	<u>116</u>	987	116	979	117	16	984	116	973	117	<u>978</u>	<u>117</u>
436.cactusADM	16	1445	132	<u>1445</u>	<u>132</u>	1444	132	16	1445	132	<u>1445</u>	<u>132</u>	1444	132
437.leslie3d	16	<u>1385</u>	<u>109</u>	1388	108	1372	110	16	<u>1378</u>	<u>109</u>	1378	109	1384	109
444.namd	16	1164	110	1153	111	<u>1158</u>	<u>111</u>	16	1128	114	1133	113	<u>1129</u>	<u>114</u>
447.dealII	16	<u>893</u>	<u>205</u>	894	205	893	205	16	903	203	904	202	<u>904</u>	<u>202</u>
450.soplex	16	1042	128	1059	126	<u>1043</u>	<u>128</u>	16	1014	132	1012	132	<u>1013</u>	<u>132</u>
453.povray	16	<u>522</u>	<u>163</u>	522	163	521	163	16	432	197	<u>433</u>	<u>196</u>	438	194
454.calculix	16	<u>912</u>	<u>145</u>	912	145	913	145	16	<u>912</u>	<u>145</u>	912	145	913	145
459.GemsFDTD	16	1585	107	1599	106	<u>1598</u>	<u>106</u>	16	1620	105	1652	103	<u>1646</u>	<u>103</u>
465.tonto	16	<u>1048</u>	<u>150</u>	1028	153	1051	150	16	965	163	973	162	<u>968</u>	<u>163</u>
470.lbm	16	<u>2350</u>	<u>93.5</u>	2350	93.6	2351	93.5	14	<u>2141</u>	<u>89.9</u>	2141	89.8	2138	90.0
481.wrf	16	944	189	<u>943</u>	<u>189</u>	937	191	16	944	189	<u>943</u>	<u>189</u>	937	191
482.sphinx3	16	2179	143	<u>2177</u>	<u>143</u>	2175	143	16	1955	159	1957	159	<u>1956</u>	<u>159</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
 numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

vm.zone\_reclaim\_mode = 1 in /etc/sysctl.conf file  
 BIOS Settings:  
 Power Management = Maximum Performance (Default = Active Power Controller)



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## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static



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## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-fno-alias -opt-prefetch

470.lbm: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3 -ansi-alias -auto-ilp32

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## Peak Optimization Flags (Continued)

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
 -fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
 -unroll2 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
 -opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
 -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
 -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
 -unroll2 -Ob0

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
 -unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
 -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

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## Peak Optimization Flags (Continued)

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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