SPEC® CINT2006 Result
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Intel Corporation

Supermicro SuperServer 7046A-T (Intel Xeon X5677, 3.46 GHz)

SPECint®_rate2006 =   270
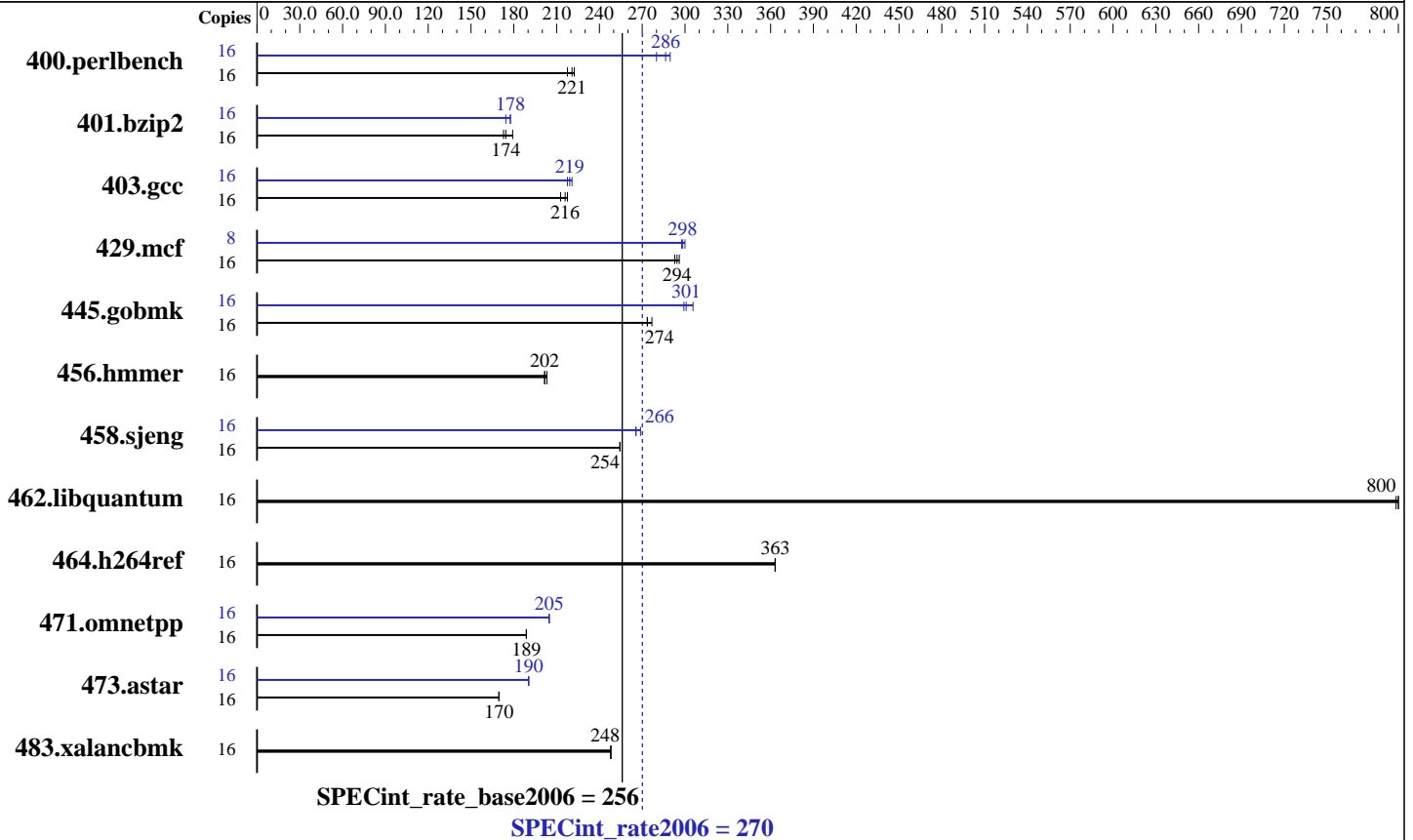
SPECint_rate_base2006 =   256

CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Mar-2010

Hardware Availability: Mar-2010

Software Availability: Nov-2009

SPECint_rate_base2006 = 256

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Hardware	**Software**

Hardware

CPU Name:	Intel Xeon X5677
CPU Characteristics:	Intel Turbo Boost Technology up to 3.73 GHz
CPU MHz:	3467
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (6x 4GB DDR3-1333, ECC, CL9)
Disk Subsystem:	1 x 300 GB SATA, 10000 RPM
Other Hardware:	None

Software

Operating System:	Windows 7 Enterprise (64-bit)
Compiler:	Intel C++ Compiler Professional 11.1 for IA32 Build 20090903 Package ID: w_cproc_p_11.1.045 Microsoft Visual Studio 2008 Professional SP1 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.1 from http://www.microquill.com/

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Results Table

Benchmark	Base Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	720	218	**707**	**221**	705	222	16	557	280	540	290	**545**	**286**
401.bzip2	16	**885**	**174**	865	179	897	173	16	889	174	869	178	**871**	**178**
403.gcc	16	**598**	**216**	593	218	606	213	16	**586**	**219**	583	221	593	218
429.mcf	16	498	293	494	296	**494**	**294**	8	245	298	**244**	**298**	243	300
445.gobmk	16	615	274	**612**	**274**	607	277	16	560	299	550	306	**559**	**301**
456.hmmer	16	737	203	740	202	**739**	**202**	16	737	203	740	202	**739**	**202**
458.sjeng	16	761	254	**762**	**254**	762	254	16	**727**	**266**	727	266	722	269
462.libquantum	16	415	798	**415**	**800**	415	800	16	415	798	**415**	**800**	415	800
464.h264ref	16	974	363	976	363	**976**	**363**	16	974	363	976	363	**976**	**363**
471.omnetpp	16	**531**	**189**	531	189	530	189	16	489	205	489	205	**489**	**205**
473.astar	16	661	170	**661**	**170**	661	170	16	591	190	**592**	**190**	592	190
483.xalancbmk	16	445	248	444	248	**445**	**248**	16	445	248	444	248	**445**	**248**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

Tested systems is used with tower case,
PC Power and Cooling dual 865W power supplys
System was configured with nVidia Quadro FX 370 discrete graphics card

Base Compiler Invocation

C benchmarks:
      icl -Qvc9 -Qstd=c99

C++ benchmarks:
      icl -Qvc9

Base Portability Flags

        403.gcc: -DSPEC_CPU_WIN32
     464.h264ref: -DWIN32 -DSPEC_CPU_NO_INTTYPES
   483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

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Base Optimization Flags

C benchmarks:
```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000
```

C++ benchmarks:
```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F512000000 shlW32M.lib          -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:
```
icl -Qvc9
```

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DWIN32 -DSPEC_CPU_NO_INTTYPES`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)`
`-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch`
`/F512000000 shlW32M.lib          -link /FORCE:MULTIPLE`

401.bzip2: `-QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)`
`-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias`
`/F512000000`

403.gcc: `-QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)`
`-Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000`

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Peak Optimization Flags (Continued)

429.mcf: `-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000`

445.gobmk: `-QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O2 -Qprec-div- -Qansi-alias /F512000000`

456.hmmer: `basepeak = yes`

458.sjeng: `-QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 /F512000000`

462.libquantum: `basepeak = yes`

464.h264ref: `basepeak = yes`

C++ benchmarks:

471.omnetpp: `-QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=block /F512000000 shlW32M.lib -link /FORCE:MULTIPLE`

473.astar: `-QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=routine /F512000000 shlW32M.lib -link /FORCE:MULTIPLE`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.00.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.00.xml`

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For other inquiries, please contact webmaster@spec.org.

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