



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu

### SPECint®\_rate2006 = 153

PRIMERGY TX300 S6, Intel Xeon X5677, 3.46 GHz

### SPECint\_rate\_base2006 = 145

CPU2006 license: 19

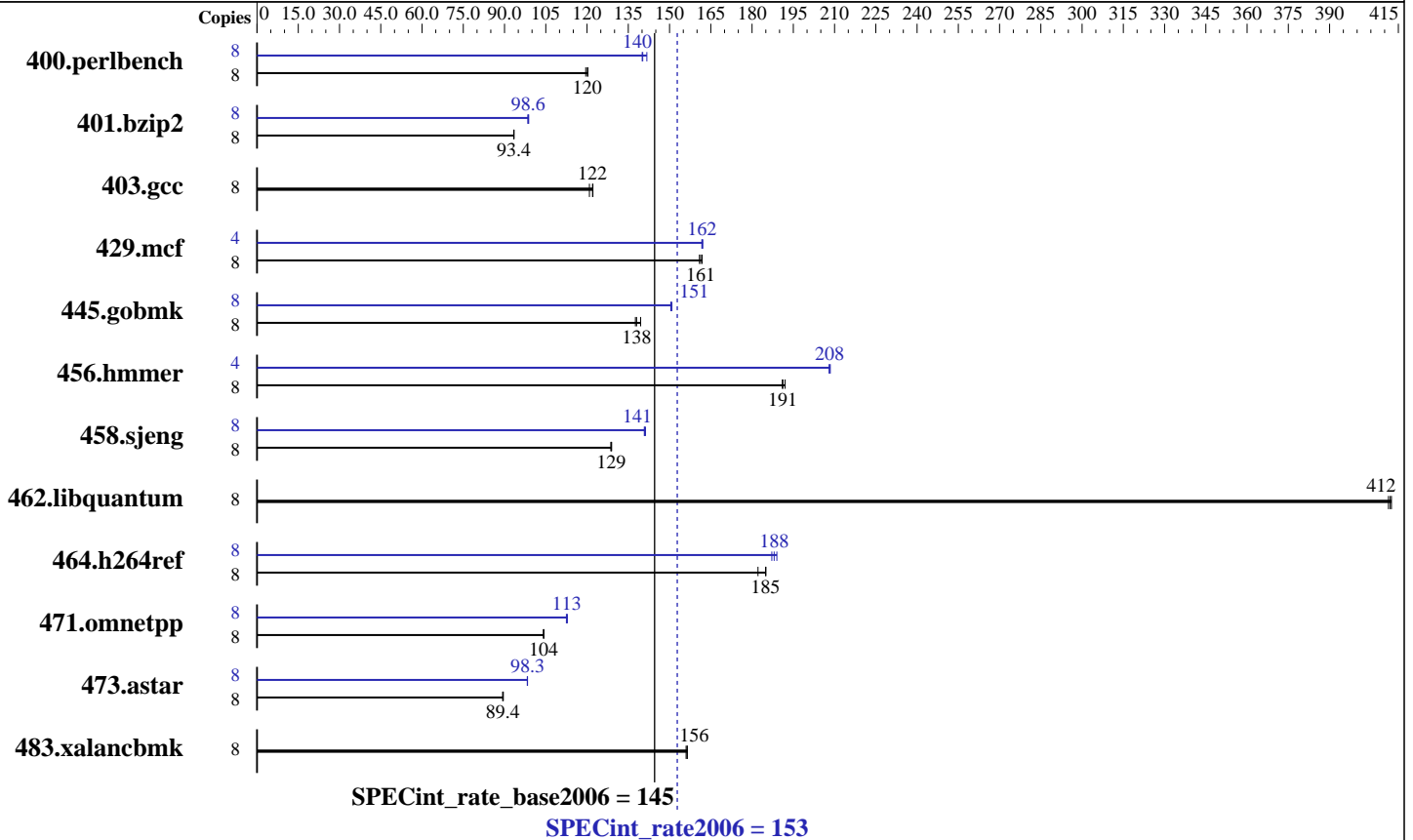
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Mar-2010

Hardware Availability: Apr-2010

Software Availability: Jan-2010



### Hardware

CPU Name: Intel Xeon X5677  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.73 GHz  
 CPU MHz: 3467  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (6x8 GB PC3-10600R, 2 rank, CL9-9-9, ECC)  
 Disk Subsystem: 1 x SATA, 160 GB, 5.4 krpm  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l\_cproc\_p\_11.1.064  
 Auto Parallel: No  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu

SPECint\_rate2006 = 153

PRIMERGY TX300 S6, Intel Xeon X5677, 3.46 GHz

SPECint\_rate\_base2006 = 145

CPU2006 license: 19  
Test sponsor: Fujitsu  
Tested by: Fujitsu

Test date: Mar-2010  
Hardware Availability: Apr-2010  
Software Availability: Jan-2010

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	654	120	650	120	<b>651</b>	<b>120</b>	8	<b>557</b>	<b>140</b>	558	140	551	142
401.bzip2	8	827	93.3	826	93.5	<b>827</b>	<b>93.4</b>	8	<b>783</b>	<b>98.6</b>	784	98.5	781	98.8
403.gcc	8	<b>528</b>	<b>122</b>	533	121	527	122	8	<b>528</b>	<b>122</b>	533	121	527	122
429.mcf	8	<b>452</b>	<b>161</b>	451	162	454	161	4	<b>225</b>	<b>162</b>	225	162	226	162
445.gobmk	8	610	138	602	139	<b>608</b>	<b>138</b>	8	556	151	<b>557</b>	<b>151</b>	558	150
456.hammer	8	391	191	<b>390</b>	<b>191</b>	389	192	4	<b>179</b>	<b>208</b>	179	208	179	208
458.sjeng	8	751	129	752	129	<b>752</b>	<b>129</b>	8	687	141	<b>687</b>	<b>141</b>	685	141
462.libquantum	8	403	411	<b>402</b>	<b>412</b>	402	412	8	403	411	<b>402</b>	<b>412</b>	402	412
464.h264ref	8	<b>957</b>	<b>185</b>	957	185	972	182	8	937	189	946	187	<b>941</b>	<b>188</b>
471.omnetpp	8	<b>480</b>	<b>104</b>	480	104	480	104	8	<b>444</b>	<b>113</b>	443	113	444	112
473.astar	8	<b>628</b>	<b>89.4</b>	629	89.4	627	89.6	8	<b>571</b>	<b>98.3</b>	572	98.3	571	98.4
483.xalancbmk	8	353	156	<b>353</b>	<b>156</b>	354	156	8	353	156	<b>353</b>	<b>156</b>	354	156

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

BIOS configuration:  
Data Reuse Optimization = Disable

## General Notes

This result was measured on the PRIMERGY TX300 S6. The PRIMERGY TX300 S6 and the PRIMERGY RX300 S6 are electronically equivalent.

For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc -m32

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Fujitsu**

**SPECint\_rate2006 = 153**

PRIMERGY TX300 S6, Intel Xeon X5677, 3.46 GHz

**SPECint\_rate\_base2006 = 145**

**CPU2006 license:** 19  
**Test sponsor:** Fujitsu  
**Tested by:** Fujitsu

**Test date:** Mar-2010  
**Hardware Availability:** Apr-2010  
**Software Availability:** Jan-2010

## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.icl1.1/libicl1.1-32bit -lsmarheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks (except as noted below):  
icpc -m32

473.astar: icpc -m64



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint\_rate2006 = 153

PRIMERGY TX300 S6, Intel Xeon X5677, 3.46 GHz

SPECint\_rate\_base2006 = 145

CPU2006 license: 19  
Test sponsor: Fujitsu  
Tested by: Fujitsu

Test date: Mar-2010  
Hardware Availability: Apr-2010  
Software Availability: Jan-2010

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias  
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32  
403.gcc: basepeak = yes  
429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias  
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32  
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32  
462.libquantum: basepeak = yes  
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint\_rate2006 = 153

PRIMERGY TX300 S6, Intel Xeon X5677, 3.46 GHz

SPECint\_rate\_base2006 = 145

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Mar-2010

Hardware Availability: Apr-2010

Software Availability: Jan-2010

## Peak Optimization Flags (Continued)

```
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
          -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
          -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
          -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64
```

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Wed Jul 23 09:53:04 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 14 April 2010.