SPEC® CINT2006 Result

SGI

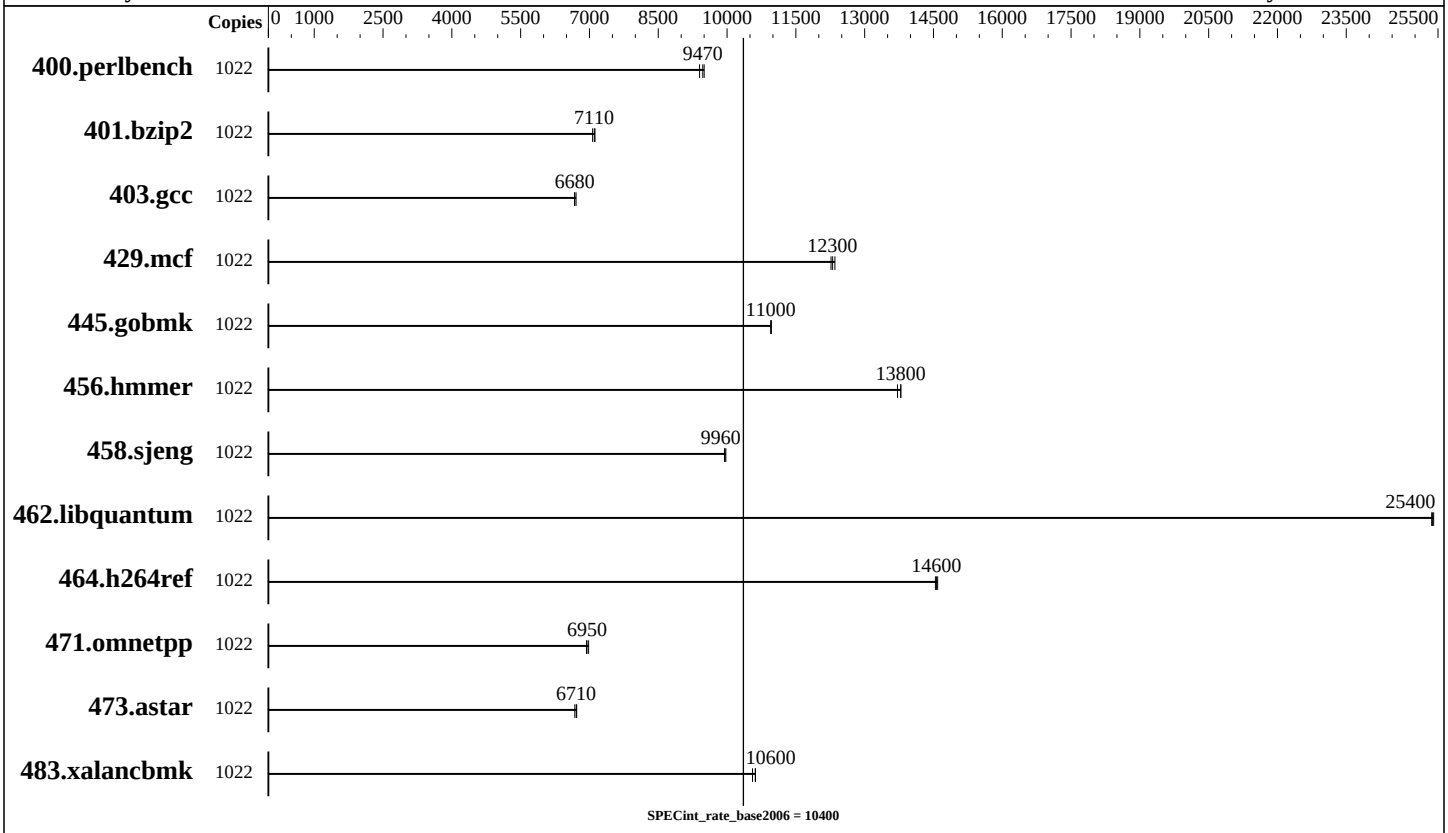
SGI Altix UV 1000 (Intel Xeon X7560, 2.26 GHz)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 10400

CPU2006 license: 4
Test sponsor: SGI
Tested by: SGI

Test date: Mar-2010
Hardware Availability: Jun-2010
Software Availability: Jun-2010



Hardware

CPU Name:	Intel Xeon X7560
CPU Characteristics:	Intel Turbo Boost Technology is not-enabled
CPU MHz:	2266
FPU:	Integrated
CPU(s) enabled:	512 cores, 64 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable:	2-256 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	24 MB I+D on chip per chip
Other Cache:	None
Memory:	2 TB (512 x 4GB dual-rank DDR3-1066 CL7 RDIMMs)
Disk Subsystem:	1 TB tmpfs
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.8-0.3.1-uv
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel:	No
File System:	tmpfs
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	1022	1062	9400	1051	9500	**1054**	**9470**							
401.bzip2	1022	1385	7120	**1388**	**7110**	1395	7070							
403.gcc	1022	1232	6680	1226	6710	**1232**	**6680**							
429.mcf	1022	755	12400	**758**	**12300**	760	12300							
445.gobmk	1022	979	11000	**978**	**11000**	978	11000							
456.hmmer	1022	691	13800	**692**	**13800**	695	13700							
458.sjeng	1022	1244	9940	1240	9970	**1242**	**9960**							
462.libquantum	1022	**834**	**25400**	835	25400	834	25400							
464.h264ref	1022	**1553**	**14600**	1550	14600	1555	14500							
471.omnetpp	1022	**919**	**6950**	915	6980	921	6930							
473.astar	1022	1074	6680	1067	6720	**1069**	**6710**							
483.xalancbmk	1022	664	10600	668	10600	**664**	**10600**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
numactl was used to bind copies to the cores
```

Operating System Notes

```
Tmpfs filesystem set up with:
  mkdir -p /mnt/shm
  mount -t tmpfs -o rw,mpol=interleave tmpfs /mnt/shm/
  The mpol=interleave option sets the NUMA memory allocation
  policy for all files to allocate from each node in turn.
```

Base Compiler Invocation

C benchmarks:

```
icc  -m32
```

C++ benchmarks:

```
icpc -m32
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

Continued on next page

Base Portability Flags (Continued)

462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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