



SPEC® CINT2006 Result

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Supermicro

SPECint®_rate2006 = 360

Motherboard X8DTE-F (Intel Xeon X5670, 2.93 GHz)

SPECint_rate_base2006 = 336

CPU2006 license: 001176

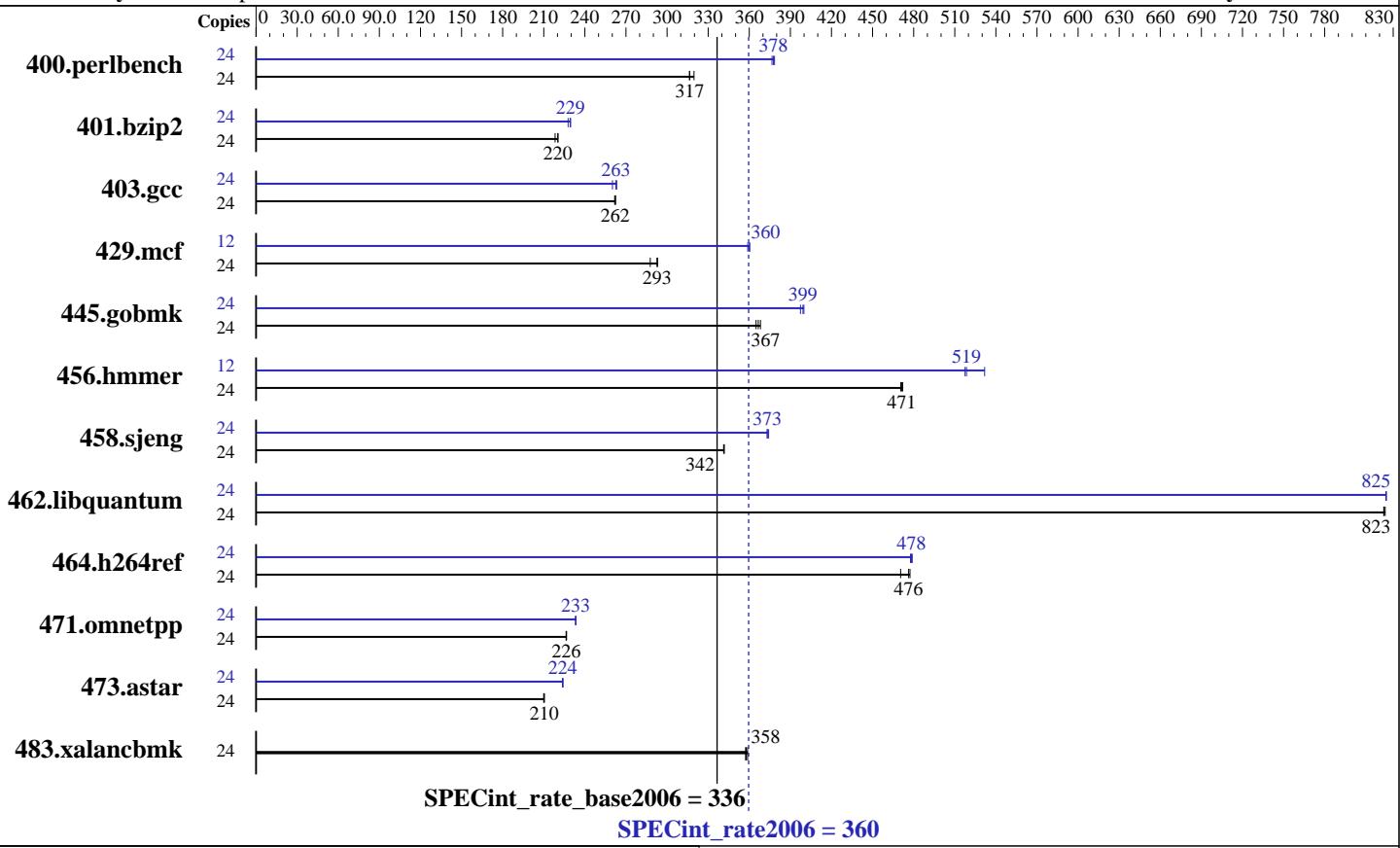
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Mar-2010

Hardware Availability: Mar-2010

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon X5670
CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz
CPU MHz: 2933
FPU: Integrated
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB DDR3-1333 RDIMM, ECC, CL9)
Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64)
Compiler: Kernel 2.6.27.19-5-default
Auto Parallel: Intel C++ Professional Compiler for IA32 and
File System: Intel 64, Version 11.1
System State: Build 20091130 Package ID: l_cproc_p_11.1.064
Base Pointers: No
Peak Pointers: ReiserFS
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Peak Pointers: Microquill SmartHeap V8.1
Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	741	317	733	320	741	316	24	621	378	620	378	623	377
401.bzip2	24	1061	218	1052	220	1051	220	24	1017	228	1009	230	1010	229
403.gcc	24	737	262	738	262	736	262	24	736	263	743	260	734	263
429.mcf	24	761	288	748	293	747	293	12	304	360	304	360	305	359
445.gobmk	24	687	367	690	365	684	368	24	631	399	630	400	634	397
456.hmmer	24	476	471	474	472	475	471	12	211	532	216	519	216	517
458.sjeng	24	850	342	851	341	850	342	24	776	374	778	373	778	373
462.libquantum	24	603	824	604	823	604	823	24	603	825	603	825	603	825
464.h264ref	24	1113	477	1115	476	1129	470	24	1112	478	1109	479	1110	478
471.omnetpp	24	662	227	663	226	663	226	24	643	233	643	233	643	233
473.astar	24	802	210	801	210	801	210	24	752	224	752	224	753	224
483.xalancbmk	24	462	359	463	358	463	357	24	462	359	463	358	463	357

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Platform Notes

Fan speed set to Full Speed in BIOS Setup.
As tested, the system used a Supermicro
PWS-865-PQ power supply, 2 SNK-P0038P heatsinks,
and 2 FAN-0077L cooling fans.

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

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Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmpllr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
            -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
            -ipo -no-prec-div -ansi-alias

456.hmmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
             -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
            -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
                -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
              -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
              -L/home/cmpllr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
            -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
            -L/home/cmpllr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64
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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=__alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

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