



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Supermicro

(Test Sponsor: Intel Corporation)

Supermicro SuperServer 6026T-NTR+ (Intel Xeon X5650, 2.66 GHz)

SPECint®\_rate2006 = 343

SPECint\_rate\_base2006 = 320

CPU2006 license: 13

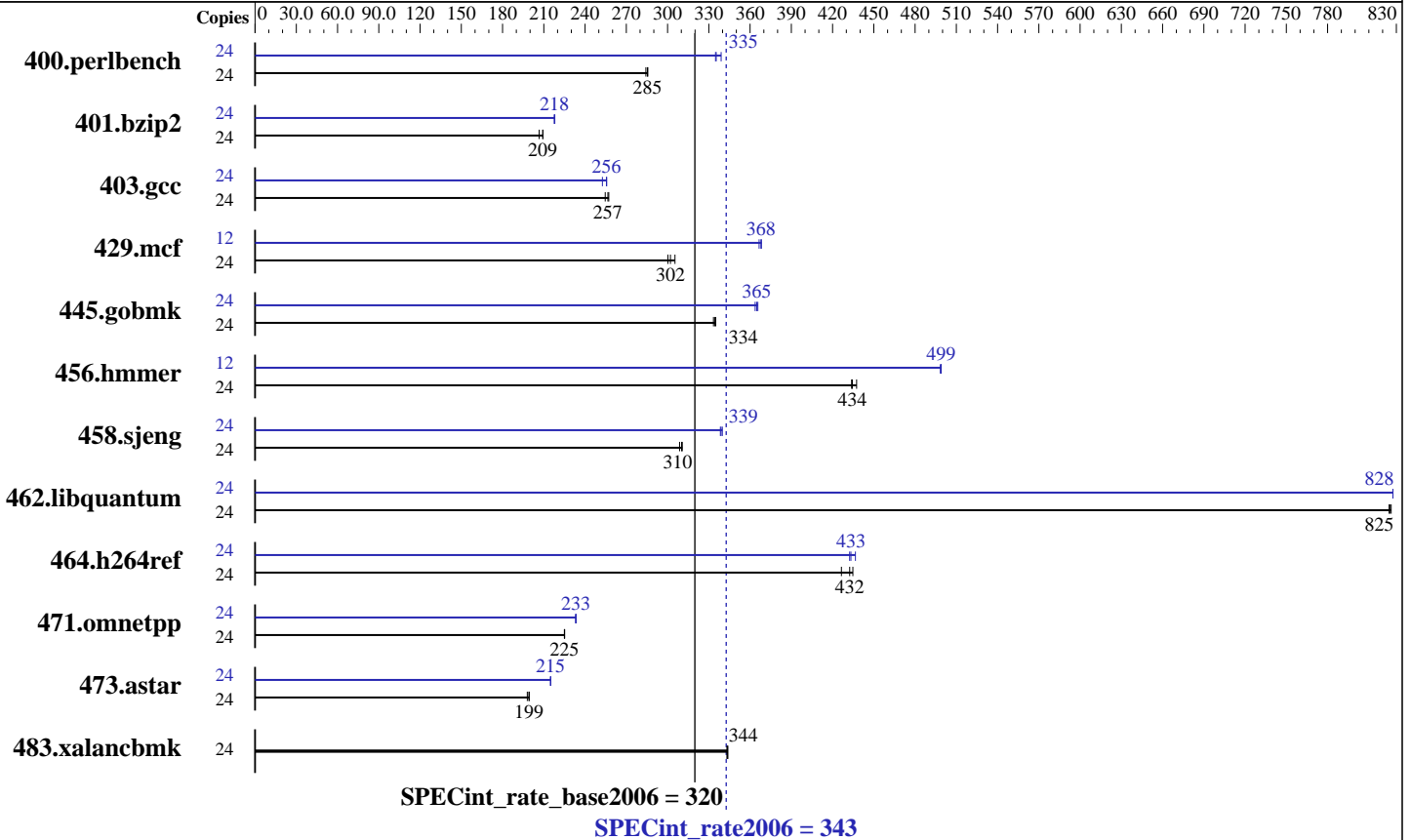
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Mar-2010

Hardware Availability: Mar-2010

Software Availability: Jan-2010



### Hardware

CPU Name: Intel Xeon X5650  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.06 GHz  
 CPU MHz: 2666  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4GB DDR3-1333, CL9)  
 Disk Subsystem: 1 x 150 GB SATA, 10000RPM  
 Other Hardware: None

### Software

Operating System: SuSe Linux SLES11  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1  
 Build 20091130 Package ID: l\_cproc\_p\_11.1.064  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1  
 Binutils 2.18.50.0.7.20080502



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Supermicro

(Test Sponsor: Intel Corporation)

Supermicro SuperServer 6026T-NTR+ (Intel Xeon X5650, 2.66 GHz)

SPECint\_rate2006 = 343

SPECint\_rate\_base2006 = 320

CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Mar-2010

Hardware Availability: Mar-2010

Software Availability: Jan-2010

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	<u>822</u>	<u>285</u>	825	284	821	286	24	<u>699</u>	<u>335</u>	700	335	692	339
401.bzip2	24	1121	207	1107	209	<u>1107</u>	<u>209</u>	24	1064	218	<u>1063</u>	<u>218</u>	1062	218
403.gcc	24	<u>753</u>	<u>257</u>	751	257	759	255	24	<u>756</u>	<u>256</u>	755	256	765	253
429.mcf	24	729	300	717	305	<u>725</u>	<u>302</u>	12	299	367	297	368	<u>297</u>	<u>368</u>
445.gobmk	24	<u>753</u>	<u>334</u>	755	333	751	335	24	689	366	692	364	<u>690</u>	<u>365</u>
456.hammer	24	<u>515</u>	<u>434</u>	516	434	512	437	12	<u>224</u>	<u>499</u>	224	499	225	498
458.sjeng	24	941	309	<u>936</u>	<u>310</u>	935	311	24	858	338	855	340	<u>857</u>	<u>339</u>
462.libquantum	24	<u>603</u>	<u>825</u>	603	825	602	826	24	601	828	601	827	<u>601</u>	<u>828</u>
464.h264ref	24	1222	435	<u>1228</u>	<u>432</u>	1245	426	24	<u>1226</u>	<u>433</u>	1216	437	1228	432
471.omnetpp	24	667	225	666	225	<u>667</u>	<u>225</u>	24	643	233	<u>643</u>	<u>233</u>	644	233
473.astar	24	<u>846</u>	<u>199</u>	845	199	851	198	24	<u>784</u>	<u>215</u>	783	215	784	215
483.xalanbmk	24	482	344	<u>482</u>	<u>344</u>	483	343	24	482	344	<u>482</u>	<u>344</u>	483	343

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## General Notes

Bios settings:  
LRU hint: Disabled  
Fan Speed: Full speed

## Base Compiler Invocation

C benchmarks:  
icc -m32  
  
C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalanbmk: -DSPEC\_CPU\_LINUX



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Supermicro

(Test Sponsor: Intel Corporation)

Supermicro SuperServer 6026T-NTR+ (Intel Xeon X5650, 2.66 GHz)

SPECint\_rate2006 = 343

SPECint\_rate\_base2006 = 320

CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Mar-2010

Hardware Availability: Mar-2010

Software Availability: Jan-2010

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -DSPEC\_CPU\_LINUX



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Supermicro

(Test Sponsor: Intel Corporation)

Supermicro SuperServer 6026T-NTR+ (Intel Xeon X5650, 2.66 GHz)

SPECint\_rate2006 = 343

SPECint\_rate\_base2006 = 320

CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Mar-2010

Hardware Availability: Mar-2010

Software Availability: Jan-2010

## Peak Optimization Flags

### C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32  
 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll2 -ansi-alias

### C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
 -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs  
 -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: basepeak = yes



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Supermicro

(Test Sponsor: Intel Corporation)

Supermicro SuperServer 6026T-NTR+ (Intel Xeon X5650, 2.66 GHz)

SPECint\_rate2006 = 343

SPECint\_rate\_base2006 = 320

CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Mar-2010

Hardware Availability: Mar-2010

Software Availability: Jan-2010

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revG.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revG.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Wed Jul 23 05:13:14 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 30 March 2010.