SPEC® CINT2006 Result
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Dell Inc.

Dell Precision T7500 (Intel Xeon X5677, 3.47 GHz)

$SPECint®\_rate2006 = 269$

$SPECint\_rate\_base2006 = 256$
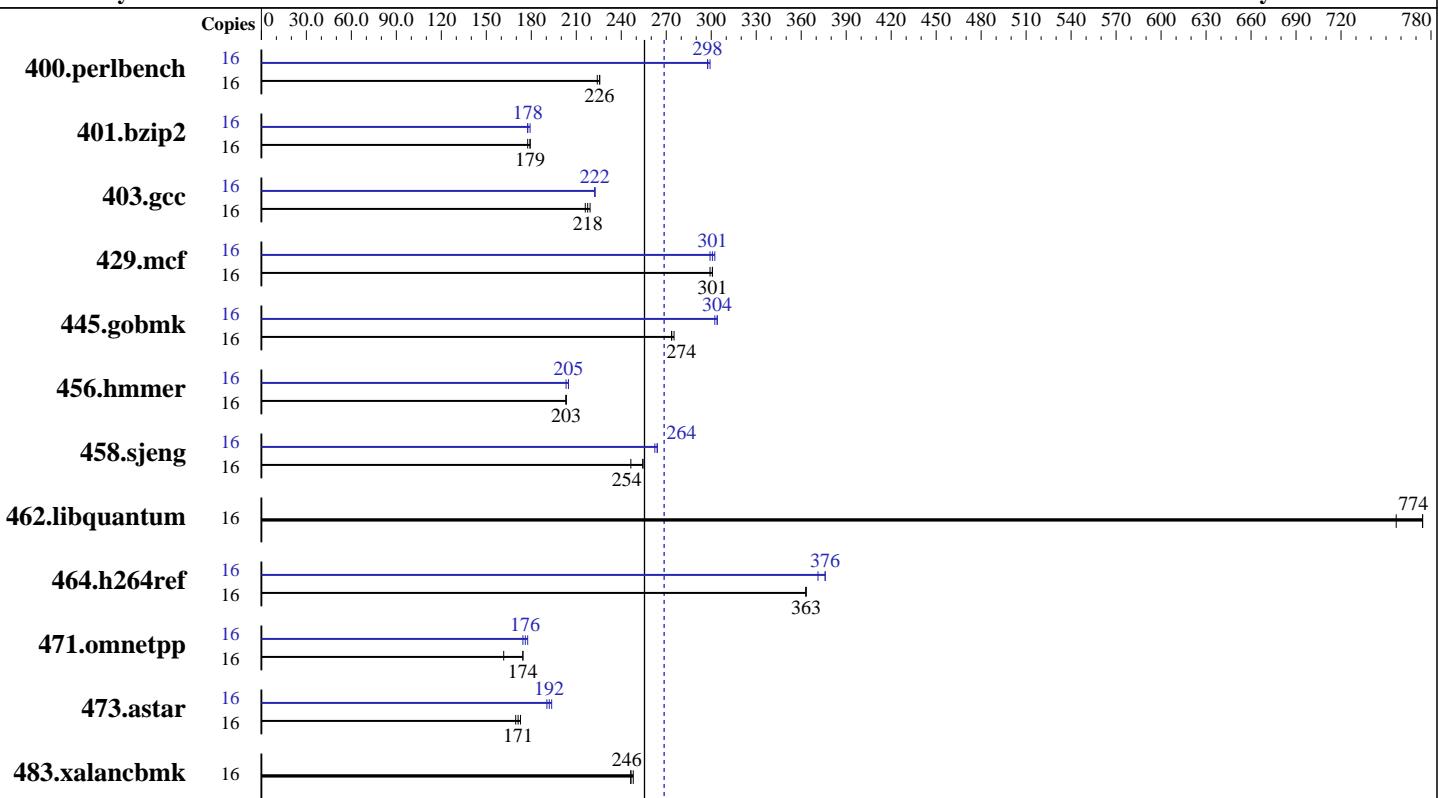
CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Feb-2010
Hardware Availability: Mar-2010
Software Availability: Dec-2009

Copies 0 30.0 60.0 90.0 120 150 180 210 240 270 300 330 360 390 420 450 480 510 540 570 600 630 660 690 720 780

Benchmark	Copies	Peak	Base
400.perlbench	16	298	226
401.bzip2	16	178	179
403.gcc	16	222	218
429.mcf	16	301	301
445.gobmk	16	304	274
456.hmmer	16	205	203
458.sjeng	16	264	254
462.libquantum	16		774
464.h264ref	16	376	363
471.omnetpp	16	176	174
473.astar	16	192	171
483.xalancbmk	16		246

SPECint_rate_base2006 = 256

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Hardware

CPU Name:	Intel Xeon X5677
CPU Characteristics:	Intel Turbo Boost Technology up to 3.73 GHz
CPU MHz:	3466
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (12x4 GB PC3-10600R)
Disk Subsystem:	1 x 160 GB SATA 7200 RPM
Other Hardware:	None

Software

Operating System:	Windows 7 Professional (64-bit)
Compiler:	Intel C++ Compiler Professional for IA-32, Version 11.1 Build 20091130 Package ID: w_cproc_p_11.1.054 Microsoft Visual Studio 2008 SP1
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill SmartHeap Library 8.1

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Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	699	224	694	226	**695**	**226**	16	**524**	**298**	525	298	523	299
401.bzip2	16	866	178	**862**	**179**	861	179	16	870	178	861	179	**868**	**178**
403.gcc	16	**590**	**218**	597	216	587	219	16	580	222	**579**	**222**	579	222
429.mcf	16	487	299	**486**	**301**	485	301	16	**485**	**301**	483	302	487	299
445.gobmk	16	611	275	612	274	**612**	**274**	16	554	302	**553**	**304**	553	304
456.hmmer	16	**735**	**203**	734	203	736	203	16	**731**	**205**	732	203	730	205
458.sjeng	16	**762**	**254**	762	254	785	246	16	736	262	734	264	**735**	**264**
462.libquantum	16	**428**	**774**	438	757	428	774	16	**428**	**774**	438	757	428	774
464.h264ref	16	976	363	**975**	**363**	974	363	16	941	376	956	371	**942**	**376**
471.omnetpp	16	**575**	**174**	572	174	616	162	16	572	174	562	178	**566**	**176**
473.astar	16	648	173	**657**	**171**	660	170	16	**586**	**192**	582	194	590	190
483.xalancbmk	16	448	246	446	248	**448**	**246**	16	448	246	446	248	**448**	**246**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
    BIOS Settings
        Memory Node Interleaving: NUMA
                Hyper-Threading: ENABLE
```

General Notes

```
    Binaries were built on Windows Vista Business (64-bit)
```

Base Compiler Invocation

C benchmarks:
```
    icl -Qvc9 -Qstd=c99
```

C++ benchmarks:
```
    icl -Qvc9
```

Base Portability Flags

```
        403.gcc: -DSPEC_CPU_WIN32
      464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
    483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

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Base Optimization Flags

C benchmarks:
```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000
```

C++ benchmarks:
```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx_features
/F512000000 shlw32m.lib          -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:
```
icl -Qvc9
```

Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qprof_use(pass 2)
               -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
               /F512000000 shlw32m.lib          -link /FORCE:MULTIPLE

    401.bzip2: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qprof_use(pass 2)
               -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
               /F512000000

      403.gcc: -Qprof_gen(pass 1) -QxSSE4.2 -Qprof_use(pass 2) -Qipo -O3
               -Qprec-div- -Qopt-prefetch /F512000000
```

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Peak Optimization Flags (Continued)

```
  429.mcf: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch
           /F512000000

445.gobmk: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qprof_use(pass 2)
           -Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

456.hmmer: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
           /F512000000

458.sjeng: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qprof_use(pass 2)
           -Qipo -O3 -Qprec-div- -Qunroll4 /F512000000

462.libquantum: basepeak = yes

464.h264ref: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qprof_use(pass 2)
             -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000
```

C++ benchmarks:

```
471.omnetpp: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qprof_use(pass 2)
             -Qipo -O3 -Qprec-div- -Qansi-alias
             -Qopt-ra-region-strategy=block /F512000000 shlw32m.lib
                     -link /FORCE:MULTIPLE

  473.astar: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qprof_use(pass 2)
             -Qipo -O3 -Qprec-div- -Qansi-alias
             -Qopt-ra-region-strategy=routine /F512000000 shlw32m.lib
                     -link /FORCE:MULTIPLE

483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/dell.flags.ic11.1.win.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/dell.flags.ic11.1.win.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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