SPEC® CINT2006 Result

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Itautec
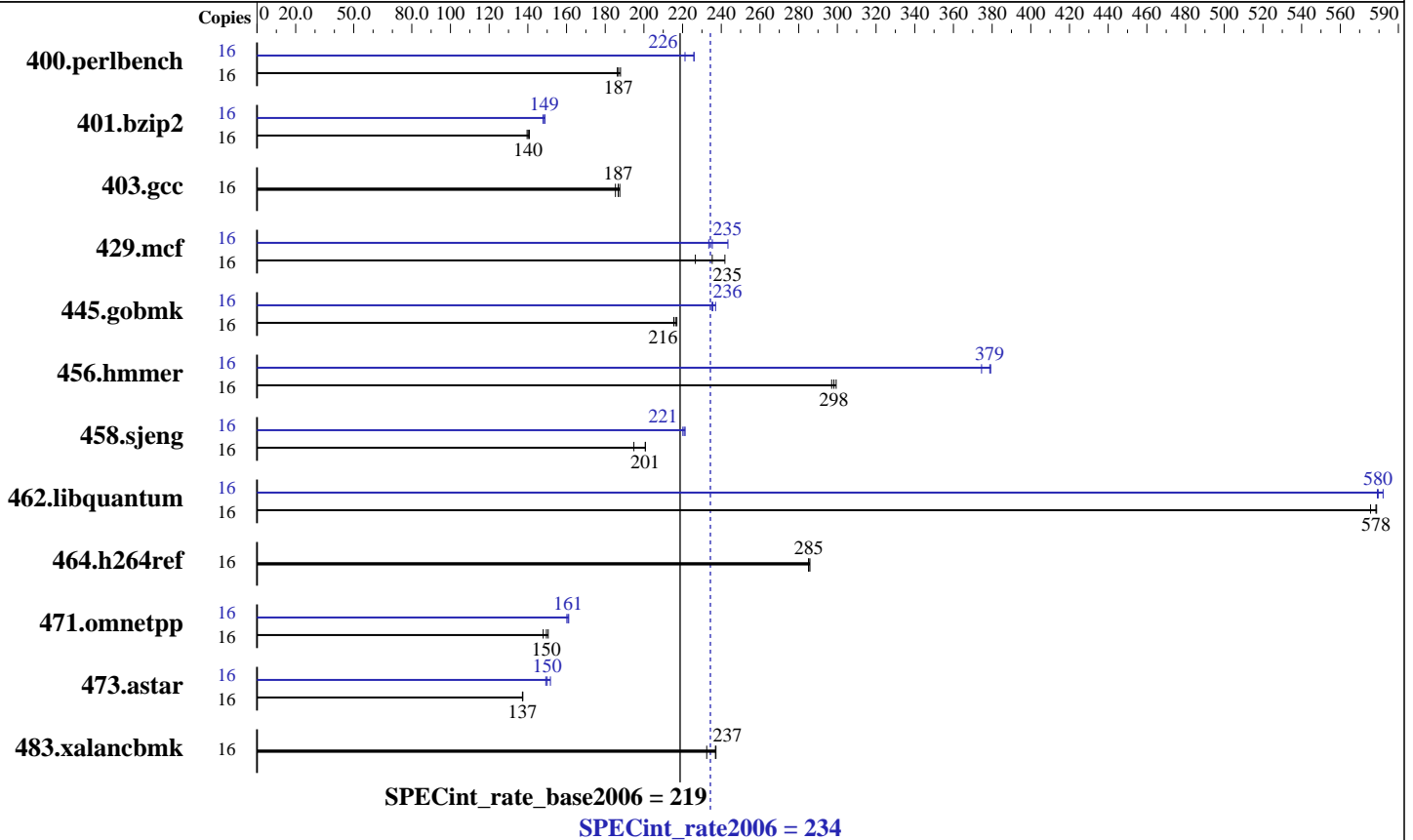
SPECint®_rate2006 = 234

Servidor Itautec LX203 (Intel Xeon E5640)

SPECint_rate_base2006 = 219

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Mar-2010
Hardware Availability: Apr-2010
Software Availability: Feb-2010

Copies — results graph

- 400.perlbench: 16 — 226; 16 — 187
- 401.bzip2: 16 — 149; 16 — 140
- 403.gcc: 16 — 187
- 429.mcf: 16 — 235; 16 — 235
- 445.gobmk: 16 — 236; 16 — 216
- 456.hmmer: 16 — 379; 16 — 298
- 458.sjeng: 16 — 221; 16 — 201
- 462.libquantum: 16 — 580; 16 — 578
- 464.h264ref: 16 — 285
- 471.omnetpp: 16 — 161; 16 — 150
- 473.astar: 16 — 150; 16 — 137
- 483.xalancbmk: 16 — 237

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Hardware

CPU Name:	Intel Xeon E5640
CPU Characteristics:	Intel Turbo Boost Technology up to 2.93 GHz
CPU MHz:	2660
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (6 x 4GB DDR3-1066, CL 7, ECC)
Disk Subsystem:	1 x 160 GB SATA-2, 7200RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-smp
Compiler:	Intel C++ Compiler 11.1 for Linux Build 20100203 Package ID: l_cproc_p_11.1.069
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1

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Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	832	188	839	186	**836**	**187**	16	**692**	**226**	692	226	707	221
401.bzip2	16	1097	141	**1101**	**140**	1106	140	16	1044	148	**1039**	**149**	1038	149
403.gcc	16	**690**	**187**	695	185	687	188	16	**690**	**187**	695	185	687	188
429.mcf	16	603	242	**620**	**235**	644	227	16	625	234	599	243	**620**	**235**
445.gobmk	16	773	217	779	215	**776**	**216**	16	708	237	**712**	**236**	713	235
456.hmmer	16	499	299	503	297	**501**	**298**	16	**394**	**379**	399	375	394	379
458.sjeng	16	994	195	**964**	**201**	964	201	16	875	221	879	220	**877**	**221**
462.libquantum	16	573	579	**573**	**578**	576	576	16	572	579	570	582	**572**	**580**
464.h264ref	16	**1241**	**285**	1239	286	1242	285	16	**1241**	**285**	1239	286	1242	285
471.omnetpp	16	**668**	**150**	664	151	676	148	16	621	161	**622**	**161**	624	160
473.astar	16	819	137	**818**	**137**	818	137	16	741	152	**749**	**150**	752	149
483.xalancbmk	16	475	232	465	237	**466**	**237**	16	475	232	465	237	**466**	**237**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

Operating System Notes

    'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.

General Notes

    This result was measured on the Servidor Itautec LX203.
    The Servidor Itautec LX213 and the Servidor Itautec LX203 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
```
icc -m32
```

C++ benchmarks:
```
icpc -m32
```

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Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc -m32
```

```
401.bzip2: icc -m64
```

```
456.hmmer: icc -m64
```

```
458.sjeng: icc -m64
```

```
462.libquantum: icc -m64
```

C++ benchmarks (except as noted below):
```
icpc -m32
```

```
473.astar: icpc -m64
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
```

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Peak Portability Flags (Continued)

```
    456.hmmer: -DSPEC_CPU_LP64
    458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
     473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
  400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -ansi-alias

     401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

       403.gcc: basepeak = yes

       429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

     445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
                 -ipo -no-prec-div -ansi-alias

     456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
                 -ansi-alias -auto-ilp32

     458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
                 -opt-prefetch

    464.h264ref: basepeak = yes
```

C++ benchmarks:

```
   471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
                 -L/opt/sh/SmartHeap_8.1/lib -lsmartheap

     473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
                 -L/opt/sh/SmartHeap_8.1/lib -lsmartheap64
```

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.1-linux64-revE.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.1-linux64-revE.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
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