



# SPEC<sup>®</sup> CINT2006 Result

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Itaotec

SPECint<sup>®</sup>\_rate2006 = 253

Servidor Itaotec MX223+ (Intel Xeon X5570)

SPECint\_rate\_base2006 = 235

CPU2006 license: 9001

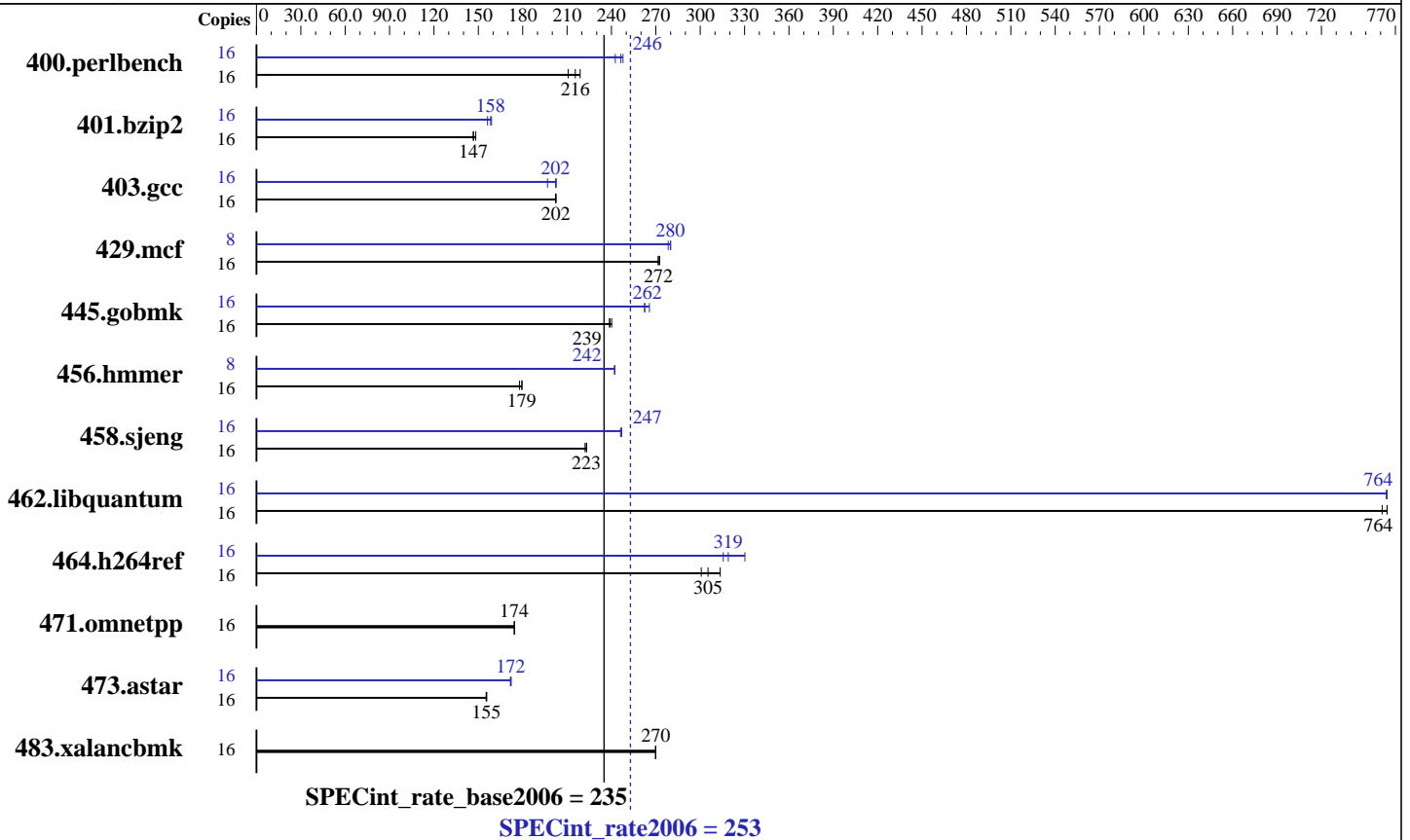
Test date: Feb-2010

Test sponsor: Itaotec

Hardware Availability: Sep-2009

Tested by: Itaotec

Software Availability: Feb-2009



## Hardware

CPU Name: Intel Xeon X5570  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz  
 CPU MHz: 2930  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (6 x 4GB DDR3-1333, CL 9, ECC)  
 Disk Subsystem: 1 x 160 GB SATA-2, 7200RPM  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2 with patch Linux kernel 20090119, Kernel 2.6.16.60-0.34-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l\_cproc\_p\_11.0.081  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	<u>725</u>	<u>216</u>	742	211	714	219	16	644	243	<u>634</u>	<u>246</u>	631	248
401.bzip2	16	1041	148	1054	147	<u>1052</u>	<u>147</u>	16	<u>975</u>	<u>158</u>	988	156	972	159
403.gcc	16	<u>637</u>	<u>202</u>	637	202	636	203	16	635	203	<u>637</u>	<u>202</u>	655	197
429.mcf	16	<u>537</u>	<u>272</u>	535	273	537	272	8	262	278	261	280	<u>261</u>	<u>280</u>
445.gobmk	16	699	240	703	239	<u>703</u>	<u>239</u>	16	632	266	640	262	<u>639</u>	<u>262</u>
456.hammer	16	<u>832</u>	<u>179</u>	840	178	832	179	8	308	242	<u>308</u>	<u>242</u>	308	242
458.sjeng	16	867	223	871	222	<u>868</u>	<u>223</u>	16	784	247	<u>784</u>	<u>247</u>	787	246
462.libquantum	16	436	761	<u>434</u>	<u>764</u>	434	764	16	<u>434</u>	<u>764</u>	434	764	434	764
464.h264ref	16	1129	314	<u>1160</u>	<u>305</u>	1177	301	16	1072	330	1122	316	<u>1110</u>	<u>319</u>
471.omnetpp	16	573	174	574	174	<u>573</u>	<u>174</u>	16	573	174	574	174	<u>573</u>	<u>174</u>
473.astar	16	723	155	721	156	<u>723</u>	<u>155</u>	16	652	172	654	172	<u>653</u>	<u>172</u>
483.xalancbmk	16	409	270	<u>409</u>	<u>270</u>	409	270	16	409	270	<u>409</u>	<u>270</u>	409	270

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.

## General Notes

This result was measured on the Servidor Itaotec MX223+.  
The Servidor Itaotec MX223+ and the Servidor Itaotec MX203+ are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/richard/sh/SmartHeap\_8.1/lib -lsmarheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc  
401.bzip2: /opt/intel/Compiler/11.0/081/bin/intel64/icc  
456.hmmer: /opt/intel/Compiler/11.0/081/bin/intel64/icc  
458.sjeng: /opt/intel/Compiler/11.0/081/bin/intel64/icc  
C++ benchmarks (except as noted below):  
icpc  
473.astar: /opt/intel/Compiler/11.0/081/bin/intel64/icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch  
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32  
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3  
429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch  
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias  
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32  
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32  
462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static  
-opt-malloc-options=3 -opt-prefetch  
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes  
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
-Wl,-z,muldefs

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## Peak Optimization Flags (Continued)

473.astar (continued):  
-L/home/richard/sh/SmartHeap\_8.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.0-int-linux64-revA.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.0-int-linux64-revA.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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