



SPEC[®] CFP2006 Result

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Itaotec

SPECfp[®]_rate2006 = 183

Servidor Itaotec MX203+ (Intel Xeon X5570)

SPECfp_rate_base2006 = 177

CPU2006 license: 9001

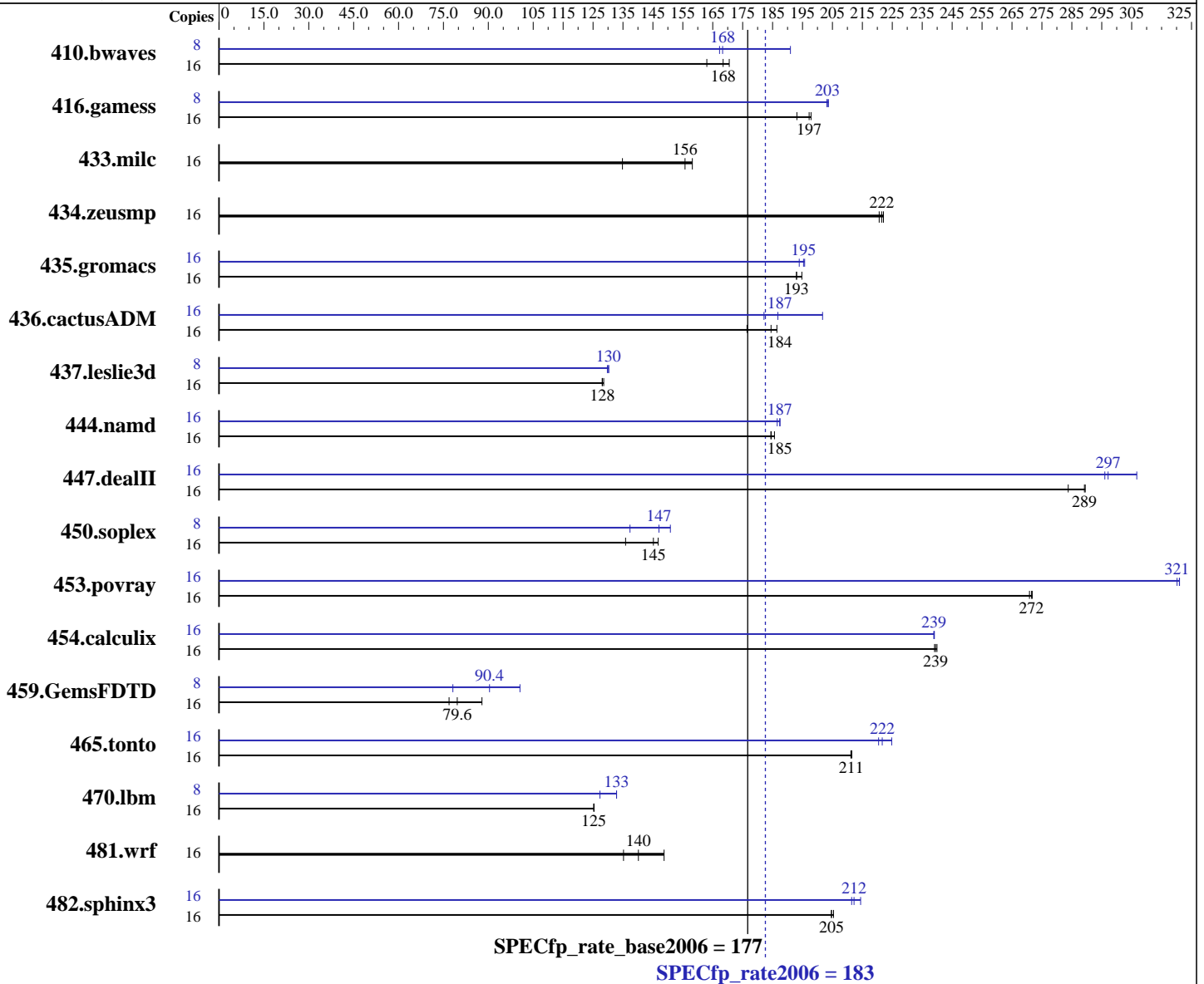
Test date: Feb-2010

Test sponsor: Itaotec

Hardware Availability: Sep-2009

Tested by: Itaotec

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon X5570
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz
 CPU MHz: 2930
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2 with patch Linux kernel 20090119, Kernel 2.6.16.60-0.34-smp
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.081, l_cprof_p_11.0.081
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (6 x 4GB DDR3-1333, CL 9, ECC)
Disk Subsystem: 1 x 160 GB SATA-2, 7200RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	1334	163	1275	171	<u>1291</u>	<u>168</u>	8	569	191	<u>646</u>	<u>168</u>	650	167
416.gamess	16	1583	198	1622	193	<u>1589</u>	<u>197</u>	8	<u>770</u>	<u>203</u>	769	204	771	203
433.milc	16	1089	135	<u>943</u>	<u>156</u>	929	158	16	1089	135	<u>943</u>	<u>156</u>	929	158
434.zeusmp	16	660	221	<u>657</u>	<u>222</u>	656	222	16	660	221	<u>657</u>	<u>222</u>	656	222
435.gromacs	16	592	193	<u>592</u>	<u>193</u>	586	195	16	584	196	<u>585</u>	<u>195</u>	589	194
436.cactusADM	16	1084	176	<u>1036</u>	<u>184</u>	1026	186	16	948	202	<u>1024</u>	<u>187</u>	1050	182
437.leslie3d	16	<u>1174</u>	<u>128</u>	1170	129	1175	128	8	577	130	<u>578</u>	<u>130</u>	579	130
444.namd	16	691	186	<u>695</u>	<u>185</u>	696	184	16	688	187	<u>685</u>	<u>187</u>	684	187
447.dealII	16	<u>633</u>	<u>289</u>	645	284	632	289	16	<u>616</u>	<u>297</u>	597	307	618	296
450.soplex	16	982	136	<u>919</u>	<u>145</u>	909	147	8	486	137	<u>454</u>	<u>147</u>	442	151
453.povray	16	313	272	<u>313</u>	<u>272</u>	314	271	16	266	320	<u>265</u>	<u>321</u>	265	321
454.calculix	16	<u>551</u>	<u>239</u>	550	240	552	239	16	<u>552</u>	<u>239</u>	552	239	552	239
459.GemsFDTD	16	2208	76.9	<u>2132</u>	<u>79.6</u>	1932	87.9	8	1086	78.2	<u>939</u>	<u>90.4</u>	844	101
465.tonto	16	746	211	<u>745</u>	<u>211</u>	744	211	16	<u>710</u>	<u>222</u>	700	225	714	220
470.lbm	16	1754	125	1756	125	<u>1755</u>	<u>125</u>	8	863	127	827	133	<u>827</u>	<u>133</u>
481.wrf	16	<u>1275</u>	<u>140</u>	1322	135	1202	149	16	<u>1275</u>	<u>140</u>	1322	135	1202	149
482.sphinx3	16	1524	205	1518	205	<u>1523</u>	<u>205</u>	16	1475	211	<u>1469</u>	<u>212</u>	1454	214

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.

General Notes

This result was measured on the Servidor Itaotec MX223+.
The Servidor Itaotec MX203+ and the Servidor Itaotec MX223+ are electronically equivalent.



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Tested by: Itaotec

Test date: Feb-2010
Hardware Availability: Sep-2009
Software Availability: Feb-2009

Base Compiler Invocation

C benchmarks:
icc
C++ benchmarks:
icpc
Fortran benchmarks:
ifort
Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static
C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static
Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static
Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc

450.soplex: icpc -m32

Fortran benchmarks (except as noted below):

ifort

437.leslie3d: ifort -m32

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

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Itautec

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Tested by: Itautec

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Hardware Availability: Sep-2009
Software Availability: Feb-2009

Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32
447.dealIII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep-
450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3
453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-
434.zeusmp: basepeak = yes
437.leslie3d: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -opt-prefetch
459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch
465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32
436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -opt-prefetch -auto-ilp32

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Peak Optimization Flags (Continued)

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.0-fp-linux64-revI.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.0-fp-linux64-revI.xml>

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