



SPEC® CINT2006 Result

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Supermicro

SPECint®2006 = 35.7

Motherboard X8SIE-F (Intel Xeon X3470, 2.80 GHz)

SPECint_base2006 = 31.8

CPU2006 license: 001176

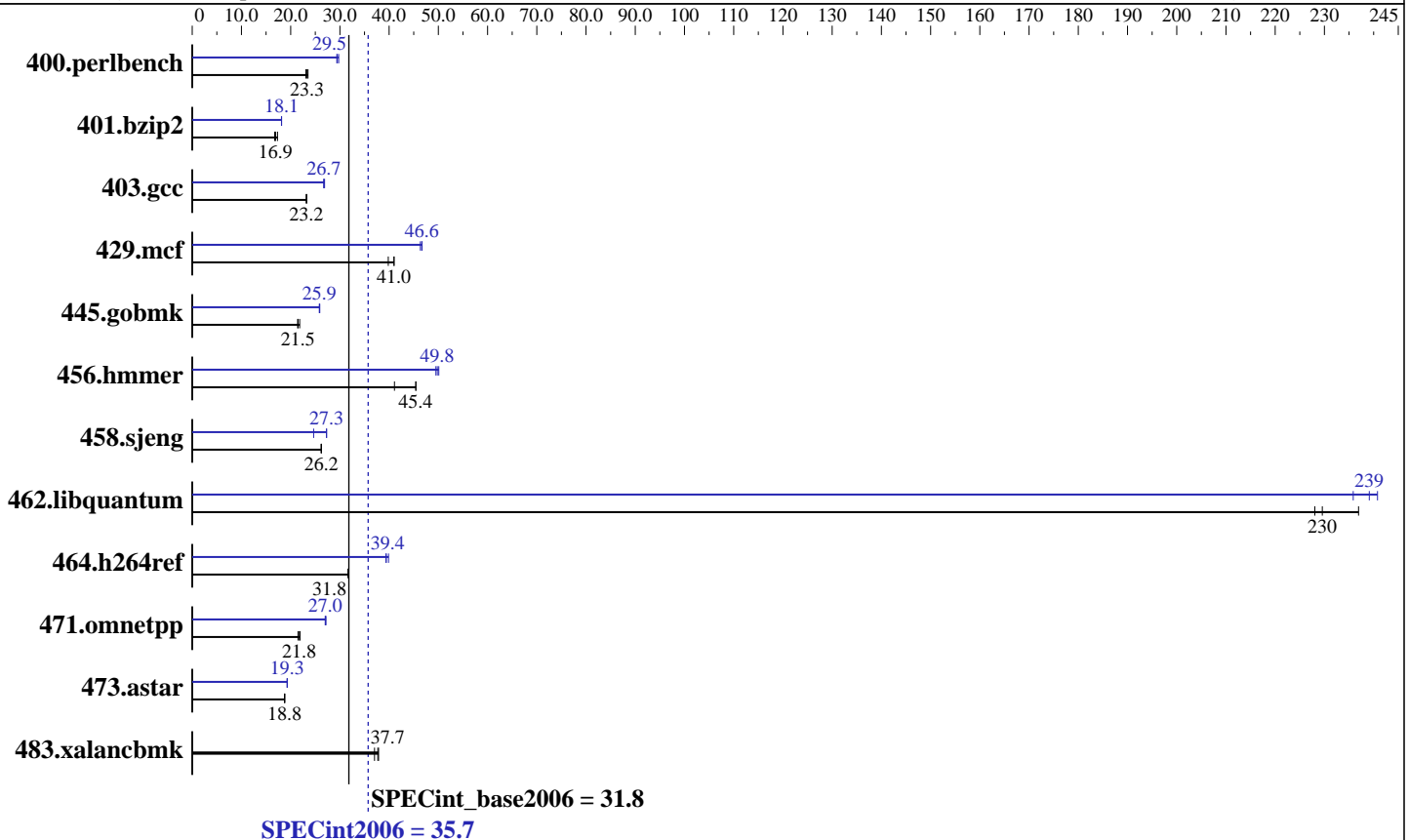
Test date: Jan-2010

Test sponsor: Supermicro

Hardware Availability: Sep-2009

Tested by: Supermicro

Software Availability: Oct-2009



Hardware

CPU Name: Intel Xeon X3460
 CPU Characteristics: Intel Turbo Boost Technology up to 3.46 GHz
 CPU MHz: 2800
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (4 x 4GB DDR3-1333 RDIMM, ECC, CL9)
 Disk Subsystem: 1 x 160 GB SATA II, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64)
 Kernel 2.6.27.19-5-default
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1
 Build 20091012 Package ID: l_cproc_p_11.1.059
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1
 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	415	23.5	423	23.1	419	23.3	333	29.4	331	29.5	328	29.8
401.bzip2	557	17.3	570	16.9	576	16.8	532	18.1	532	18.1	532	18.2
403.gcc	347	23.2	346	23.2	347	23.2	301	26.7	301	26.7	299	26.9
429.mcf	229	39.8	223	41.0	222	41.0	196	46.6	197	46.3	195	46.7
445.gobmk	489	21.4	481	21.8	489	21.5	406	25.9	406	25.8	405	25.9
456.hammer	205	45.4	227	41.1	205	45.4	188	49.5	187	49.8	186	50.1
458.sjeng	462	26.2	462	26.2	461	26.3	443	27.3	490	24.7	444	27.3
462.libquantum	87.4	237	90.3	230	90.9	228	86.7	239	86.1	241	87.9	236
464.h264ref	699	31.6	696	31.8	697	31.8	561	39.4	555	39.9	562	39.4
471.omnetpp	291	21.5	287	21.8	285	21.9	231	27.0	230	27.2	231	27.0
473.astar	374	18.8	374	18.8	374	18.8	364	19.3	363	19.3	364	19.3
483.xalancbmk	183	37.7	186	37.0	182	37.9	183	37.7	186	37.0	182	37.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Default Bios Settings

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

456.hmmr: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 464.h264ref: -DSPEC_CPU_LP64
 471.omnetpp: -DSPEC_CPU_LP64
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
 -par-runtime-control -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
 -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

429.mcf: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

471.omnetpp: icpc -m32



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Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalanbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div -static(pass 2) -prof-use(pass 2)
            -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc
          -opt-malloc-options=3 -auto-ilp32

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
            -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
            -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
            -prof-use(pass 2) -unroll4

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
                -par-runtime-control -opt-prefetch
                -par-schedule-static=32768 -ansi-alias

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
              -prof-use(pass 2) -unroll2 -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs

```

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Peak Optimization Flags (Continued)

471.omnetpp (continued):

`-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap`

473.astar: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`

`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`

`-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs`

`-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.xml>

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For other inquiries, please contact webmaster@spec.org.

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