



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Acer Incorporated

**SPECint®\_rate2006 = 70.7**

Gateway GW2000h-GW170h F1 (Intel Xeon E5502)

**SPECint\_rate\_base2006 = 66.0**

CPU2006 license: 97

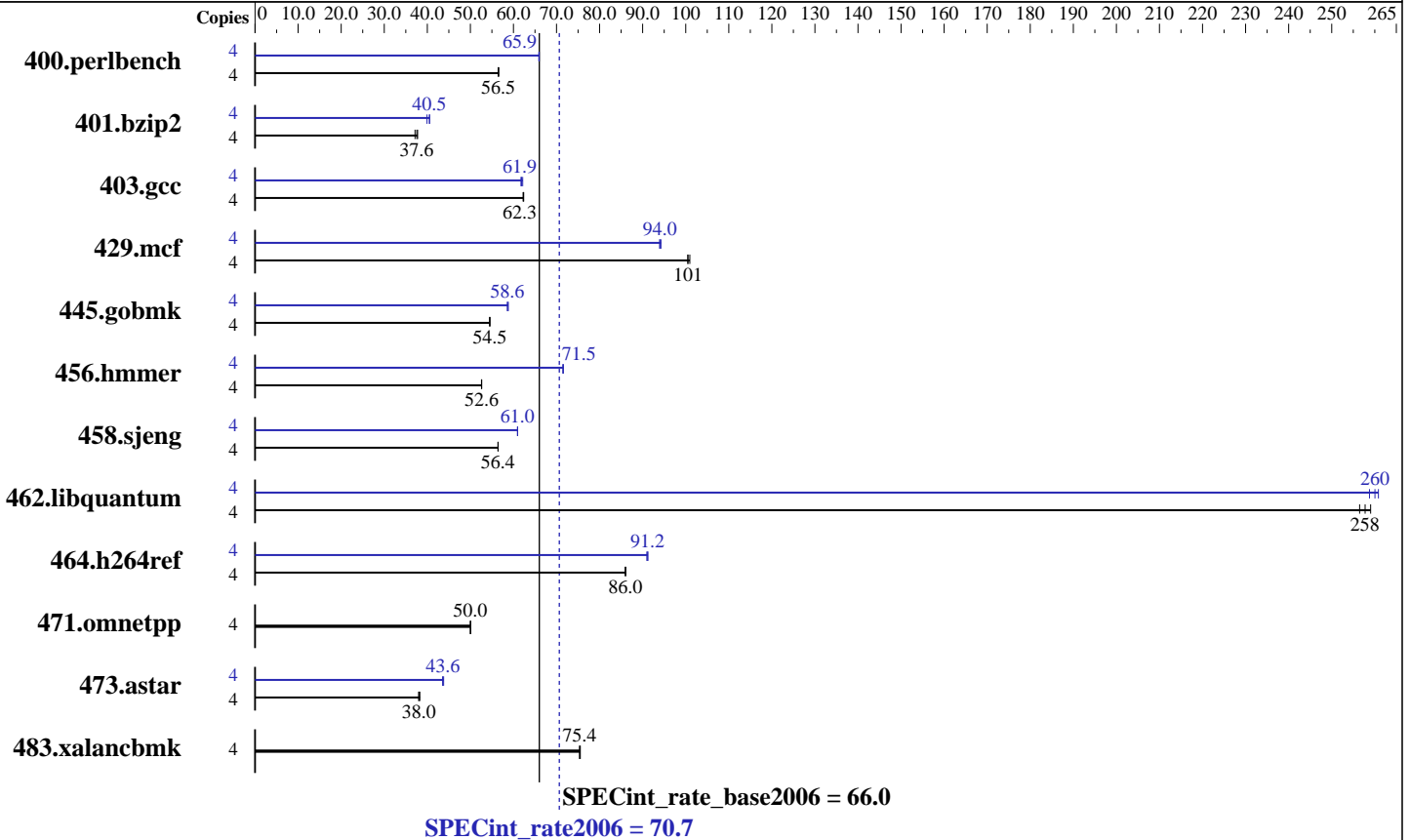
Test date: Nov-2009

Test sponsor: Acer Incorporated

Hardware Availability: Jan-2010

Tested by: Acer Incorporated

Software Availability: Feb-2009



### Hardware

CPU Name: Intel Xeon E5502  
 CPU Characteristics:  
 CPU MHz: 1867  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 4 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (12 x 2 GB DDR3-1066 RDIMM, running at 800 MHz)  
 Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64)  
 Kernel 2.6.27.19-5  
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux  
 Build 20090131 Package ID: I\_cproc\_p\_11.0.080, I\_cprof\_p\_11.0.080  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1  
 Binutils 2.18.50.0.7.20080502



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint\_rate2006 = 70.7

Gateway GW2000h-GW170h F1 (Intel Xeon E5502)

SPECint\_rate\_base2006 = 66.0

CPU2006 license: 97

Test date: Nov-2009

Test sponsor: Acer Incorporated

Hardware Availability: Jan-2010

Tested by: Acer Incorporated

Software Availability: Feb-2009

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	692	56.5	<b><u>692</u></b>	<b><u>56.5</u></b>	691	56.6	4	592	66.0	<b><u>593</u></b>	<b><u>65.9</u></b>	593	65.9
401.bzip2	4	<b><u>1028</u></b>	<b><u>37.6</u></b>	1023	37.7	1039	37.1	4	967	39.9	952	40.5	<b><u>953</u></b>	<b><u>40.5</u></b>
403.gcc	4	516	62.4	<b><u>517</u></b>	<b><u>62.3</u></b>	517	62.2	4	522	61.7	<b><u>520</u></b>	<b><u>61.9</u></b>	518	62.2
429.mcf	4	361	101	<b><u>363</u></b>	<b><u>101</u></b>	363	100	4	388	94.0	387	94.3	<b><u>388</u></b>	<b><u>94.0</u></b>
445.gobmk	4	770	54.5	770	54.5	<b><u>770</u></b>	<b><u>54.5</u></b>	4	<b><u>716</u></b>	<b><u>58.6</u></b>	714	58.8	716	58.6
456.hammer	4	<b><u>709</u></b>	<b><u>52.6</u></b>	709	52.6	710	52.6	4	522	71.5	521	71.6	<b><u>522</u></b>	<b><u>71.5</u></b>
458.sjeng	4	<b><u>858</u></b>	<b><u>56.4</u></b>	857	56.4	858	56.4	4	794	61.0	794	60.9	<b><u>794</u></b>	<b><u>61.0</u></b>
462.libquantum	4	320	259	323	256	<b><u>322</u></b>	<b><u>258</u></b>	4	<b><u>319</u></b>	<b><u>260</u></b>	320	259	318	261
464.h264ref	4	1030	85.9	<b><u>1029</u></b>	<b><u>86.0</u></b>	1028	86.1	4	<b><u>971</u></b>	<b><u>91.2</u></b>	971	91.2	973	91.0
471.omnetpp	4	500	50.0	<b><u>500</u></b>	<b><u>50.0</u></b>	500	50.0	4	500	50.0	<b><u>500</u></b>	<b><u>50.0</u></b>	500	50.0
473.astar	4	<b><u>738</u></b>	<b><u>38.0</u></b>	739	38.0	734	38.3	4	<b><u>644</u></b>	<b><u>43.6</u></b>	641	43.8	644	43.6
483.xalancbmk	4	<b><u>366</u></b>	<b><u>75.4</u></b>	366	75.4	365	75.5	4	<b><u>366</u></b>	<b><u>75.4</u></b>	366	75.4	365	75.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was set for stacksize unlimited

## General Notes

This result was measured on the Gateway GW2000h-GW170h F1.  
The Acer AW2000h-AW170h F1 and Gateway GW2000h-GW170h F1 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint\_rate2006 = 70.7

Gateway GW2000h-GW170h F1 (Intel Xeon E5502)

SPECint\_rate\_base2006 = 66.0

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Nov-2009

Hardware Availability: Jan-2010

Software Availability: Feb-2009

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint\_rate2006 = 70.7

Gateway GW2000h-GW170h F1 (Intel Xeon E5502)

SPECint\_rate\_base2006 = 66.0

CPU2006 license: 97

Test date: Nov-2009

Test sponsor: Acer Incorporated

Hardware Availability: Jan-2010

Tested by: Acer Incorporated

Software Availability: Feb-2009

## Peak Portability Flags (Continued)

458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch  
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32  
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3  
429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch  
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias  
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32  
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32  
462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static  
-opt-malloc-options=3 -opt-prefetch  
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes  
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint\_rate2006 = 70.7

Gateway GW2000h-GW170h F1 (Intel Xeon E5502)

SPECint\_rate\_base2006 = 66.0

CPU2006 license: 97

Test date: Nov-2009

Test sponsor: Acer Incorporated

Hardware Availability: Jan-2010

Tested by: Acer Incorporated

Software Availability: Feb-2009

## Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.20100119.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.20100119.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Wed Jul 23 06:30:49 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 16 February 2010.