



# SPEC<sup>®</sup> CINT2006 Result

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## Fujitsu

SPECint<sup>®</sup>\_rate2006 = 42.6

PRIMERGY RX100 S6, Intel Pentium G6950, 2.80 GHz

SPECint\_rate\_base2006 = 39.3

CPU2006 license: 19

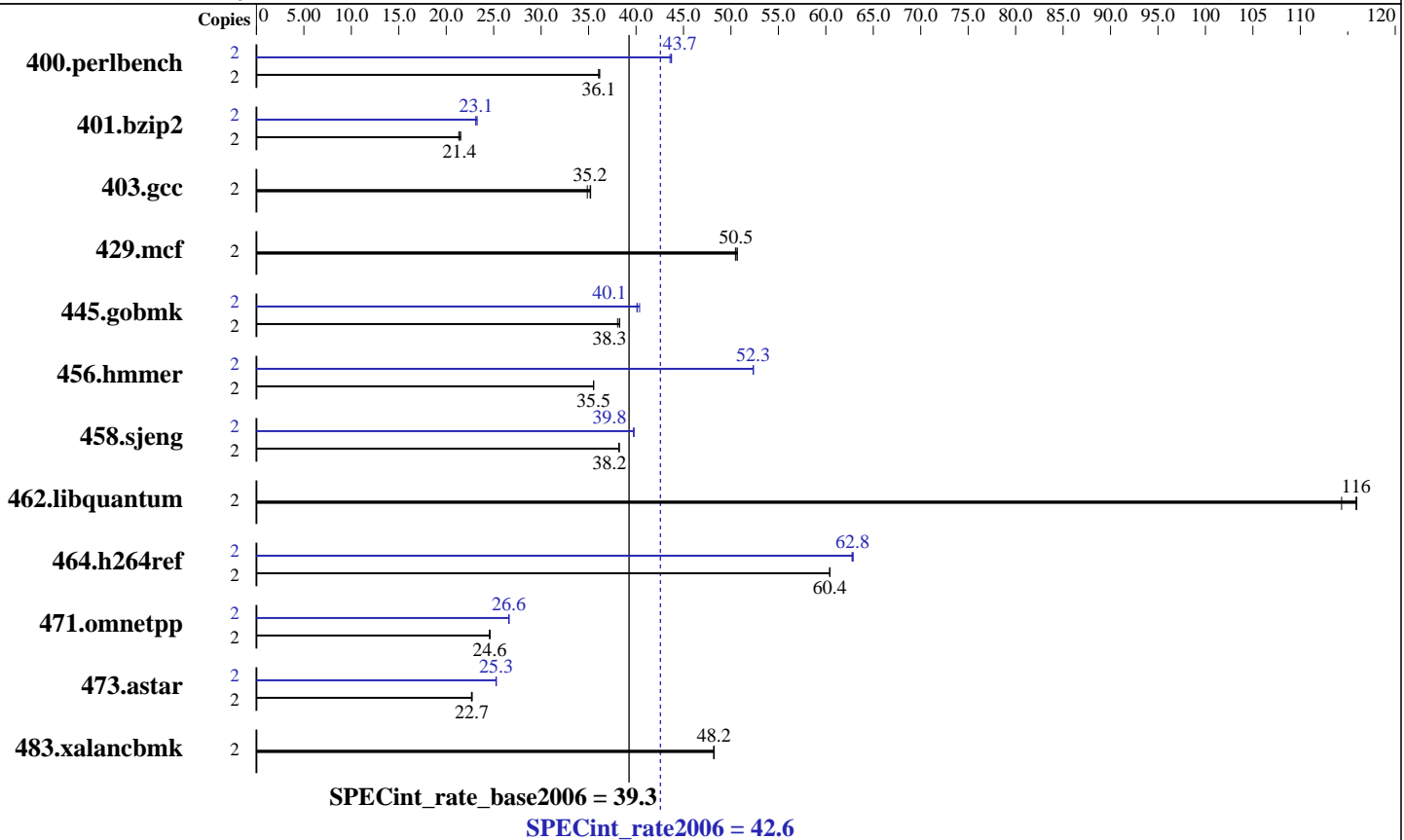
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jan-2010

Hardware Availability: Jan-2010

Software Availability: Nov-2009



### Hardware

CPU Name: Intel Pentium G6950  
 CPU Characteristics:  
 CPU MHz: 2800  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 3 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 8 GB (2x4 GB PC3-10600E, 2 rank, CL9-9-9, ECC, see add'l detail in notes)  
 Disk Subsystem: 1 x SATA, 250 GB, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-smp  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091012 Package ID: l\_cproc\_p\_11.1.059  
 Auto Parallel: No  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	540	36.2	<u>542</u>	<u>36.1</u>	542	36.1	2	447	43.7	<u>447</u>	<u>43.7</u>	448	43.6
401.bzip2	2	<u>901</u>	<u>21.4</u>	897	21.5	905	21.3	2	835	23.1	<u>834</u>	<u>23.1</u>	830	23.3
403.gcc	2	<u>458</u>	<u>35.2</u>	457	35.2	462	34.9	2	<u>458</u>	<u>35.2</u>	457	35.2	462	34.9
429.mcf	2	360	50.7	361	50.5	<u>361</u>	<u>50.5</u>	2	360	50.7	361	50.5	<u>361</u>	<u>50.5</u>
445.gobmk	2	548	38.3	551	38.1	<u>548</u>	<u>38.3</u>	2	<u>523</u>	<u>40.1</u>	523	40.1	520	40.4
456.hammer	2	525	35.5	<u>525</u>	<u>35.5</u>	525	35.5	2	<u>356</u>	<u>52.3</u>	356	52.4	357	52.3
458.sjeng	2	<u>633</u>	<u>38.2</u>	633	38.2	634	38.2	2	608	39.8	609	39.7	<u>609</u>	<u>39.8</u>
462.libquantum	2	357	116	<u>358</u>	<u>116</u>	362	114	2	357	116	<u>358</u>	<u>116</u>	362	114
464.h264ref	2	733	60.4	732	60.4	<u>733</u>	<u>60.4</u>	2	705	62.8	704	62.9	<u>705</u>	<u>62.8</u>
471.omnetpp	2	508	24.6	<u>509</u>	<u>24.6</u>	509	24.6	2	469	26.6	470	26.6	<u>470</u>	<u>26.6</u>
473.astar	2	617	22.7	<u>619</u>	<u>22.7</u>	620	22.7	2	556	25.2	<u>556</u>	<u>25.3</u>	555	25.3
483.xalancbmk	2	286	48.2	<u>286</u>	<u>48.2</u>	286	48.2	2	286	48.2	<u>286</u>	<u>48.2</u>	286	48.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
taskset has been used to bind processes to cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

The system automatically configures the memory to run at 1066 MHz.

## General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSSE3 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSSE3 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.icl1.1/libicl1.1-32bit -lsmarheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3  
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3  
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3  
-no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3  
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias  
-opt-ra-region-strategy=routine -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmarheap64

483.xalanbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.20100202.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.20100202.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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