



SPEC® CINT2006 Result

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Fujitsu

SPECint®2006 = 20.1

PRIMERGY TX150 S7, Intel Celeron G1101, 2.26 GHz

SPECint_base2006 = 18.5

CPU2006 license: 19

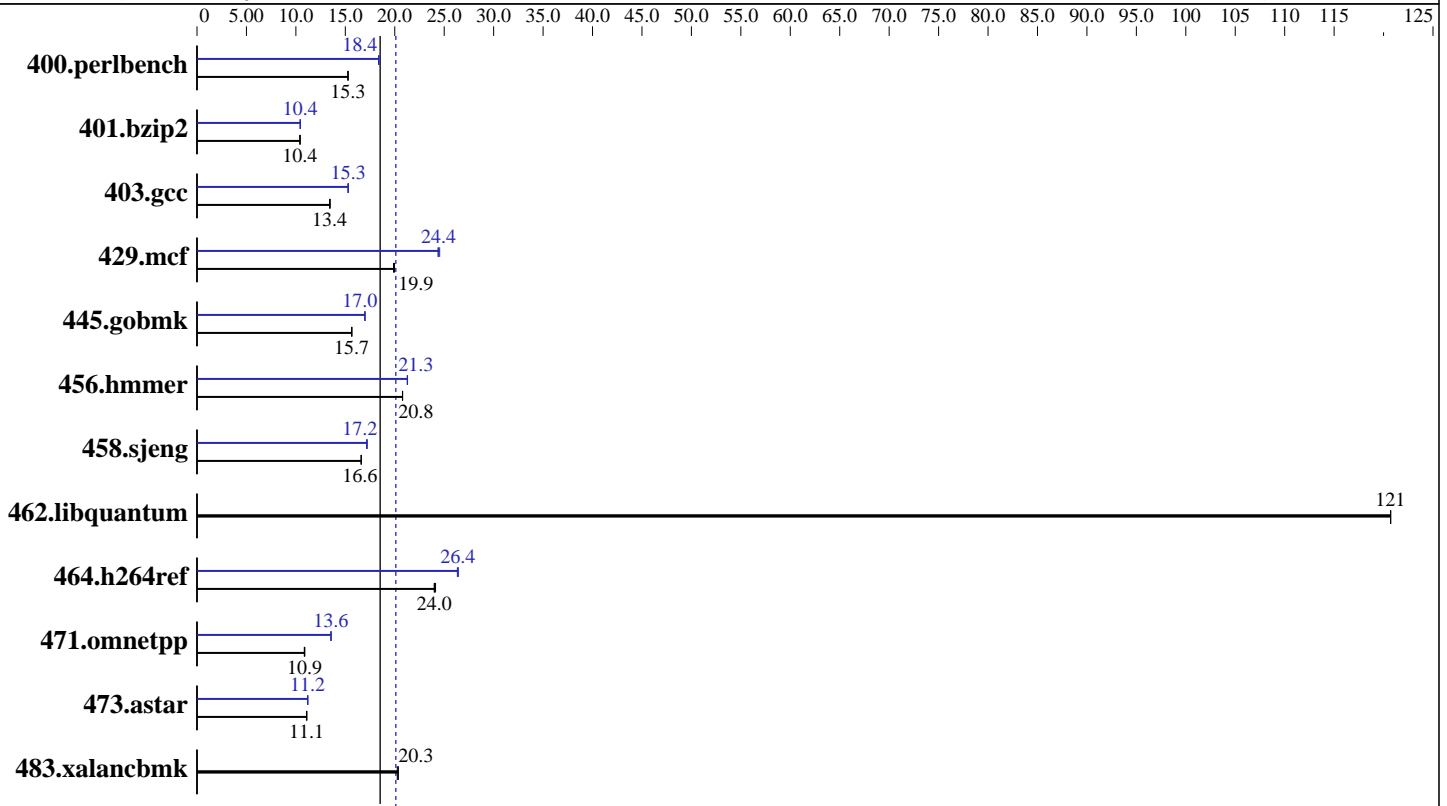
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jan-2010

Hardware Availability: Jan-2010

Software Availability: Nov-2009



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Hardware

CPU Name: Intel Celeron G1101
 CPU Characteristics:
 CPU MHz: 2267
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 2 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (2x4 GB PC3-10600E, 2 rank, CL9-9-9, ECC, see add'l detail in notes)
 Disk Subsystem: 1 x SATA, 250 GB, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-smp
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091012 Package ID: l_cproc_p_11.1.059
 Auto Parallel: Yes
 File System: ext3
 System State: Multi-User Run Level 3
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<u>639</u>	<u>15.3</u>	639	15.3	641	15.2	<u>531</u>	18.4	<u>532</u>	18.4	<u>532</u>	<u>18.4</u>
401.bzip2	924	10.4	928	10.4	<u>927</u>	<u>10.4</u>	<u>925</u>	<u>10.4</u>	925	10.4	923	10.5
403.gcc	598	13.5	<u>600</u>	<u>13.4</u>	600	13.4	528	15.3	<u>526</u>	<u>15.3</u>	526	15.3
429.mcf	457	20.0	459	19.9	<u>458</u>	<u>19.9</u>	372	24.5	374	24.4	<u>373</u>	<u>24.4</u>
445.gobmk	670	15.7	<u>670</u>	<u>15.7</u>	670	15.6	618	17.0	<u>618</u>	<u>17.0</u>	617	17.0
456.hammer	<u>449</u>	<u>20.8</u>	449	20.8	449	20.8	<u>439</u>	<u>21.3</u>	439	21.3	438	21.3
458.sjeng	<u>728</u>	<u>16.6</u>	729	16.6	728	16.6	<u>705</u>	<u>17.2</u>	703	17.2	705	17.2
462.libquantum	172	121	<u>172</u>	<u>121</u>	172	121	172	121	<u>172</u>	<u>121</u>	172	121
464.h264ref	917	24.1	923	24.0	<u>921</u>	<u>24.0</u>	<u>839</u>	<u>26.4</u>	838	26.4	840	26.3
471.omnetpp	573	10.9	576	10.8	<u>575</u>	<u>10.9</u>	<u>461</u>	<u>13.6</u>	461	13.6	461	13.5
473.astar	632	11.1	<u>633</u>	<u>11.1</u>	634	11.1	627	11.2	<u>627</u>	<u>11.2</u>	626	11.2
483.xalancbmk	339	20.4	<u>340</u>	<u>20.3</u>	341	20.3	339	20.4	<u>340</u>	<u>20.3</u>	341	20.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

The system automatically configures the memory to run at 1066 MHz.

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64



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Base Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

Base Optimization Flags

C benchmarks:

```

-xSSSE3 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch

```

C++ benchmarks:

```

-xSSSE3 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

```

Base Other Flags

C benchmarks:

```

403.gcc: -Dalloca=_alloca

```

Peak Compiler Invocation

C benchmarks (except as noted below):

```

icc -m64

```

```

429.mcf: icc -m32

```

C++ benchmarks (except as noted below):

```

icpc -m64

```

```

471.omnetpp: icpc -m32

```



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Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
               -no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
           -no-prec-div -static -auto-ilp32 -opt-prefetch
           -ansi-alias

403.gcc: -xSSSE3 -ipo -O3 -no-prec-div -static -inline-calloc
         -opt-malloc-options=3

429.mcf: -xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -O2 -ipo
          -no-prec-div -ansi-alias

456.hmmr: -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2
         -ansi-alias -auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
          -no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
            -no-prec-div -static -unroll2 -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
            -no-prec-div -ansi-alias -opt-ra-region-strategy=block
            -Wl,-z,muldefs
            -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

```

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Peak Optimization Flags (Continued)

```
473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
          -no-prec-div -ansi-alias -opt-ra-region-strategy=routine
          -Wl,-z,muldefs
          -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64
```

```
483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.20100202.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.20100202.xml>

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For other inquiries, please contact webmaster@spec.org.

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