



# SPEC<sup>®</sup> CFP2006 Result

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## Bull SAS

### SPECfp<sup>®</sup>\_rate2006 = 50.5

### NovaScale R410 F2 (Intel Core i3-530, 2.93 GHz)

### SPECfp\_rate\_base2006 = 49.2

CPU2006 license: 20

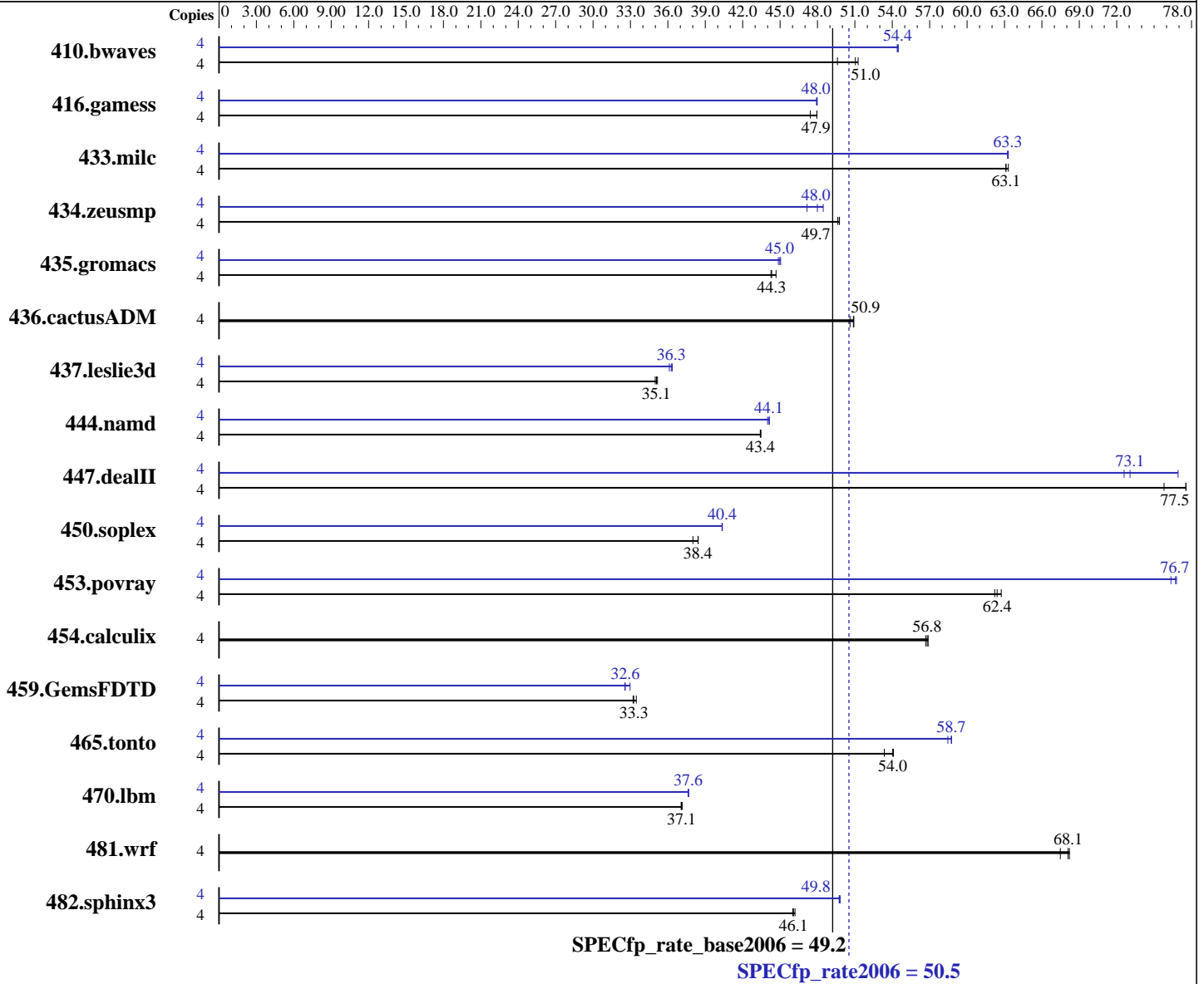
Test sponsor: Bull SAS

Tested by: Dell Inc.

Test date: Jan-2010

Hardware Availability: Jan-2010

Software Availability: Dec-2009



#### Hardware

CPU Name: Intel Core i3-530  
 CPU Characteristics:  
 CPU MHz: 2933  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

#### Software

Operating System: Red Hat Enterprise Linux Server release 5.3, Kernel 2.6.18-128.el5  
 Compiler: Intel Fortran Compiler and Intel C++ Compiler Professional Edition 11.1 For Linux Build 20091012 Package ID: l\_cproc\_p\_11.1.059, l\_cprof\_p\_11.1.059  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)

Continued on next page



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L3 Cache: 4 MB I+D on chip per chip  
Other Cache: None  
Memory: 8 GB (4 x 2 GB DDR3-1333 DR UDIMM)  
Disk Subsystem: 1 x 160 GB 7200 RPM SATA  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: Binutils 2.18.50.0.7.20080502

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	1096	49.6	1061	51.3	<b>1065</b>	<b>51.0</b>	4	<b>998</b>	<b>54.4</b>	998	54.5	999	54.4
416.gamess	4	<b>1633</b>	<b>47.9</b>	1633	48.0	1651	47.4	4	1632	48.0	<b>1633</b>	<b>48.0</b>	1634	47.9
433.milc	4	582	63.1	<b>582</b>	<b>63.1</b>	580	63.3	4	<b>580</b>	<b>63.3</b>	581	63.2	580	63.3
434.zeusmp	4	<b>732</b>	<b>49.7</b>	731	49.8	734	49.6	4	772	47.2	751	48.5	<b>759</b>	<b>48.0</b>
435.gromacs	4	639	44.7	645	44.3	<b>644</b>	<b>44.3</b>	4	634	45.0	637	44.9	<b>635</b>	<b>45.0</b>
436.cactusADM	4	944	50.6	<b>939</b>	<b>50.9</b>	939	50.9	4	944	50.6	<b>939</b>	<b>50.9</b>	939	50.9
437.leslie3d	4	1069	35.2	1075	35.0	<b>1072</b>	<b>35.1</b>	4	<b>1037</b>	<b>36.3</b>	1034	36.4	1041	36.1
444.namd	4	739	43.4	<b>739</b>	<b>43.4</b>	738	43.5	4	729	44.0	<b>727</b>	<b>44.1</b>	727	44.1
447.dealII	4	604	75.8	590	77.5	<b>590</b>	<b>77.5</b>	4	<b>626</b>	<b>73.1</b>	595	76.9	630	72.6
450.soplex	4	878	38.0	<b>868</b>	<b>38.4</b>	868	38.4	4	826	40.4	<b>826</b>	<b>40.4</b>	827	40.3
453.povray	4	342	62.2	<b>341</b>	<b>62.4</b>	339	62.7	4	277	76.8	<b>277</b>	<b>76.7</b>	279	76.3
454.calculix	4	<b>581</b>	<b>56.8</b>	580	56.9	582	56.7	4	<b>581</b>	<b>56.8</b>	580	56.9	582	56.7
459.GemsFDTD	4	1278	33.2	<b>1276</b>	<b>33.3</b>	1268	33.5	4	1304	32.5	<b>1303</b>	<b>32.6</b>	1287	33.0
465.tonto	4	738	53.4	728	54.1	<b>728</b>	<b>54.0</b>	4	673	58.5	670	58.8	<b>670</b>	<b>58.7</b>
470.lbm	4	1483	37.1	1479	37.1	<b>1481</b>	<b>37.1</b>	4	<b>1460</b>	<b>37.6</b>	1460	37.6	1459	37.7
481.wrf	4	<b>656</b>	<b>68.1</b>	662	67.5	655	68.2	4	<b>656</b>	<b>68.1</b>	662	67.5	655	68.2
482.sphinx3	4	1694	46.0	1687	46.2	<b>1692</b>	<b>46.1</b>	4	1567	49.7	<b>1567</b>	<b>49.8</b>	1564	49.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

BIOS Settings:  
Power Management = Maximum Performance (Default = Active Power Controller)



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## General Notes

The Dell PowerEdge R210 and the Bull NovaScale R410 F2 models are electronically equivalent. This result was measured on a Dell PowerEdge R210.

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static

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## Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX



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**Test sponsor:** Bull SAS

**Tested by:** Dell Inc.

**Test date:** Jan-2010

**Hardware Availability:** Jan-2010

**Software Availability:** Dec-2009

## Peak Optimization Flags

### C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-fno-alias -opt-prefetch

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3 -ansi-alias -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

### C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-fno-alias -auto-ilp32

447.dealIII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)

437.leslie3d: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3 -opt-prefetch

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0

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## Peak Optimization Flags (Continued)

```
465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -unroll4 -auto -inline-calloc -opt-malloc-options=3
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
            -opt-prefetch -auto-ilp32
```

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-fp-linux64-revA.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-fp-linux64-revA.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 06:26:48 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 16 February 2010.