# SPEC® CINT2006 Result
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## Cisco Systems

### UCS C210 M1 (Intel Xeon E5540)

SPECint®_rate2006 = 216

SPECint_rate_base2006 = 201

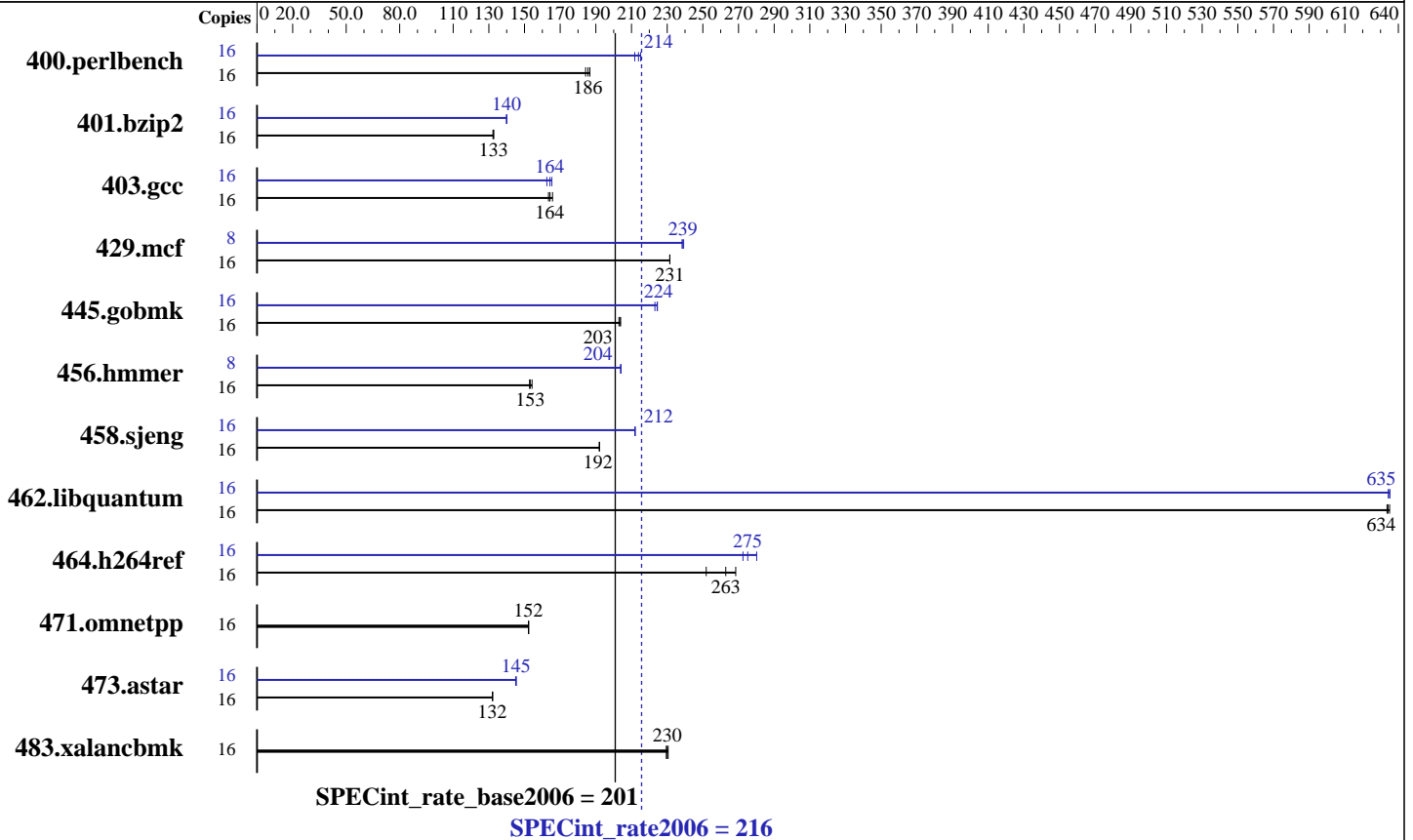**CPU2006 license:** 9019　　　　　　　　　　**Test date:** Jan-2010
**Test sponsor:** Cisco Systems　　　　　　**Hardware Availability:** Oct-2009
**Tested by:** Cisco Systems　　　　　　　**Software Availability:** Mar-2009



SPECint_rate_base2006 = 201

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## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5540 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 2.80 GHz |
| CPU MHz: | 2533 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 2 chips, 4 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 Chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 8 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 24 GB (12 * 2GB DDR3-1066 MHz) |
| Disk Subsystem: | 73 GB SATA, 15kRPM |
| Other Hardware: | None |

## Software

| | |
|---|---|
| Operating System: | SuSe Linux Enterprise Server 11 (x86_64), Kernel 2.6.27-15-2-default, RC4 |
| Compiler: | Intel C++ and Fortran Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080, l_cprof_p_11.0.080 |
| Auto Parallel: | No |
| File System: | ReiserFS |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 64-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Binutils 2.18.50.0.7.20080502 and SmartHeap |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 16 | 837 | 187 | **842** | **186** | 848 | 184 | 16 | 738 | 212 | **731** | **214** | 727 | 215 |
| 401.bzip2 | 16 | 1167 | 132 | **1164** | **133** | 1164 | 133 | 16 | 1104 | 140 | **1103** | **140** | 1102 | 140 |
| 403.gcc | 16 | **784** | **164** | 777 | 166 | 788 | 163 | 16 | **784** | **164** | 779 | 165 | 792 | 163 |
| 429.mcf | 16 | 630 | 231 | 630 | 232 | **630** | **231** | 8 | 305 | 239 | **306** | **239** | 306 | 238 |
| 445.gobmk | 16 | 827 | 203 | 823 | 204 | **825** | **203** | 16 | **748** | **224** | 748 | 225 | 752 | 223 |
| 456.hmmer | 16 | **974** | **153** | 968 | 154 | 977 | 153 | 8 | **366** | **204** | 366 | 204 | 366 | 204 |
| 458.sjeng | 16 | **1008** | **192** | 1008 | 192 | 1009 | 192 | 16 | 913 | 212 | 914 | 212 | **913** | **212** |
| 462.libquantum | 16 | 523 | 634 | 522 | 635 | **523** | **634** | 16 | **522** | **635** | 523 | 634 | 522 | 635 |
| 464.h264ref | 16 | 1319 | 268 | **1347** | **263** | 1406 | 252 | 16 | 1264 | 280 | 1299 | 273 | **1287** | **275** |
| 471.omnetpp | 16 | 657 | 152 | 656 | 152 | **657** | **152** | 16 | 657 | 152 | 656 | 152 | **657** | **152** |
| 473.astar | 16 | 850 | 132 | 851 | 132 | **851** | **132** | 16 | **774** | **145** | 774 | 145 | 772 | 145 |
| 483.xalancbmk | 16 | 479 | 231 | 481 | 229 | **480** | **230** | 16 | 479 | 231 | 481 | 229 | **480** | **230** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

## Operating System Notes

    ulimit -s unlimited was used to set the stacksize to unlimited prior to run

## Base Compiler Invocation

C benchmarks:
      icc

C++ benchmarks:
      icpc

## Base Portability Flags

     400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

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## Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

401.bzip2: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

456.hmmer: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

458.sjeng: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

C++ benchmarks (except as noted below):
```
icpc
```

473.astar: `/opt/intel/Compiler/11.0/080/bin/intel64/icpc`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

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# Peak Optimization Flags

C benchmarks:

```
    400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                   -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                   -prof-use(pass 2) -ansi-alias -opt-prefetch

        401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                   -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                   -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

          403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
                   -opt-malloc-options=3

          429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                   -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                   -prof-use(pass 2) -opt-prefetch

        445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
                   -ipo -no-prec-div -ansi-alias

        456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
                   -ansi-alias -auto-ilp32

        458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                   -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                   -prof-use(pass 2) -unroll4 -auto-ilp32

    462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static
                   -opt-malloc-options=3 -opt-prefetch

      464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                   -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                   -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
      471.omnetpp: basepeak = yes

        473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                   -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                   -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
                   -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

     483.xalancbmk: basepeak = yes
```

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# Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revH.20100317.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revH.20100317.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
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