



# SPEC® CINT2006 Result

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## Fujitsu

**SPECint®2006 = 24.2**

PRIMERGY TX150 S7, Intel Pentium G6950, 2.80 GHz

**SPECint\_base2006 = 22.2**

CPU2006 license: 19

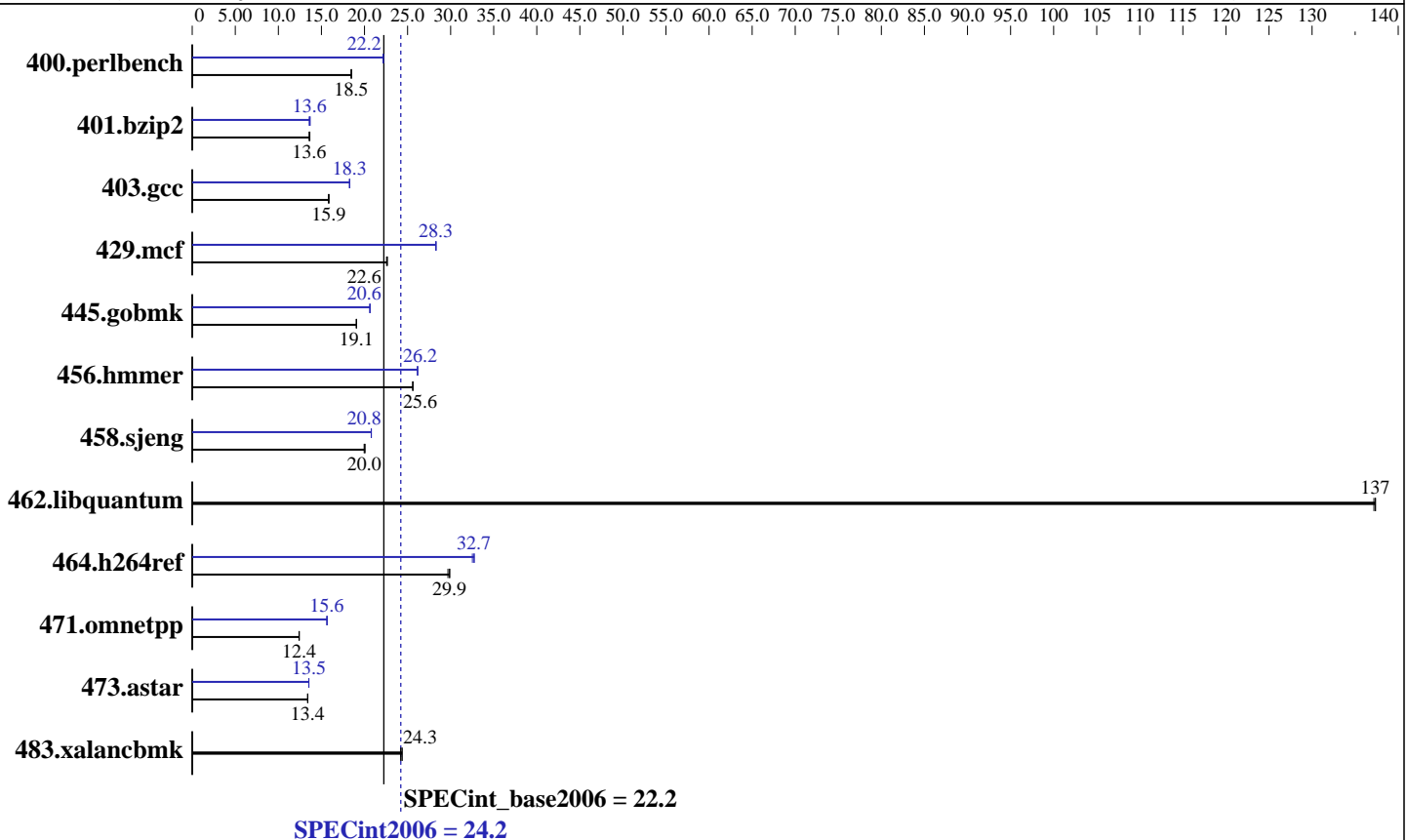
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jan-2010

Hardware Availability: Jan-2010

Software Availability: Nov-2009



### Hardware

CPU Name: Intel Pentium G6950  
 CPU Characteristics: 2800  
 CPU MHz: 2800  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 3 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 8 GB (2x4 GB PC3-10600E, 2 rank, CL9-9-9, ECC, see add'l detail in notes)  
 Disk Subsystem: 1 x SATA, 250 GB, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-smp  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091012 Package ID: l\_cproc\_p\_11.1.059  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b><u>529</u></b>	<b><u>18.5</u></b>	529	18.5	530	18.5	<b><u>441</u></b>	<b><u>22.2</u></b>	441	22.2	440	22.2
401.bzip2	707	13.7	<b><u>709</u></b>	<b><u>13.6</u></b>	711	13.6	711	13.6	<b><u>707</u></b>	<b><u>13.6</u></b>	705	13.7
403.gcc	507	15.9	<b><u>508</u></b>	<b><u>15.9</u></b>	509	15.8	<b><u>441</u></b>	<b><u>18.3</u></b>	440	18.3	441	18.3
429.mcf	403	22.6	<b><u>403</u></b>	<b><u>22.6</u></b>	404	22.6	322	28.3	<b><u>322</u></b>	<b><u>28.3</u></b>	322	28.3
445.gobmk	<b><u>550</u></b>	<b><u>19.1</u></b>	550	19.1	550	19.1	<b><u>508</u></b>	<b><u>20.6</u></b>	509	20.6	508	20.6
456.hammer	<b><u>364</u></b>	<b><u>25.6</u></b>	364	25.6	364	25.6	357	26.2	<b><u>357</u></b>	<b><u>26.2</u></b>	357	26.2
458.sjeng	606	20.0	603	20.1	<b><u>604</u></b>	<b><u>20.0</u></b>	582	20.8	582	20.8	<b><u>582</u></b>	<b><u>20.8</u></b>
462.libquantum	151	137	<b><u>151</u></b>	<b><u>137</u></b>	151	137	151	137	<b><u>151</u></b>	<b><u>137</u></b>	151	137
464.h264ref	740	29.9	<b><u>741</u></b>	<b><u>29.9</u></b>	745	29.7	676	32.7	680	32.5	<b><u>677</u></b>	<b><u>32.7</u></b>
471.omnetpp	503	12.4	504	12.4	<b><u>503</u></b>	<b><u>12.4</u></b>	<b><u>400</u></b>	<b><u>15.6</u></b>	399	15.7	400	15.6
473.astar	523	13.4	524	13.4	<b><u>524</u></b>	<b><u>13.4</u></b>	518	13.5	<b><u>519</u></b>	<b><u>13.5</u></b>	519	13.5
483.xalancbmk	283	24.4	285	24.2	<b><u>283</u></b>	<b><u>24.3</u></b>	283	24.4	285	24.2	<b><u>283</u></b>	<b><u>24.3</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

The system automatically configures the memory to run at 1066 MHz.

## General Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to granularity=fine,scatter  
For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64



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## Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

## Base Optimization Flags

C benchmarks:

```
-xSSSE3 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch
```

C++ benchmarks:

```
-xSSSE3 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m64
```

```
429.mcf: icc -m32
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
471.omnetpp: icpc -m32
```



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## Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

## Peak Optimization Flags

C benchmarks:

```

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
               -no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
           -no-prec-div -static -auto-ilp32 -opt-prefetch
           -ansi-alias

403.gcc: -xSSSE3 -ipo -O3 -no-prec-div -static -inline-calloc
         -opt-malloc-options=3

429.mcf: -xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -O2 -ipo
          -no-prec-div -ansi-alias

456.hmmr: -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2
         -ansi-alias -auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
          -no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
            -no-prec-div -static -unroll2 -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
            -no-prec-div -ansi-alias -opt-ra-region-strategy=block
            -Wl,-z,muldefs
            -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

```

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## Peak Optimization Flags (Continued)

```
473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
          -no-prec-div -ansi-alias -opt-ra-region-strategy=routine
          -Wl,-z,muldefs
          -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64
```

```
483.xalancbmk: basepeak = yes
```

## Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.20100202.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.20100202.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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