SPEC® CINT2006 Result
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Fujitsu

PRIMERGY RX100 S6, Intel Xeon X3430, 2.40 GHz

$SPECint®_rate2006 = $ 92.3

$SPECint_rate_base2006 = $ 85.7

CPU2006 license: 19	**Test date:** Dec-2009
Test sponsor: Fujitsu	**Hardware Availability:** Jan-2010
Tested by: Fujitsu	**Software Availability:** Nov-2009

400.perlbench 4 / 4 — 85.1 / 68.6
401.bzip2 4 / 4 — 49.9 / 45.6
403.gcc 4 — 74.3
429.mcf 4 / 4 — 122 / 121
445.gobmk 4 / 4 — 75.6 / 71.5
456.hmmer 4 / 4 — 146 / 122
458.sjeng 4 / 4 — 81.1 / 73.8
462.libquantum 4 — 273
464.h264ref 4 / 4 — 117 / 112
471.omnetpp 4 / 4 — 70.4 / 64.8
473.astar 4 / 4 — 51.4 / 45.1
483.xalancbmk 4 — 97.1

SPECint_rate_base2006 = 85.7

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Hardware

CPU Name:	Intel Xeon X3430
CPU Characteristics:	Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	16 GB (4x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC)
Disk Subsystem:	1 x SATA, 250 GB, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-smp
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091012 Package ID: l_cproc_p_11.1.059
Auto Parallel:	No
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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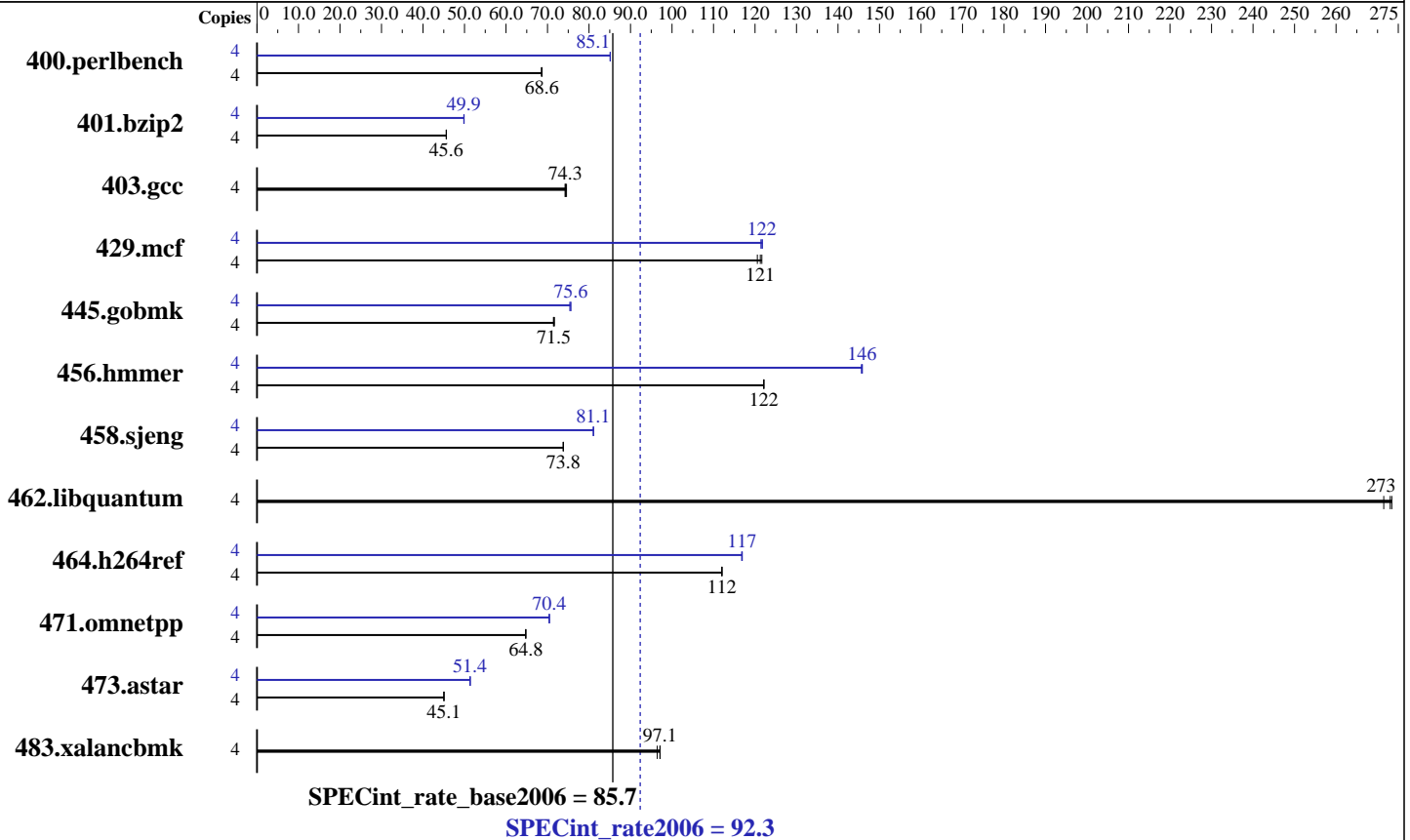
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Results Table

Benchmark	Base						Peak					
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	570	68.5	569	68.7	**569**	**68.6**	4	**459**	**85.1**	459	85.1	459	85.1
401.bzip2	4	**846**	**45.6**	846	45.6	846	45.6	4	773	49.9	**774**	**49.9**	774	49.8
403.gcc	4	**433**	**74.3**	432	74.6	434	74.2	4	**433**	**74.3**	432	74.6	434	74.2
429.mcf	4	303	121	**301**	**121**	300	122	4	300	122	300	121	**300**	**122**
445.gobmk	4	**587**	**71.5**	585	71.7	587	71.4	4	555	75.6	557	75.4	**555**	**75.6**
456.hmmer	4	306	122	306	122	**306**	**122**	4	256	146	**256**	**146**	256	146
458.sjeng	4	**656**	**73.8**	656	73.8	656	73.8	4	597	81.1	597	81.0	**597**	**81.1**
462.libquantum	4	305	271	303	273	**304**	**273**	4	305	271	303	273	**304**	**273**
464.h264ref	4	790	112	**790**	**112**	790	112	4	**757**	**117**	757	117	758	117
471.omnetpp	4	386	64.8	386	64.8	**386**	**64.8**	4	**355**	**70.4**	355	70.5	355	70.4
473.astar	4	623	45.1	**623**	**45.1**	623	45.0	4	547	51.3	**547**	**51.4**	547	51.4
483.xalancbmk	4	**284**	**97.1**	284	97.1	286	96.4	4	**284**	**97.1**	284	97.1	286	96.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

General Notes

```
For information about Fujitsu please visit: http://www.fujitsu.com
```

Base Compiler Invocation

```
C benchmarks:
     icc -m32

C++ benchmarks:
     icpc -m32
```

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
```

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Base Portability Flags (Continued)

462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m32
```

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks (except as noted below):
```
icpc -m32
```

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`

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Peak Portability Flags (Continued)

473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-ipo(pass 2)`
`-O3(pass 2)` `-no-prec-div(pass 2)` `-static(pass 2)`
`-prof-use(pass 2)` `-ansi-alias`

401.bzip2: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-ipo(pass 2)`
`-O3(pass 2)` `-no-prec-div(pass 2)` `-static(pass 2)`
`-prof-use(pass 2)` `-opt-prefetch` `-ansi-alias` `-auto-ilp32`

403.gcc: `basepeak = yes`

429.mcf: `-xSSE4.2` `-ipo` `-O3` `-no-prec-div` `-static` `-opt-prefetch`

445.gobmk: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-prof-use(pass 2)` `-O2`
`-ipo` `-no-prec-div` `-ansi-alias`

456.hmmer: `-xSSE4.2` `-ipo` `-O3` `-no-prec-div` `-static` `-unroll2`
`-ansi-alias` `-auto-ilp32`

458.sjeng: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-ipo(pass 2)`
`-O3(pass 2)` `-no-prec-div(pass 2)` `-static(pass 2)`
`-prof-use(pass 2)` `-unroll4` `-auto-ilp32`

462.libquantum: `basepeak = yes`

464.h264ref: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-ipo(pass 2)`
`-O3(pass 2)` `-no-prec-div(pass 2)` `-static(pass 2)`
`-prof-use(pass 2)` `-unroll2` `-ansi-alias`

C++ benchmarks:

471.omnetpp: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-ipo(pass 2)`
`-O3(pass 2)` `-no-prec-div(pass 2)` `-prof-use(pass 2)`
`-ansi-alias` `-opt-ra-region-strategy=block` `-Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit` `-lsmartheap`

473.astar: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-ipo(pass 2)`
`-O3(pass 2)` `-no-prec-div(pass 2)` `-prof-use(pass 2)`
`-ansi-alias` `-opt-ra-region-strategy=routine` `-Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit` `-lsmartheap64`

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.20100202.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.20100202.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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