



# SPEC® CINT2006 Result

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## Fujitsu

**SPECint®2006 = 28.7**

PRIMERGY RX100 S6, Intel Core i3-540, 3.06 GHz

**SPECint\_base2006 = 26.3**

CPU2006 license: 19

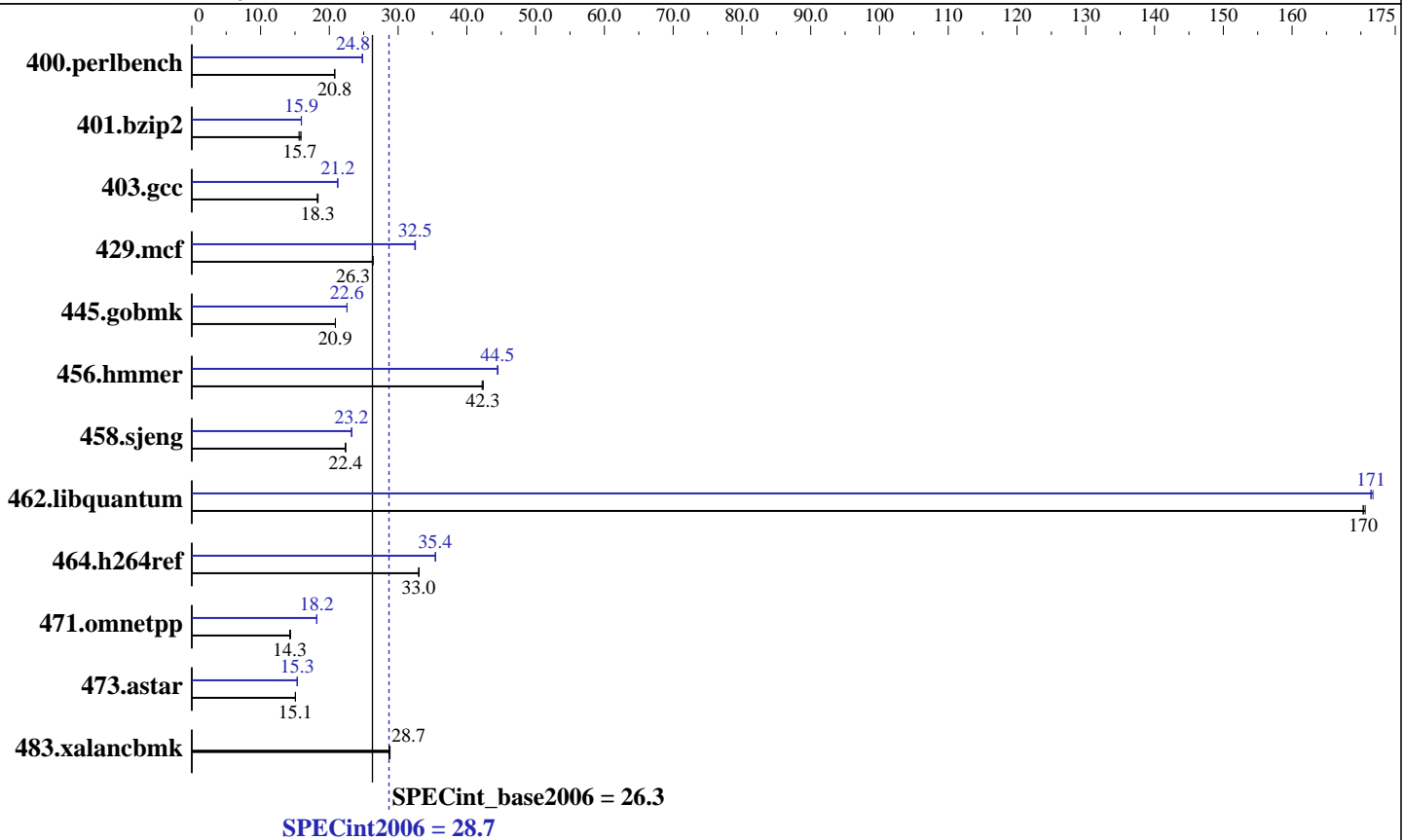
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Dec-2009

Hardware Availability: Jan-2010

Software Availability: Nov-2009



### Hardware

CPU Name: Intel Core i3-540  
 CPU Characteristics: 3067  
 CPU MHz: 3067  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 4 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 8 GB (2x4 GB PC3-10600E, 2 rank, CL9-9-9, ECC)  
 Disk Subsystem: 1 x SATA, 250 GB, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-smp  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091012 Package ID: 1\_cproc\_p\_11.1.059  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	471	20.8	470	20.8	<b>470</b>	<b>20.8</b>	<b>393</b>	<b>24.8</b>	393	24.9	394	24.8
401.bzip2	607	15.9	619	15.6	<b>614</b>	<b>15.7</b>	<b>606</b>	<b>15.9</b>	605	15.9	606	15.9
403.gcc	438	18.4	<b>440</b>	<b>18.3</b>	441	18.2	380	21.2	<b>379</b>	<b>21.2</b>	379	21.2
429.mcf	346	26.4	<b>347</b>	<b>26.3</b>	347	26.3	281	32.4	280	32.5	<b>281</b>	<b>32.5</b>
445.gobmk	502	20.9	<b>502</b>	<b>20.9</b>	502	20.9	465	22.6	<b>465</b>	<b>22.6</b>	465	22.6
456.hammer	221	42.2	<b>220</b>	<b>42.3</b>	220	42.4	210	44.5	<b>210</b>	<b>44.5</b>	210	44.5
458.sjeng	542	22.3	<b>541</b>	<b>22.4</b>	541	22.4	521	23.2	<b>521</b>	<b>23.2</b>	520	23.3
462.libquantum	122	170	<b>122</b>	<b>170</b>	121	171	<b>121</b>	<b>171</b>	121	171	121	172
464.h264ref	<b>671</b>	<b>33.0</b>	671	33.0	670	33.0	624	35.5	<b>625</b>	<b>35.4</b>	625	35.4
471.omnetpp	435	14.4	438	14.3	<b>438</b>	<b>14.3</b>	<b>344</b>	<b>18.2</b>	344	18.2	344	18.2
473.astar	466	15.1	466	15.1	<b>466</b>	<b>15.1</b>	<b>458</b>	<b>15.3</b>	458	15.3	458	15.3
483.xalancbmk	240	28.7	239	28.8	<b>240</b>	<b>28.7</b>	240	28.7	239	28.8	<b>240</b>	<b>28.7</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## General Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to granularity=fine,scatter  
For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc -m64  
  
C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

```

445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

## Base Optimization Flags

C benchmarks:

```

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch

```

C++ benchmarks:

```

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

```

## Base Other Flags

C benchmarks:

```

403.gcc: -Dalloca=_alloca

```

## Peak Compiler Invocation

C benchmarks (except as noted below):

```

icc -m64

```

```

400.perlbench: icc -m32

```

```

429.mcf: icc -m32

```

```

445.gobmk: icc -m32

```

```

464.h264ref: icc -m32

```

C++ benchmarks (except as noted below):

```

icpc -m64

```

```

471.omnetpp: icpc -m32

```



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## Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

## Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div -static(pass 2) -prof-use(pass 2)
           -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc
         -opt-malloc-options=3 -auto-ilp32

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
          -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
          -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
           -prof-use(pass 2) -unroll4

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
               -par-runtime-control -opt-prefetch
               -par-schedule-static=32768 -ansi-alias

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
             -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
             -prof-use(pass 2) -unroll2 -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
             -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
             -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs

```

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## Peak Optimization Flags (Continued)

471.omnetpp (continued):  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.20100202.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.20100202.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Wed Jul 23 05:51:09 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 2 February 2010.