



# SPEC<sup>®</sup> CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

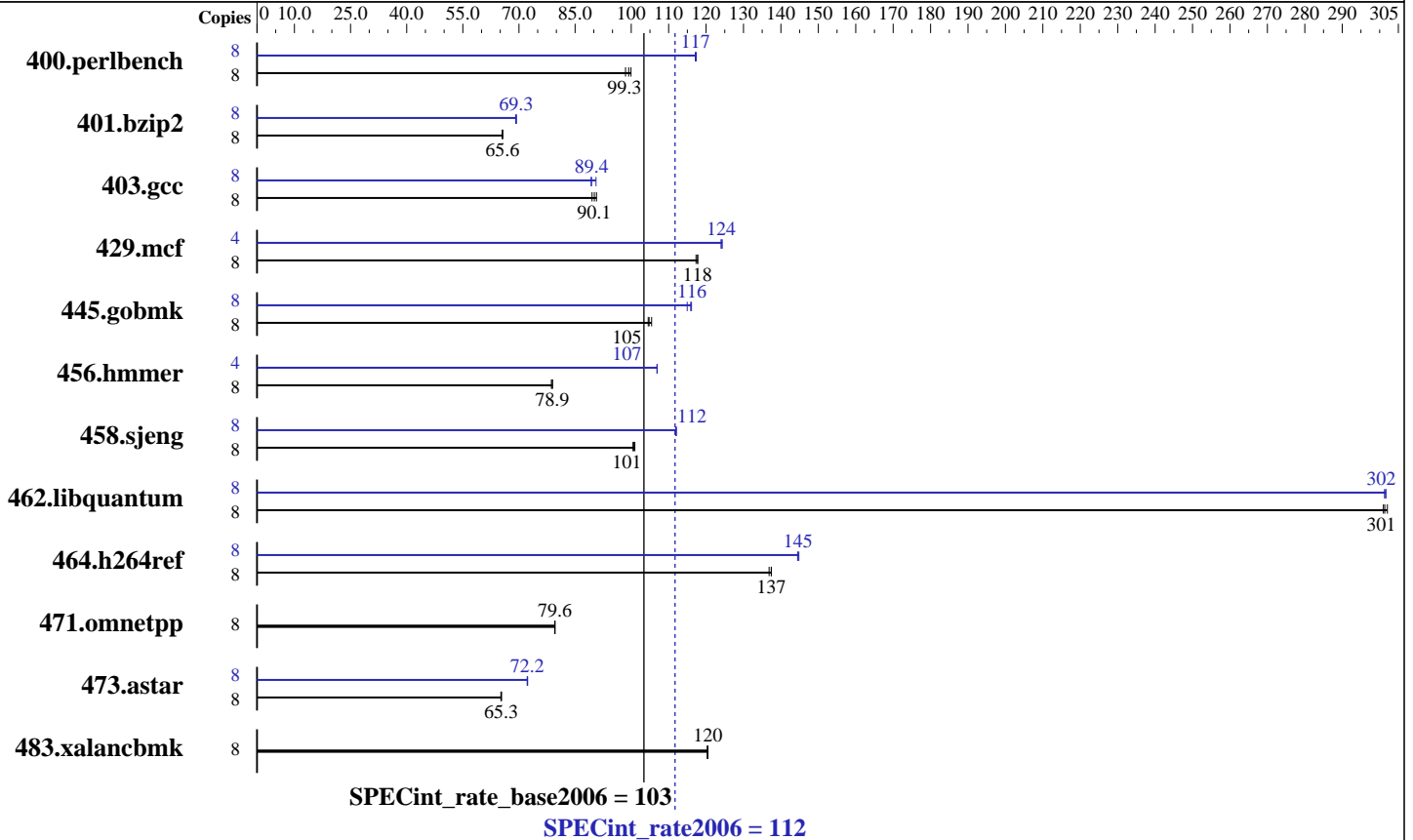
SPECint<sup>®</sup>\_rate2006 = 112

Servidor Itaotec LX113 (Intel Xeon X3450)

SPECint\_rate\_base2006 = 103

CPU2006 license: 9001  
Test sponsor: Itaotec  
Tested by: Itaotec

Test date: Dec-2009  
Hardware Availability: Dec-2009  
Software Availability: Feb-2009



### Hardware

CPU Name: Intel Xeon X3450  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2667  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 8 GB (8 x 1GB DDR3-1333, CL 9, ECC)  
 Disk Subsystem: 1 x 160 GB SATA-2, 7200RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 5.3, Kernel 2.6.18-128.el5  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l\_cproc\_p\_11.0.081  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint\_rate2006 = 112

Servidor Itautec LX113 (Intel Xeon X3450)

SPECint\_rate\_base2006 = 103

CPU2006 license: 9001  
Test sponsor: Itautec  
Tested by: Itautec

Test date: Dec-2009  
Hardware Availability: Dec-2009  
Software Availability: Feb-2009

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	<b>787</b>	<b>99.3</b>	794	98.5	782	99.9	8	667	117	<b>667</b>	<b>117</b>	666	117
401.bzip2	8	1176	65.7	<b>1177</b>	<b>65.6</b>	1178	65.5	8	1117	69.1	1114	69.3	<b>1115</b>	<b>69.3</b>
403.gcc	8	<b>714</b>	<b>90.1</b>	710	90.7	719	89.5	8	<b>720</b>	<b>89.4</b>	711	90.6	721	89.3
429.mcf	8	<b>620</b>	<b>118</b>	620	118	622	117	4	293	124	294	124	<b>294</b>	<b>124</b>
445.gobmk	8	796	105	<b>801</b>	<b>105</b>	803	105	8	723	116	730	115	<b>724</b>	<b>116</b>
456.hammer	8	949	78.7	945	79.0	<b>946</b>	<b>78.9</b>	4	349	107	349	107	<b>349</b>	<b>107</b>
458.sjeng	8	959	101	<b>962</b>	<b>101</b>	963	100	8	865	112	864	112	<b>865</b>	<b>112</b>
462.libquantum	8	549	302	<b>550</b>	<b>301</b>	551	301	8	<b>550</b>	<b>302</b>	549	302	550	301
464.h264ref	8	1294	137	<b>1288</b>	<b>137</b>	1288	137	8	1225	144	<b>1225</b>	<b>145</b>	1223	145
471.omnetpp	8	628	79.6	628	79.6	<b>628</b>	<b>79.6</b>	8	628	79.6	628	79.6	<b>628</b>	<b>79.6</b>
473.astar	8	862	65.2	<b>860</b>	<b>65.3</b>	860	65.3	8	778	72.2	776	72.3	<b>778</b>	<b>72.2</b>
483.xalancbmk	8	458	120	<b>458</b>	<b>120</b>	459	120	8	458	120	<b>458</b>	<b>120</b>	459	120

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.

## General Notes

This result was measured on the Servidor Itautec LX103.  
The Servidor Itautec LX113, the Servidor Itautec LX114 and the Servidor Itautec LX103 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint\_rate2006 = 112

Servidor Itautec LX113 (Intel Xeon X3450)

SPECint\_rate\_base2006 = 103

CPU2006 license: 9001

Test date: Dec-2009

Test sponsor: Itautec

Hardware Availability: Dec-2009

Tested by: Itautec

Software Availability: Feb-2009

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/opt/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc

401.bzip2: /opt/intel/Compiler/11.0/081/bin/intel64/icc  
456.hmmer: /opt/intel/Compiler/11.0/081/bin/intel64/icc  
458.sjeng: /opt/intel/Compiler/11.0/081/bin/intel64/icc

C++ benchmarks (except as noted below):  
icpc

473.astar: /opt/intel/Compiler/11.0/081/bin/intel64/icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint\_rate2006 = 112

Servidor Itautec LX113 (Intel Xeon X3450)

SPECint\_rate\_base2006 = 103

CPU2006 license: 9001  
Test sponsor: Itautec  
Tested by: Itautec

Test date: Dec-2009  
Hardware Availability: Dec-2009  
Software Availability: Feb-2009

## Peak Portability Flags (Continued)

458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch  
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32  
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3  
429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch  
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias  
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32  
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32  
462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static  
-opt-malloc-options=3 -opt-prefetch  
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes  
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
-Wl,-z,muldefs -L/opt/sh/SmartHeap\_8.1/lib -lsmartheap64

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint\_rate2006 = 112

Servidor Itautec LX113 (Intel Xeon X3450)

SPECint\_rate\_base2006 = 103

CPU2006 license: 9001

Test date: Dec-2009

Test sponsor: Itautec

Hardware Availability: Dec-2009

Tested by: Itautec

Software Availability: Feb-2009

## Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.0-int-linux64-revA.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.0-int-linux64-revA.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Wed Jul 23 06:24:12 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 19 January 2010.