SPEC® CINT2006 Result
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Fujitsu

PRIMERGY TX150 S7, Intel Xeon X3460, 2.80 GHz

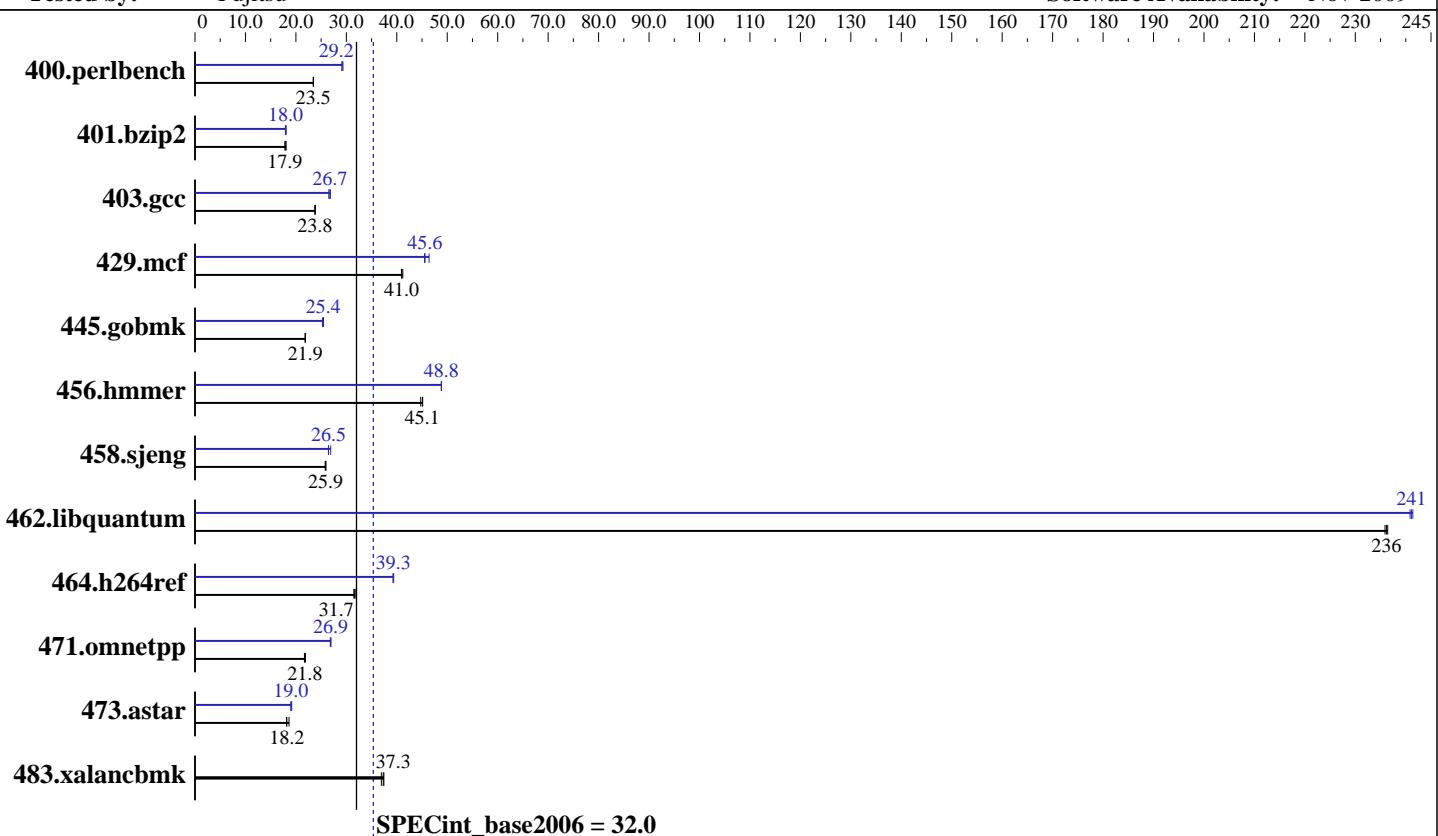$SPECint^{®}2006 =$ 35.3

$SPECint\_base2006 =$ 32.0

CPU2006 license: 19	**Test date:** Dec-2009
Test sponsor: Fujitsu	**Hardware Availability:** Jan-2010
Tested by: Fujitsu	**Software Availability:** Nov-2009

Graph axis: 0 10.0 20.0 30.0 40.0 50.0 60.0 70.0 80.0 90.0 100 110 120 130 140 150 160 170 180 190 200 210 220 230 245

- 400.perlbench: 29.2 / 23.5
- 401.bzip2: 18.0 / 17.9
- 403.gcc: 26.7 / 23.8
- 429.mcf: 45.6 / 41.0
- 445.gobmk: 25.4 / 21.9
- 456.hmmer: 48.8 / 45.1
- 458.sjeng: 26.5 / 25.9
- 462.libquantum: 241 / 236
- 464.h264ref: 39.3 / 31.7
- 471.omnetpp: 26.9 / 21.8
- 473.astar: 19.0 / 18.2
- 483.xalancbmk: 37.3

SPECint_base2006 = 32.0

SPECint2006 = 35.3

Hardware

CPU Name:	Intel Xeon X3460
CPU Characteristics:	Intel Turbo Boost Technology up to 3.46 GHz
CPU MHz:	2800
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	16 GB (4x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC)
Disk Subsystem:	1 x SATA, 250 GB, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-smp
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091012 Package ID: l_cproc_p_11.1.059
Auto Parallel:	Yes
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**416**	**23.5**	417	23.4	416	23.5	335	29.1	333	29.3	**335**	**29.2**
401.bzip2	**540**	**17.9**	534	18.1	540	17.9	539	17.9	534	18.1	**537**	**18.0**
403.gcc	338	23.8	**338**	**23.8**	338	23.8	304	26.5	**301**	**26.7**	300	26.8
429.mcf	**223**	**41.0**	221	41.2	223	40.9	200	45.5	197	46.4	**200**	**45.6**
445.gobmk	480	21.9	480	21.9	**480**	**21.9**	412	25.5	415	25.3	**412**	**25.4**
456.hmmer	209	44.7	**207**	**45.1**	207	45.1	191	48.8	**191**	**48.8**	191	48.8
458.sjeng	**467**	**25.9**	467	25.9	469	25.8	**457**	**26.5**	457	26.4	450	26.9
462.libquantum	**87.7**	**236**	87.8	236	87.6	236	85.8	241	**85.9**	**241**	86.0	241
464.h264ref	698	31.7	**699**	**31.7**	703	31.5	**563**	**39.3**	562	39.4	564	39.3
471.omnetpp	288	21.7	286	21.9	**287**	**21.8**	**232**	**26.9**	232	26.9	233	26.8
473.astar	**386**	**18.2**	377	18.6	387	18.1	**369**	**19.0**	369	19.0	366	19.2
483.xalancbmk	187	36.9	**185**	**37.3**	184	37.4	187	36.9	**185**	**37.3**	184	37.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
      icc -m64

C++ benchmarks:
      icpc -m64

Base Portability Flags

      400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
         401.bzip2: -DSPEC_CPU_LP64
           403.gcc: -DSPEC_CPU_LP64
           429.mcf: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```
      445.gobmk: -DSPEC_CPU_LP64
      456.hmmer: -DSPEC_CPU_LP64
      458.sjeng: -DSPEC_CPU_LP64
  462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
     464.h264ref: -DSPEC_CPU_LP64
     471.omnetpp: -DSPEC_CPU_LP64
       473.astar: -DSPEC_CPU_LP64
  483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
   -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
   -par-runtime-control -opt-prefetch
```

C++ benchmarks:

```
   -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
   -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64
```

Base Other Flags

C benchmarks:

```
       403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
   icc  -m64
```

```
   400.perlbench: icc -m32
```

```
        429.mcf: icc -m32
```

```
     445.gobmk: icc -m32
```

```
    464.h264ref: icc -m32
```

C++ benchmarks (except as noted below):
```
   icpc -m64
```

```
    471.omnetpp: icpc -m32
```

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Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_X64
   401.bzip2: -DSPEC_CPU_LP64
     403.gcc: -DSPEC_CPU_LP64
   456.hmmer: -DSPEC_CPU_LP64
   458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
   473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -ansi-alias -opt-prefetch

   401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div -static(pass 2) -prof-use(pass 2)
              -auto-ilp32 -opt-prefetch -ansi-alias

     403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
              -opt-malloc-options=3 -auto-ilp32

     429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

   445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
              -ipo -no-prec-div -ansi-alias

   456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
              -ansi-alias -auto-ilp32

   458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
              -prof-use(pass 2) -unroll4

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
                -par-runtime-control -opt-prefetch
                -par-schedule-static=32768 -ansi-alias

  464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
  471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
```

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Peak Optimization Flags (Continued)

471.omnetpp (continued):
        -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
        -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
        -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
        -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

   403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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