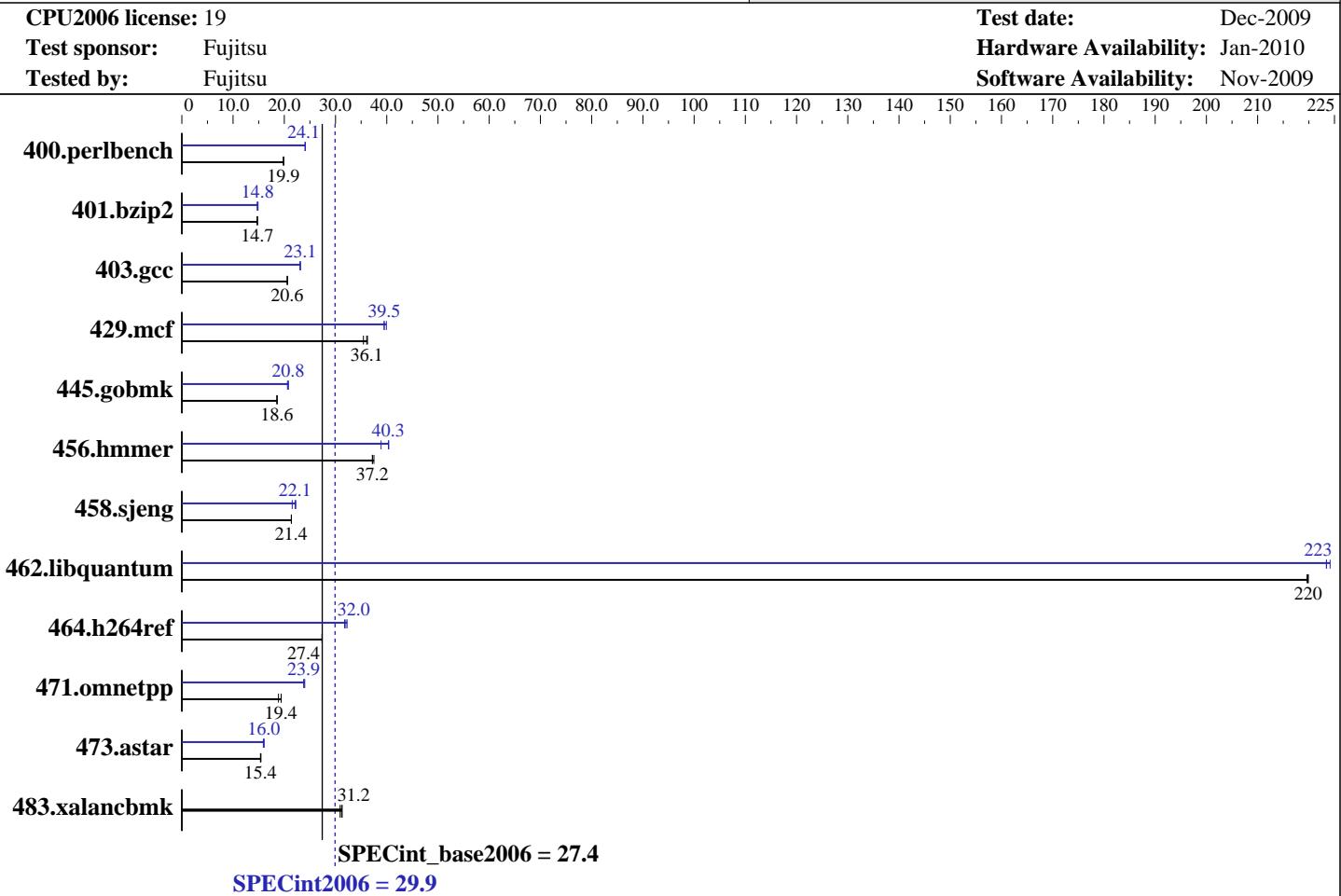# SPEC® CINT2006 Result
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Fujitsu

PRIMERGY TX150 S7, Intel Xeon X3430, 2.40 GHz

$SPECint^®2006 =$ 29.9

$SPECint\_base2006 =$ 27.4

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Dec-2009
Hardware Availability: Jan-2010
Software Availability: Nov-2009

Benchmark	Result
400.perlbench	24.1 / 19.9
401.bzip2	14.8 / 14.7
403.gcc	23.1 / 20.6
429.mcf	39.5 / 36.1
445.gobmk	20.8 / 18.6
456.hmmer	40.3 / 37.2
458.sjeng	22.1 / 21.4
462.libquantum	223 / 220
464.h264ref	32.0 / 27.4
471.omnetpp	23.9 / 19.4
473.astar	16.0 / 15.4
483.xalancbmk	31.2

SPECint_base2006 = 27.4
SPECint2006 = 29.9

Hardware

CPU Name: Intel Xeon X3430
CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz: 2400
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (4x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC)
Disk Subsystem: 1 x SATA, 250 GB, 7200 RPM
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-smp
Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1
Build 20091012 Package ID: l_cproc_p_11.1.059
Auto Parallel: Yes
File System: ext3
System State: Multi-User Run Level 3
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V8.1
Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	492	19.9	492	19.8	**492**	**19.9**	**406**	**24.1**	406	24.1	405	24.1
401.bzip2	655	14.7	655	14.7	**655**	**14.7**	649	14.9	656	14.7	**652**	**14.8**
403.gcc	391	20.6	391	20.6	**391**	**20.6**	349	23.1	**348**	**23.1**	348	23.1
429.mcf	**253**	**36.1**	257	35.4	252	36.2	228	39.9	**231**	**39.5**	231	39.5
445.gobmk	**564**	**18.6**	565	18.6	564	18.6	505	20.8	**505**	**20.8**	509	20.6
456.hmmer	249	37.5	**251**	**37.2**	251	37.2	231	40.4	**231**	**40.3**	240	38.9
458.sjeng	566	21.4	566	21.4	**566**	**21.4**	544	22.2	562	21.5	**547**	**22.1**
462.libquantum	**94.2**	**220**	94.2	220	94.3	220	92.4	224	92.7	223	**92.7**	**223**
464.h264ref	**808**	**27.4**	808	27.4	808	27.4	**691**	**32.0**	697	31.8	686	32.3
471.omnetpp	322	19.4	331	18.9	**322**	**19.4**	263	23.8	260	24.0	**262**	**23.9**
473.astar	454	15.4	457	15.4	**457**	**15.4**	437	16.1	**439**	**16.0**	441	15.9
483.xalancbmk	220	31.3	223	30.9	**221**	**31.2**	220	31.3	223	30.9	**221**	**31.2**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

```
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
For information about Fujitsu please visit: http://www.fujitsu.com
```

Base Compiler Invocation

C benchmarks:
```
icc -m64
```

C++ benchmarks:
```
icpc -m64
```

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
   401.bzip2: -DSPEC_CPU_LP64
     403.gcc: -DSPEC_CPU_LP64
     429.mcf: -DSPEC_CPU_LP64
```

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Base Portability Flags (Continued)

```
     445.gobmk: -DSPEC_CPU_LP64
     456.hmmer: -DSPEC_CPU_LP64
     458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
    464.h264ref: -DSPEC_CPU_LP64
    471.omnetpp: -DSPEC_CPU_LP64
      473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64
```

Base Other Flags

C benchmarks:

```
     403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m64
```

```
400.perlbench: icc -m32
```

```
     429.mcf: icc -m32
```

```
   445.gobmk: icc -m32
```

```
  464.h264ref: icc -m32
```

C++ benchmarks (except as noted below):
```
icpc -m64
```

```
   471.omnetpp: icpc -m32
```

SPEC CINT2006 Result

Fujitsu

PRIMERGY TX150 S7, Intel Xeon X3430, 2.40 GHz

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$SPECint_base2006 = $ 27.4

CPU2006 license: 19		**Test date:**	Dec-2009
Test sponsor:	Fujitsu	**Hardware Availability:**	Jan-2010
Tested by:	Fujitsu	**Software Availability:**	Nov-2009

Peak Portability Flags

```
   400.perlbench: -DSPEC_CPU_LINUX_X64
       401.bzip2: -DSPEC_CPU_LP64
         403.gcc: -DSPEC_CPU_LP64
      456.hmmer: -DSPEC_CPU_LP64
      458.sjeng: -DSPEC_CPU_LP64
   462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
       473.astar: -DSPEC_CPU_LP64
   483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
   400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -ansi-alias -opt-prefetch

       401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div -static(pass 2) -prof-use(pass 2)
                  -auto-ilp32 -opt-prefetch -ansi-alias

         403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
                  -opt-malloc-options=3 -auto-ilp32

         429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

       445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
                  -ipo -no-prec-div -ansi-alias

       456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
                  -ansi-alias -auto-ilp32

       458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -unroll4

   462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
                  -par-runtime-control -opt-prefetch
                  -par-schedule-static=32768 -ansi-alias

      464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
      471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                  -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
```

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Peak Optimization Flags (Continued)

471.omnetpp (continued):
    -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
    -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
    -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
    -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
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