



# SPEC® CINT2006 Result

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## Fujitsu

### SPECint®\_rate2006 = 127

PRIMERGY TX150 S7, Intel Xeon X3470, 2.93 GHz

### SPECint\_rate\_base2006 = 120

CPU2006 license: 19

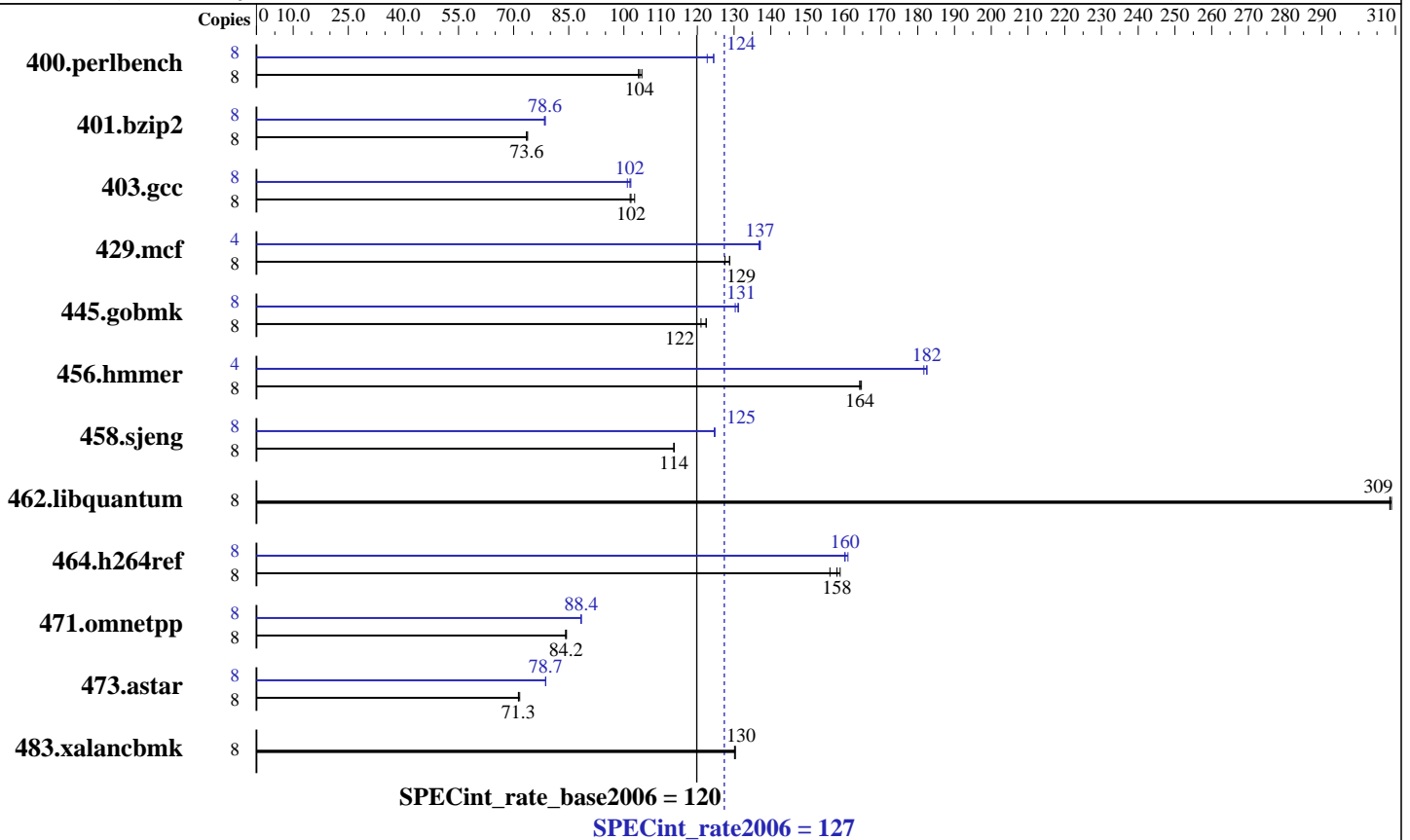
Test date: Nov-2009

Test sponsor: Fujitsu

Hardware Availability: Jan-2010

Tested by: Fujitsu

Software Availability: Nov-2009



### Hardware

CPU Name: Intel Xeon X3470  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 2933  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 16 GB (4x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC)  
 Disk Subsystem: 1 x SATA, 250 GB, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-smp  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091012 Package ID: 1\_cproc\_p\_11.1.059  
 Auto Parallel: No  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	<b>749</b>	<b>104</b>	745	105	752	104	8	<b>628</b>	<b>124</b>	628	124	637	123
401.bzip2	8	1046	73.8	<b>1049</b>	<b>73.6</b>	1051	73.5	8	985	78.4	982	78.6	<b>983</b>	<b>78.6</b>
403.gcc	8	626	103	633	102	<b>632</b>	<b>102</b>	8	632	102	<b>633</b>	<b>102</b>	638	101
429.mcf	8	572	128	566	129	<b>567</b>	<b>129</b>	4	266	137	267	137	<b>266</b>	<b>137</b>
445.gobmk	8	685	122	<b>686</b>	<b>122</b>	694	121	8	<b>640</b>	<b>131</b>	644	130	640	131
456.hammer	8	<b>454</b>	<b>164</b>	455	164	453	165	4	205	182	<b>205</b>	<b>182</b>	204	183
458.sjeng	8	<b>852</b>	<b>114</b>	852	114	851	114	8	777	125	<b>776</b>	<b>125</b>	776	125
462.libquantum	8	537	309	<b>537</b>	<b>309</b>	537	308	8	537	309	<b>537</b>	<b>309</b>	537	308
464.h264ref	8	1114	159	<b>1120</b>	<b>158</b>	1134	156	8	1105	160	1100	161	<b>1105</b>	<b>160</b>
471.omnetpp	8	594	84.1	592	84.4	<b>594</b>	<b>84.2</b>	8	<b>566</b>	<b>88.4</b>	566	88.4	566	88.4
473.astar	8	788	71.3	784	71.7	<b>787</b>	<b>71.3</b>	8	713	78.7	713	78.7	<b>713</b>	<b>78.7</b>
483.xalancbmk	8	423	130	424	130	<b>424</b>	<b>130</b>	8	423	130	424	130	<b>424</b>	<b>130</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc -m32  
  
C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

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## Base Portability Flags (Continued)

462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Peak Portability Flags (Continued)

473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

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## Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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