



# SPEC® CINT2006 Result

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## Acer Incorporated

### SPECint®\_rate2006 = 47.1

## Gateway GT110 F1 (Intel Xeon E3110)

### SPECint\_rate\_base2006 = 44.3

CPU2006 license: 97

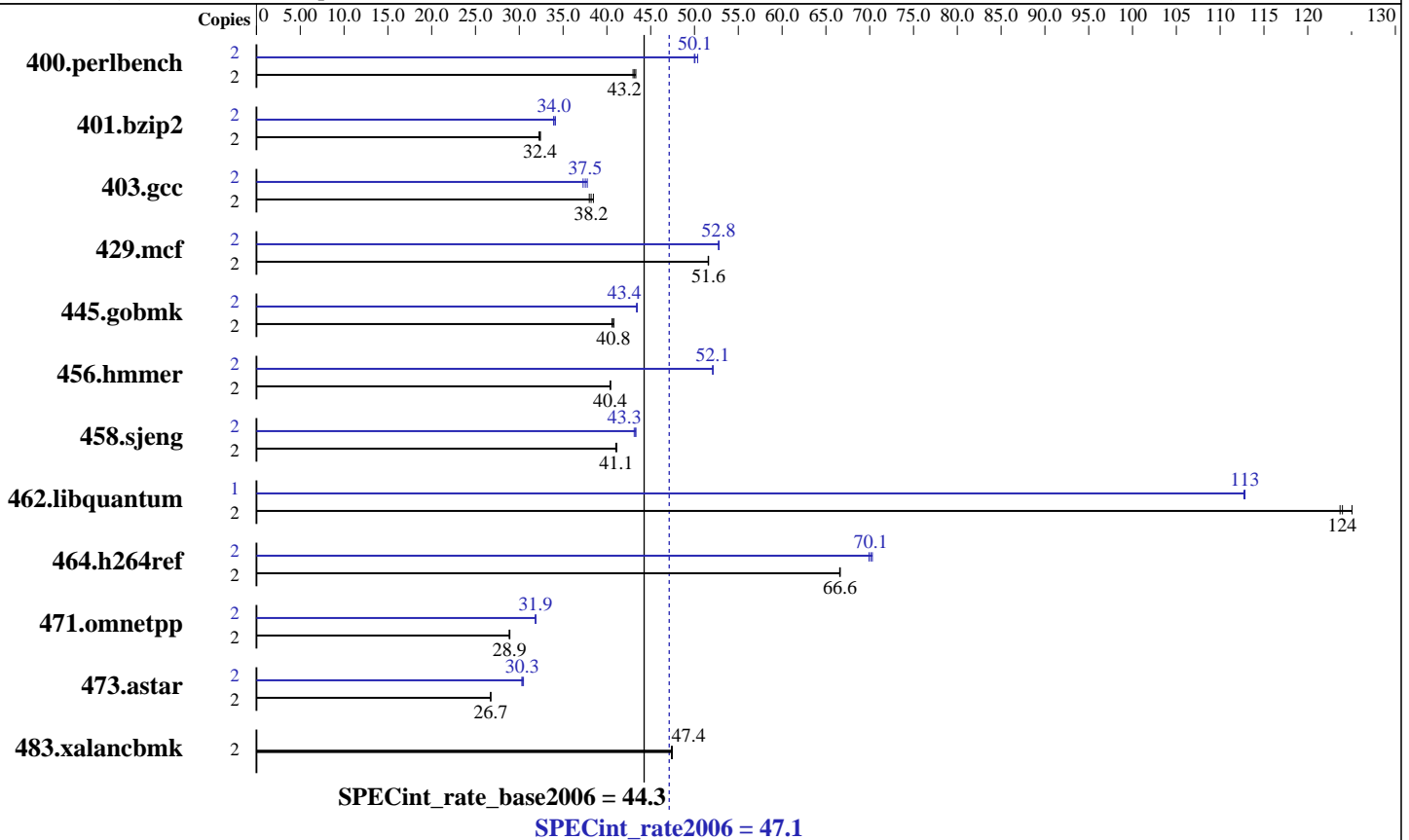
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Nov-2009

Hardware Availability: Dec-2009

Software Availability: Nov-2008



### Hardware

CPU Name: Intel Xeon E3110  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 6 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (4 x 2048 MB ECC Unbuffered DDR2-800 CL5)  
 Disk Subsystem: 1 x 640 GB 7200RPM SATA HDD  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64)  
 Kernel 2.6.27.19-5  
 Compiler: Intel C++ Compiler 11.0 for Linux  
 Build 20080930 Package ID: l\_cproc\_p\_11.0.066  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Multi-user, level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1  
 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	451	43.3	<b>453</b>	<b>43.2</b>	455	43.0	2	<b>390</b>	<b>50.1</b>	391	50.0	388	50.4
401.bzip2	2	<b>596</b>	<b>32.4</b>	596	32.4	598	32.3	2	<b>568</b>	<b>34.0</b>	569	33.9	566	34.1
403.gcc	2	424	38.0	<b>421</b>	<b>38.2</b>	419	38.5	2	<b>429</b>	<b>37.5</b>	426	37.8	432	37.3
429.mcf	2	354	51.6	353	51.6	<b>354</b>	<b>51.6</b>	2	<b>346</b>	<b>52.8</b>	345	52.8	346	52.7
445.gobmk	2	514	40.8	517	40.6	<b>515</b>	<b>40.8</b>	2	483	43.5	484	43.4	<b>483</b>	<b>43.4</b>
456.hammer	2	462	40.4	<b>462</b>	<b>40.4</b>	462	40.4	2	358	52.1	<b>358</b>	<b>52.1</b>	358	52.1
458.sjeng	2	589	41.1	590	41.0	<b>589</b>	<b>41.1</b>	2	561	43.1	559	43.3	<b>559</b>	<b>43.3</b>
462.libquantum	2	<b>334</b>	<b>124</b>	331	125	335	124	1	184	113	184	113	<b>184</b>	<b>113</b>
464.h264ref	2	<b>665</b>	<b>66.6</b>	664	66.6	665	66.6	2	630	70.3	<b>631</b>	<b>70.1</b>	633	69.9
471.omnetpp	2	<b>433</b>	<b>28.9</b>	432	28.9	433	28.8	2	393	31.8	392	31.9	<b>392</b>	<b>31.9</b>
473.astar	2	<b>525</b>	<b>26.7</b>	525	26.7	525	26.7	2	<b>463</b>	<b>30.3</b>	461	30.5	463	30.3
483.xalancbmk	2	291	47.4	291	47.5	<b>291</b>	<b>47.4</b>	2	291	47.4	291	47.5	<b>291</b>	<b>47.4</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

'ulimit -s unlimited' was set for stacksize unlimited

## General Notes

taskset was used to bind processes to cores except for 462.libquantum peak  
OMP\_NUM\_THREADS set to number of processors  
KMP\_AFFINITY set to "physical,0"  
KMP\_STACKSIZE set to 64M

This result was measured on the Gateway GT110 F1.  
The Acer AT110 F1 and Gateway GT110 F1 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll4

462.libquantum: -xSSE4.1 -ipo -O3 -no-prec-div -static  
-opt-malloc-options=3 -parallel -par-runtime-control  
-opt-prefetch

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.20100119.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.20100119.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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