



SPEC® CINT2006 Result

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Sun Microsystems

SPECint®_rate2006 = 213

Sun Fire X4270 (Intel Xeon E5540 2.53GHz)

SPECint_rate_base2006 = 199

CPU2006 license: 6

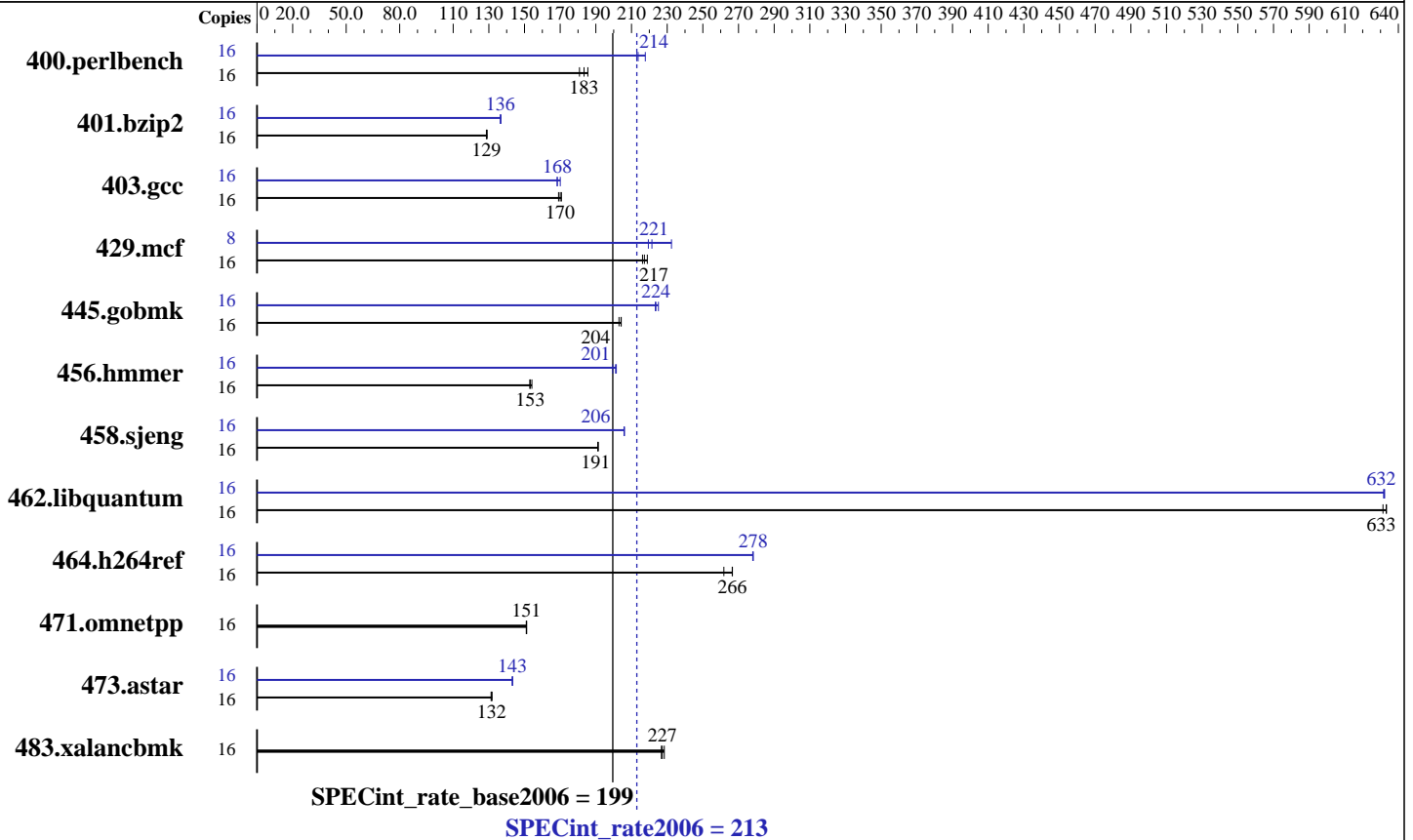
Test date: Aug-2009

Test sponsor: Sun Microsystems

Hardware Availability: Apr-2009

Tested by: Sun Microsystems

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon E5540
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2534
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 or 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (6x4 GB DDR3-1333 downclocked to 1066 MHz)
 Disk Subsystem: 1 x 134 GB, SAS, 10 K RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	864	181	<u>852</u>	<u>183</u>	843	186	16	734	213	718	218	<u>731</u>	<u>214</u>
401.bzip2	16	1200	129	1196	129	<u>1200</u>	<u>129</u>	16	1129	137	<u>1131</u>	<u>136</u>	1131	136
403.gcc	16	<u>758</u>	<u>170</u>	754	171	762	169	16	<u>765</u>	<u>168</u>	757	170	765	168
429.mcf	16	667	219	675	216	<u>672</u>	<u>217</u>	8	314	232	<u>329</u>	<u>221</u>	332	219
445.gobmk	16	822	204	827	203	<u>822</u>	<u>204</u>	16	746	225	751	223	<u>750</u>	<u>224</u>
456.hammer	16	969	154	<u>974</u>	<u>153</u>	976	153	16	<u>743</u>	<u>201</u>	741	201	748	199
458.sjeng	16	1013	191	1012	191	<u>1012</u>	<u>191</u>	16	939	206	941	206	<u>941</u>	<u>206</u>
462.libquantum	16	525	631	523	633	<u>524</u>	<u>633</u>	16	524	632	<u>525</u>	<u>632</u>	525	632
464.h264ref	16	<u>1329</u>	<u>266</u>	1328	267	1353	262	16	1273	278	1274	278	<u>1273</u>	<u>278</u>
471.omnetpp	16	661	151	662	151	<u>662</u>	<u>151</u>	16	661	151	662	151	<u>662</u>	<u>151</u>
473.astar	16	856	131	<u>854</u>	<u>132</u>	852	132	16	785	143	<u>784</u>	<u>143</u>	783	143
483.xalancbmk	16	483	228	<u>486</u>	<u>227</u>	487	227	16	483	228	<u>486</u>	<u>227</u>	487	227

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

Default BIOS settings used.

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

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Base Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static
-opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090901.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090901.00.xml>

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For other inquiries, please contact webmaster@spec.org.

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