SPEC® CINT2006 Result
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SGI

SGI Altix 4700 Bandwidth System (Dual-Core
Intel Itanium 2 9040 1.60GHz/18M)

$SPECint®\_rate2006 =$ Not Run

SPECint_rate_base2006 = 9030

CPU2006 license: 4	**Test date:** Jul-2009
Test sponsor: SGI	**Hardware Availability:** Nov-2007
Tested by: SGI	**Software Availability:** Jul-2009

Copies

Benchmark	Copies	Value
400.perlbench	1020	4600
401.bzip2	1020	7470
403.gcc	1020	2460
429.mcf	1020	9850
445.gobmk	1020	8900
456.hmmer	1020	18400
458.sjeng	1020	9430
462.libquantum	1020	32500
464.h264ref	1020	19400
471.omnetpp	1020	4980
473.astar	1020	11200
483.xalancbmk	1020	6570

SPECint_rate_base2006 = 9030

Hardware
CPU Name: Dual-Core Intel Itanium 2 9040
CPU Characteristics: 533MHz FSB
CPU MHz: 1594
FPU: Integrated
CPU(s) enabled: 1024 cores, 512 chips, 2 cores/chip
CPU(s) orderable: 8 to 512 blades with 1 chip per blade
Primary Cache: 16 KB I + 16 KB D on chip per core
Secondary Cache: 1 MB I + 256 KB D on chip per core
L3 Cache: 9 MB I+D on chip per core
Other Cache: None
Memory: 4 TB (8*1GB DDR2-400 DIMMS per blade)
Disk Subsystem: 279 TB RAID5
1024 x 300 GB FC (Hitachi Ultrastar 10k rpm)
Other Hardware: None

Software
Operating System: SUSE Linux Enterprise Server 10 (ia64) SP2,
Kernel 2.6.16.60-0.35.PTF.352893.0-default
Compiler: Intel C++ Compiler for Linux 11.1
(Build 20090630)
Auto Parallel: No
File System: xfs
System State: Multi-user, run level 3
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other Software: MicroQuill SmartHeap Library 8
(www.microquill.com)
SGI ProPack 6 Service Pack 2

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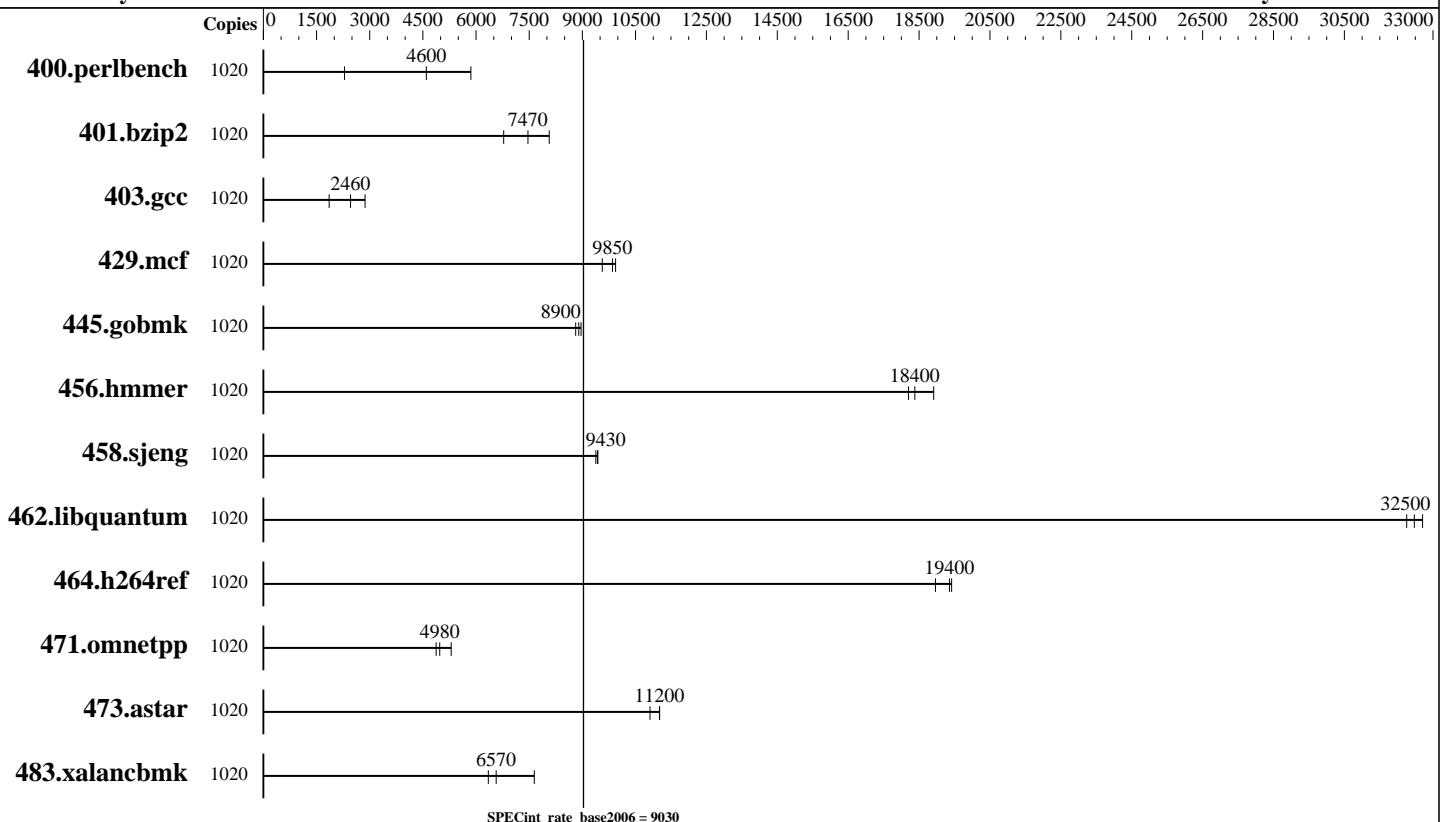SPECint_rate_base2006 = 9030

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	1020	1702	5850	**2168**	**4600**	4355	2290							
401.bzip2	1020	1452	6780	1220	8070	**1319**	**7470**							
403.gcc	1020	4426	1860	2862	2870	**3340**	**2460**							
429.mcf	1020	**944**	**9850**	936	9940	973	9560							
445.gobmk	1020	**1202**	**8900**	1194	8960	1214	8810							
456.hmmer	1020	523	18200	**518**	**18400**	503	18900							
458.sjeng	1020	1308	9440	1316	9380	**1309**	**9430**							
462.libquantum	1020	646	32700	**651**	**32500**	655	32300							
464.h264ref	1020	1162	19400	**1166**	**19400**	1190	19000							
471.omnetpp	1020	1308	4870	1203	5300	**1281**	**4980**							
473.astar	1020	640	11200	**641**	**11200**	656	10900							
483.xalancbmk	1020	921	7640	1109	6350	**1071**	**6570**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

Processes were bound to CPUs using dplace.
limit stacksize unlimited
System was configured with 4-cpu boot cpuset
echo 0 > /dev/cpuset/memory_spread_page
runspec was launched from root cpuset

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_IA64

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Base Portability Flags (Continued)

```
        401.bzip2: -DSPEC_CPU_LP64
          403.gcc: -DSPEC_CPU_LP64
          429.mcf: -DSPEC_CPU_LP64
       445.gobmk: -DSPEC_CPU_LP64
       456.hmmer: -DSPEC_CPU_LP64
        458.sjeng: -DSPEC_CPU_LP64
    462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
      464.h264ref: -DSPEC_CPU_LP64
      471.omnetpp: -DSPEC_CPU_LP64
        473.astar: -DSPEC_CPU_LP64
    483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
     -fast -fp-relaxed -no-opt-prefetch-initial-values -ansi-alias
```

C++ benchmarks:
```
     -fast -fp-relaxed -no-opt-prefetch-initial-values -ansi-alias
     -Wl,-z,muldefs libsmartheapC64.a libsmartheap64.a
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-ipf.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-ipf.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 03:41:32 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 31 August 2009.